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1.1 classnames 2.1.5

1.1.1 Available under license :

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1.2 d3 3.5.5

1.3 datepicker_ui 1.11.1 :2014/8/27

1.3.1 Available under license :

```
/*!
 * jQuery UI Datepicker 1.11.1
 * http://jqueryui.com
 *
 * Copyright 2014 jQuery Foundation and other contributors
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 * http://jquery.org/license
 *
 * http://api.jqueryui.com/datepicker/
 */
```

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1.4 es6-promise 3.0.2

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1.5 event-emitter 0.3.3

1.5.1 Available under license :

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1.6 FileSaver.js 1.1.0

1.6.1 Available under license :

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[1]: <http://eligrey.com>

1.7 httpclient 1.1

1.8 jquery 2.1.1

1.8.1 Available under license :

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1.9 jquery-ui 1.11.2 :2014/11/17

1.9.1 Available under license :

```
/*! jQuery UI - v1.11.2 - 2014-10-16
```

```
* http://jqueryui.com
```

```
* Includes: core.js, widget.js, mouse.js, position.js, accordion.js, autocomplete.js, button.js, datepicker.js, dialog.js, draggable.js, droppable.js, effect.js, effect-blind.js, effect-bounce.js, effect-clip.js, effect-drop.js, effect-explode.js, effect-fade.js, effect-fold.js, effect-highlight.js, effect-puff.js, effect-pulsate.js, effect-scale.js, effect-shake.js, effect-size.js, effect-slide.js, effect-transfer.js, menu.js, progressbar.js, resizable.js, selectable.js, selectmenu.js, slider.js, sortable.js, spinner.js, tabs.js, tooltip.js
```

```
* Copyright 2014 jQuery Foundation and other contributors; Licensed MIT */
```

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1.10 jquery.flot.Stack 1.2

1.10.1 Available under license :

/* Flot plugin for stacking data sets rather than overlying them.

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The plugin assumes the data is sorted on x (or y if stacking horizontally). For line charts, it is assumed that if a line has an undefined gap (from a null point), then the line above it should have the same gap - insert zeros instead of "null" if you want another behaviour. This also holds for the start and end of the chart. Note that stacking a mix of positive and negative values in most instances doesn't make sense (so it looks weird).

Two or more series are stacked when their "stack" attribute is set to the same key (which can be any number or string or just "true"). To specify the default stack, you can set the stack option like this:

```
series: {  
    stack: null/false, true, or a key (number/string)  
}
```

You can also specify it for a single series, like this:

```
$.plot($("#placeholder"), [{  
    data: [ ... ],  
    stack: true  
}])
```

The stacking order is determined by the order of the data series in the array (later series end up on top of the previous).

Internally, the plugin modifies the datapoints in each series, adding an offset to the y value. For line series, extra data points are inserted through interpolation. If there's a second y value, it's also adjusted (e.g for bar charts or filled areas).

*/

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1.11 jquery.flot.tooltip 0.6.2

1.11.1 Available under license :

```
/*  
 * jquery.flot.tooltip  
 *  
 * description: easy-to-use tooltips for Flot charts  
 * version: 0.6.2  
 * author: Krzysztof Urbas @krzysu [myviews.pl]  
 *  
 *  
 * build on 2013-09-30  
 * released under MIT License, 2012  
 */
```

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1.12 jquery.flot.valuelabels 2

1.12.1 Available under license :

```
/* Value Labels Plugin for flot.  
* Homepage:  
* http://sites.google.com/site/petrstuff/projects/flotvallab  
*  
* Released under the MIT license by Petr Blahos, December 2009.  
*  
*/
```

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1.13 jszip.js 2.4.0

1.13.1 Available under license :

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Version 3, 29 June 2007

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*/

1.14 linux 3.10.70 :2015_T1.1p10

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/*

* ahci_mv.c - Marvell AHCI SATA platform support

*

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*

* based on the AHCI SATA platform driver by Jeff Garzik and Anton Vorontsov

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```

```

#include <linux/kernel.h>
#include <linux/gfp.h>
#include <linux/module.h>
#include <linux/pm.h>
#include <linux/init.h>
#include <linux/interrupt.h>
#include <linux/device.h>
#include <linux/platform_device.h>
#include <linux/libata.h>
#include <linux/ahci_platform.h>
#include <linux/ata_platform.h>
#include <linux/mbus.h>
#include "ahci.h"

#define AHCI_WINDOW_CTRL(win) (0x60 + ((win) << 4))
#define AHCI_WINDOW_BASE(win) (0x64 + ((win) << 4))
#define AHCI_WINDOW_SIZE(win) (0x68 + ((win) << 4))

#define VENDOR_SPECIFIC_0_ADDR 0xa0
#define VENDOR_SPECIFIC_0_DATA 0xa4

static void ahci_mv_windows_config(struct ahci_host_priv *hpriv,
    const struct mbus_dram_target_info *dram)
{
    int i;

    for (i = 0; i < 4; i++) {
        writel(0, hpriv->mmio + AHCI_WINDOW_CTRL(i));
        writel(0, hpriv->mmio + AHCI_WINDOW_BASE(i));
        writel(0, hpriv->mmio + AHCI_WINDOW_SIZE(i));
    }

    for (i = 0; i < dram->num_cs; i++) {
        const struct mbus_dram_window *cs = dram->cs + i;

        writel((cs->mbus_attr << 8) |
            (dram->mbus_dram_target_id << 4) | 1,
            hpriv->mmio + AHCI_WINDOW_CTRL(i));
        writel(cs->base >> 16, hpriv->mmio + AHCI_WINDOW_BASE(i));
        writel(((cs->size - 1) & 0xffff0000),
            hpriv->mmio + AHCI_WINDOW_SIZE(i));
    }
}

```

```

}
}

static void ahci_mv_host_stop(struct ata_host *host);

static struct ata_port_operations ahci_mv_ops = {
    .inherits = &ahci_ops,
    .host_stop = ahci_mv_host_stop,
};

static const struct ata_port_info ahci_mv_port_info = {
    .flags = AHCI_FLAG_COMMON,
    .pio_mask = ATA_PIO4,
    .udma_mask = ATA_UDMA6,
    .port_ops = &ahci_mv_ops,
};

static struct scsi_host_template ahci_mv_platform_sht = {
    AHCI_SHT("ahci_mv_platform"),
};

static const struct of_device_id ahci_mv_of_match[] = {
    { .compatible = "marvell,ahci-sata", },
    {},
};
MODULE_DEVICE_TABLE(of, ahci_of_match);

static int ahci_mv_probe(struct platform_device *pdev)
{
    struct device *dev = &pdev->dev;
    const struct mbus_dram_target_info *dram;
    struct ata_port_info pi = ahci_mv_port_info;
    const struct ata_port_info *ppi[] = { &pi, NULL };
    struct ahci_host_priv *hpriv;
    struct ata_host *host;
    struct resource *mem;

    int irq;
    int n_ports;
    int i;
    int rc;

    mem = platform_get_resource(pdev, IORESOURCE_MEM, 0);
    if (!mem) {
        dev_err(dev, "no mmio space\n");
        return -EINVAL;
    }

    irq = platform_get_irq(pdev, 0);

```

```

if (irq <= 0) {
    dev_err(dev, "no irq\n");
    return -EINVAL;
}

hpriv = devm_kzalloc(dev, sizeof(*hpriv), GFP_KERNEL);
if (!hpriv) {
    dev_err(dev, "can't alloc ahci_host_priv\n");
    return -ENOMEM;
}

hpriv->clk = devm_clk_get(&pdev->dev, NULL);
if (IS_ERR(hpriv->clk)) {
    dev_err(dev, "can't get clock\n");
    return PTR_ERR(hpriv->clk);
}

rc = clk_prepare_enable(hpriv->clk);
if (rc < 0) {
    dev_err(dev, "can't enable clock\n");
    return rc;
}

hpriv->flags |= (unsigned long)pi.private_data;

hpriv->mmio = devm_request_and_ioremap(dev, mem);
if (!hpriv->mmio) {
    dev_err(dev, "can't map %pR\n", mem);
    clk_disable_unprepare(hpriv->clk);
    return -ENOMEM;
}

/*
 * (Re-)program MBUS remapping windows if we are asked to.
 */
dram = mv_mbus_dram_info();
if (dram)
    ahci_mv_windows_config(hpriv, dram);

ahci_save_initial_config(dev, hpriv, 0, 0);

/* prepare host */
if (hpriv->cap & HOST_CAP_NCQ)
    pi.flags |= ATA_FLAG_NCQ;

if (hpriv->cap & HOST_CAP_PMP)
    pi.flags |= ATA_FLAG_PMP;

```

```

ahci_set_em_messages(hpriv, &pi);

/*
 * CAP.NP sometimes indicate the index of the last enabled
 * port, at other times, that of the last possible port, so
 * determining the maximum port number requires looking at
 * both CAP.NP and port_map.
 */
n_ports = max(ahci_nr_ports(hpriv->cap), fls(hpriv->port_map));

host = ata_host_alloc_pinfo(dev, ppi, n_ports);
if (!host) {
    clk_disable_unprepare(hpriv->clk);
    return -ENOMEM;
}

host->private_data = hpriv;

if (!(hpriv->cap & HOST_CAP_SSS) || ahci_ignore_sss)
    host->flags |= ATA_HOST_PARALLEL_SCAN;
else
    printk(KERN_INFO "ahci: SSS flag set, parallel bus scan disabled\n");

if (pi.flags & ATA_FLAG_EM)
    ahci_reset_em(host);

for (i = 0; i < host->n_ports; i++) {
    struct ata_port *ap = host->ports[i];

    ata_port_desc(ap, "mmio %pR", mem);
    ata_port_desc(ap, "port 0x%x", 0x100 + ap->port_no * 0x80);

    /* set enclosure management message type */
    if (ap->flags & ATA_FLAG_EM)
        ap->em_message_type = hpriv->em_msg_type;

    /* disabled/not-implemented port */
    if (!(hpriv->port_map & (1 << i)))
        ap->ops = &ata_dummy_port_ops;
}

/* Enabling regret bit to enables the SATA unit to regret
a request that didn't receive an acknowledge and avoid a deadlock */

writel(0x4, hpriv->mmio + VENDOR_SPECIFIC_0_ADDR);
writel(0x80, hpriv->mmio + VENDOR_SPECIFIC_0_DATA);

rc = ahci_reset_controller(host);

```

```

if (rc) {
    clk_disable_unprepare(hpriv->clk);
    return rc;
}

ahci_init_controller(host);
ahci_print_info(host, "platform");

rc = ata_host_activate(host, irq, ahci_interrupt, IRQF_SHARED,
    &ahci_mv_platform_sht);
if (rc) {
    clk_disable_unprepare(hpriv->clk);
    return rc;
}

return 0;
}

static void ahci_mv_host_stop(struct ata_host *host)
{
    struct ahci_host_priv *hpriv = host->private_data;
    clk_disable_unprepare(hpriv->clk);
}

#ifdef CONFIG_PM_SLEEP
static int ahci_mv_suspend(struct device *dev)
{
    struct ata_host *host = dev_get_drvdata(dev);
    struct ahci_host_priv *hpriv = host->private_data;
    void __iomem *mmio = hpriv->mmio;
    u32 ctl;
    int rc;

    if (hpriv->flags & AHCI_HFLAG_NO_SUSPEND) {
        dev_err(dev, "firmware update required for suspend/resume\n");
        return -EIO;
    }

    /*
     * AHCI spec rev1.1 section 8.3.3:
     * Software must disable interrupts prior to requesting a
     * transition of the HBA to D3 state.
     */
    ctl = readl(mmio + HOST_CTL);
    ctl &= ~HOST_IRQ_EN;
    writel(ctl, mmio + HOST_CTL);
    readl(mmio + HOST_CTL); /* flush */
}

```

```

rc = ata_host_suspend(host, PMSG_SUSPEND);
if (rc)
    return rc;

return 0;
}

static int ahci_mv_resume(struct device *dev)
{
    struct ata_host *host = dev_get_drvdata(dev);
    struct ahci_host_priv *hpriv = host->private_data;
    const struct mbus_dram_target_info *dram;
    int rc;

    dram = mv_mbus_dram_info();
    if (dram)
        ahci_mv_windows_config(hpriv, dram);

    if (dev->power.power_state.event == PM_EVENT_SUSPEND) {
        rc = ahci_reset_controller(host);
        if (rc)
            return rc;

        ahci_init_controller(host);
    }

    ata_host_resume(host);

    return 0;
}
#endif

static SIMPLE_DEV_PM_OPS(ahci_mv_pm_ops, ahci_mv_suspend, ahci_mv_resume);

static struct platform_driver ahci_mv_driver = {
    .probe = ahci_mv_probe,
    .remove = ata_platform_remove_one,
    .driver = {
        .name = "ahci_mv",
        .owner = THIS_MODULE,
        .of_match_table = ahci_mv_of_match,
        .pm = &ahci_mv_pm_ops,
    },
};

module_platform_driver(ahci_mv_driver);

MODULE_DESCRIPTION("Marvell AHCI SATA platform driver");

```

```
MODULE_AUTHOR("Marcin Wojtas <mw@semihalf.com>");  
MODULE_LICENSE("GPL");  
MODULE_ALIAS("platform:ahci_mv");
```

1.15 Iodash 3.7.0

1.15.1 Available under license :

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1.16 moment 2.10.6

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```
<signature of Ty Coon>, 1 April 1989
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```

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1.23 react-tabs 2.1.0

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*/

```

1.26 u-boot 2013_Q3_eng_drop_2

1.26.1 Available under license :

```

From d223f6b3f1be839e23c5b21aa4a266f84a75acec Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 09:07:29 +0300
Subject: [PATCH 0631/2009] fix: common: update copyright header

```

use include to mvCopyright.h file instead of an old copyright header

```

Change-Id: I0e25072e7e163c750bbfec50958a194c78e6c68
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3035
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>

```

```

board/mv_ebu/common/common/mvList.c | 17 +++-----
board/mv_ebu/common/common/mvList.h | 17 +++-----
board/mv_ebu/common/common/mvStack.c | 17 +++-----
board/mv_ebu/common/common/mvStack.h | 17 +++-----
4 files changed, 12 insertions(+), 56 deletions(-)

```

```

diff --git a/board/mv_ebu/common/common/mvList.c b/board/mv_ebu/common/common/mvList.c
index 5d7928f..541b190 100644

```

```

--- a/board/mv_ebu/common/common/mvList.c
+++ b/board/mv_ebu/common/common/mvList.c
@@ -1,17 +1,6 @@

```

```

_/*****

```

```

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_*****

+#include "mvCopyright.h"
+
+/******
* mvList.c - Implementation File for Linked List.
*
* DESCRIPTION:
diff --git a/board/mv_ebu/common/common/mvList.h b/board/mv_ebu/common/common/mvList.h
index 9050b9e..ad26655 100644
--- a/board/mv_ebu/common/common/mvList.h
+++ b/board/mv_ebu/common/common/mvList.h
@@ -1,17 +1,6 @@
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-* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL).                      *
_*****

+#include "mvCopyright.h"
+
+/******
* mvList.h - Header File for Linked List.
*
* DESCRIPTION:
diff --git a/board/mv_ebu/common/common/mvStack.c b/board/mv_ebu/common/common/mvStack.c
index 696cb06..a2a0b0b 100644
--- a/board/mv_ebu/common/common/mvStack.c
+++ b/board/mv_ebu/common/common/mvStack.c
@@ -1,17 +1,6 @@

```

```

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_* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL). *
_******
+#include "mvCopyright.h"
+
+/******
_* mvQueue.c
_*
_* FILENAME: $Workfile: mvStack.c $
diff --git a/board/mv_ebu/common/common/mvStack.h b/board/mv_ebu/common/common/mvStack.h
index e7ff74b..353d52a 100644
--- a/board/mv_ebu/common/common/mvStack.h
+++ b/board/mv_ebu/common/common/mvStack.h
@@ -1,17 +1,6 @@
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_* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL). *
_******
+#include "mvCopyright.h"
+
+/******
_* mvStack.h - Header File for :
_*
_* FILENAME: $Workfile: mvStack.h $
--
1.7.9.5

```

From 0adbe3af131850060daafb19644ddb1742beb9af Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 10:28:42 +0300
Subject: [PATCH 0636/2009] fix: cpu: update copyright header

use include to mvCopyright.h file instead of GPL header

Change-Id: Idc0e1117289627feb86e93f9911d9599d87b8116
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: <http://vgitil04.il.marvell.com:8080/3036>
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>

```
.../common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c | 16 +-----  
board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c      | 16 +-----  
board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c    | 16 +-----  
board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c  | 16 +-----  
4 files changed, 4 insertions(+), 60 deletions(-)
```

```
diff --git a/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c  
b/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c  
index 5b9a091..db2268f 100644
```

```
--- a/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c  
+++ b/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c  
@@ -1,18 +1,4 @@
```

```
_/*
```

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- */
```

```
+#include "mvCopyright.h"
```

```
#include "mvOs.h"
```

```
#include "../mvCpuCntrs.h"
```

```
diff --git a/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c b/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c  
index 33f8c06..f78bc0b 100644
```

```
--- a/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c  
+++ b/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c
```

```

@@ -1,18 +1,4 @@
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- * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
- */
+#include "mvCopyright.h"

#include "mvOs.h"
#include "mvCpuCnters.h"
diff --git a/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cnters.c
b/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cnters.c
index 18120be..19460af 100644
--- a/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cnters.c
+++ b/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cnters.c
@@ -1,18 +1,4 @@
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- */
+#include "mvCopyright.h"

#include "mvOs.h"
#include "mvCpuL2Cnters.h"
diff --git a/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cnters.c
b/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cnters.c
index 0b38381..836f267 100644

```



```

--- a/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
@@ -1,18 +1,4 @@
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- * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
- */
+#include "mvCopyright.h"

```

```

#include "mvOs.h"
#include "../mvCpuCntrs.h"

```

--

1.7.9.5

From 61c56e2b35786fa69e425f3e73d4cbd8e54e8e18 Mon Sep 17 00:00:00 2001

From: Neta Zur <neta@marvell.com>

Date: Tue, 9 Jul 2013 16:16:38 +0300

Subject: [PATCH 0531/2009] Marvell copyright header

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Change-Id: Ie8b6f03bfab41273575b451e7c741507b8cca17d

Signed-off-by: Neta Zur <neta@marvell.com>

Reviewed-on: <http://vgitil04.il.marvell.com:8080/2463>

Reviewed-by: Maen Suleiman <maen@marvell.com>

Reviewed-by: Nadav Haklai <nadavh@marvell.com>

Tested-by: Nadav Haklai <nadavh@marvell.com>

board/mv_ebu/common/common/mvCopyright.h | 97 ++++++

1 file changed, 97 insertions(+)

create mode 100644 board/mv_ebu/common/common/mvCopyright.h

```

diff --git a/board/mv_ebu/common/common/mvCopyright.h b/board/mv_ebu/common/common/mvCopyright.h
new file mode 100644
index 0000000..458562b
--- /dev/null
+++ b/board/mv_ebu/common/common/mvCopyright.h
@@ -0,0 +1,97 @@
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--

1.7.9.5

From 3119d3409bd752808772589578e525a1294935dc Mon Sep 17 00:00:00 2001

From: Neta Zur <neta@marvell.com>

Date: Tue, 1 Oct 2013 11:01:52 +0200

Subject: [PATCH 0671/2009] fix: qd-dsdt: update copyright header

use qd-dsdt copyright header file instead of proprietary header

Change-Id: Id745d6cb389de29a86b5762e9d15316bf7d08dcc

Signed-off-by: Neta Zur <neta@marvell.com>

Reviewed-on: <http://vgitil04.il.marvell.com:8080/3619>

Tested-by: Star_Automation <star@marvell.com>

Reviewed-by: Nadav Haklai <nadavh@marvell.com>

```
.../mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h | 15 ++-----
.../common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h | 11 +-----
.../common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h | 15 ++-----
.../common/mv_hal/qd-dsdt/Include/msApiTypes.h | 11 +-----
4 files changed, 6 insertions(+), 46 deletions(-)
```

diff --git a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h

b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h

index 4fd3f94..ca5b8e7 100644

--- a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h

+++ b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h

@@ -1,17 +1,6 @@

+#include <Copyright.h>

+

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-* AND GALILEO TECHNOLOGY, INC. (GTI).
-*****
* gtPhy.h
*
* DESCRIPTION:
diff --git a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h
b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h
index a2ef122..ee1b364 100644
--- a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h
+++ b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h
@@ -1,13 +1,4 @@
-/*
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- */
+#include <Copyright.h>

/*
* FILENAME:  $Workfile: mv_types.h $
diff --git a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h b/board/mv_ebu/common/mv_hal/qd-
dsdt/Include/h/msApi/gtVct.h
index 1c58a1b..ded5c5d 100755
--- a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h
+++ b/board/mv_ebu/common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h
@@ -1,17 +1,6 @@
+#include <Copyright.h>
+
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- * ASIA PTE LTD. (MAPL), MARVELL JAPAN K.K. (MJKK), GALILEO TECHNOLOGY LTD. (GTL)

```

```

-* AND GALILEO TECHNOLOGY, INC. (GTI).
_*****
* gtPhy.h
*
* DESCRIPTION:
diff --git a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h b/board/mv_ebu/common/mv_hal/qd-
dsdt/Include/msApiTypes.h
index 3afd0c7..5e29ab6 100755
--- a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h
+++ b/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h
@@ -1,13 +1,4 @@
-/*
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- */
+#include <Copyright.h>

/*
* FILENAME:  $Workfile: mv_types.h $
--
1.7.9.5
From d94585ce826b7e523b124e24ab8887517f159640 Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 10:31:30 +0300
Subject: [PATCH 0638/2009] fix: neta: update copyright header

    use Marvell triple license header instead of GPL header

Change-Id: Ibf7ffa3abf58354f7c9cdc705258ff04af69c930
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3037
Reviewed-by: Dmitri Epshtein <dima@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
Tested-by: Nadav Haklai <nadavh@marvell.com>
---
board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c | 36 +++++
1 file changed, 36 insertions(+)

diff --git a/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
b/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
index 232bb6f..8260963 100644
--- a/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c

```

+++ b/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
@@ -9,6 +9,12 @@ introductory statement regarding license alternatives, (ii) delete the two
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+
*****/

#include "mvOs.h"

--

1.7.9.5

From b607bda07a2440b3778130b0c6bb5a9ba91b04c0 Mon Sep 17 00:00:00 2001

From: Neta Zur <neta@marvell.com>

Date: Mon, 1 Jul 2013 15:01:52 +0300

Subject: [PATCH 0497/2009] Add Marvell copyright to VANIR driver

Add Marvell copyright and Marvell GPL license to the VANIR driver files.
This is Marvell driver that was originated from Freescale driver.

Change-Id: I1dc2893307d608591bfa9e64b18ed9e84969ef59

Signed-off-by: Neta Zur <neta@marvell.com>

Reviewed-on: <http://vgitil04.il.marvell.com:8080/2376>

Reviewed-by: Star_Automation <star@marvell.com>

Tested-by: Omri Itach <omrii@marvell.com>

Reviewed-by: Omri Itach <omrii@marvell.com>

drivers/block/mv94xx_scsi.c | 18 ++++++

include/mv94xx_scsi.h | 18 ++++++

2 files changed, 36 insertions(+)

diff --git a/drivers/block/mv94xx_scsi.c b/drivers/block/mv94xx_scsi.c

index 8090c04..f3f0d89 100644

--- a/drivers/block/mv94xx_scsi.c

+++ b/drivers/block/mv94xx_scsi.c

@@ -1,3 +1,21 @@

+/******

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+*****/


```

/*
 * Copyright (C) Freescale Semiconductor, Inc. 2006.
 * Author: Jason Jin<Jason.jin@freescale.com>
diff --git a/include/mv94xx_scsi.h b/include/mv94xx_scsi.h
index 16fcb34..6f1c058 100644
--- a/include/mv94xx_scsi.h
+++ b/include/mv94xx_scsi.h
@@ -1,3 +1,21 @@
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+*****/
/*
 * Copyright (C) Freescale Semiconductor, Inc. 2006.
 * Author: Jason Jin<Jason.jin@freescale.com>
--
1.7.9.5

```

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