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1.3 lua 5.0.2

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1.4 saaj 1.4

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import org.apache.axis.AxisFault;

import org.apache.axis.EngineConfiguration;

import org.apache.axis.components.logger.LogFactory;

import org.apache.axis.deployment.wsdd.WSDDConstants;

import org.apache.axis.message.SOAPBodyElement;

import org.apache.axis.utils.Messages;

import org.apache.axis.utils.Options;

import

org.apache.axis.utils.StringUtils;

import org.apache.commons.logging.Log;

import javax.xml.rpc.ServiceException;

import java.io.ByteArrayInputStream;

import java.io.FileInputStream;

import java.io.InputStream;

```

import java.net.URL;
import java.util.Vector;

/**
 * An admin client object that can be used both from the command line
 * and programmatically.
 *
 * @author Rob Jellinghaus (robj@unrealities.com)
 * @author Doug Davis (dug@us.ibm.com)
 * @author Simeon Simeonov (simeons@macromedia.com)
 */

public class AdminClient
{
    protected static Log log =
        LogFactory.getLog(AdminClient.class.getName());

    private static ThreadLocal defaultConfiguration = new ThreadLocal();

    /**
     * If the user calls this with an EngineConfiguration object, all
     * AdminClients on this thread will use that EngineConfiguration
     * rather than the default one. This is primarily to enable the
     * deployment of custom transports and handlers.
     *
     * @param config the EngineConfiguration which should be used
     */
    public static void setDefaultConfiguration(EngineConfiguration config)
    {
        defaultConfiguration.set(config);
    }

    private static String getUsageInfo()
    {
        String lSep = System.getProperty("line.separator");
        StringBuffer msg = new StringBuffer();
        // 26 is the # of lines in resources.properties
        msg.append(Messages.getMessage("acUsage00")).append(lSep);
        msg.append(Messages.getMessage("acUsage01")).append(lSep);
        msg.append(Messages.getMessage("acUsage02")).append(lSep);
        msg.append(Messages.getMessage("acUsage03")).append(lSep);
        msg.append(Messages.getMessage("acUsage04")).append(lSep);
        msg.append(Messages.getMessage("acUsage05")).append(lSep);
        msg.append(Messages.getMessage("acUsage06")).append(lSep);
        msg.append(Messages.getMessage("acUsage07")).append(lSep);
        msg.append(Messages.getMessage("acUsage08")).append(lSep);
    }
}

```

```

    msg.append(Messages.getMessage("acUsage09")).append(lSep);
    msg.append(Messages.getMessage("acUsage10")).append(lSep);
    msg.append(Messages.getMessage("acUsage11")).append(lSep);
    msg.append(Messages.getMessage("acUsage12")).append(lSep);
    msg.append(Messages.getMessage("acUsage13")).append(lSep);
    msg.append(Messages.getMessage("acUsage14")).append(lSep);
    msg.append(Messages.getMessage("acUsage15")).append(lSep);
    msg.append(Messages.getMessage("acUsage16")).append(lSep);
    msg.append(Messages.getMessage("acUsage17")).append(lSep);
    msg.append(Messages.getMessage("acUsage18")).append(lSep);
    msg.append(Messages.getMessage("acUsage19")).append(lSep);
    msg.append(Messages.getMessage("acUsage20")).append(lSep);
    msg.append(Messages.getMessage("acUsage21")).append(lSep);
    msg.append(Messages.getMessage("acUsage22")).append(lSep);

    msg.append(Messages.getMessage("acUsage23")).append(lSep);
    msg.append(Messages.getMessage("acUsage24")).append(lSep);
    msg.append(Messages.getMessage("acUsage25")).append(lSep);
    msg.append(Messages.getMessage("acUsage26")).append(lSep);
    return msg.toString();
}

```

```
/**
```

```
 * the object that represents our call
```

```
 */
```

```
protected Call call;
```

```
/**
```

```
 * Construct an admin client w/o a logger.
```

```
 * If the client cannot create a call object, then it does not throw an exception.
```

```
 * Instead it prints a message to {@link System.err}.
```

```
 * This is for 'historical reasons'
```

```
 */
```

```
public AdminClient()
```

```
{
```

```
    try {
```

```
        initAdminClient();
```

```
    } catch (ServiceException e) {
```

```
        System.err.println(Messages.getMessage("couldntCall00") + ": " + e);
```

```
        call = null;
```

```
    }
```

```
}
```

```
/**
```

```
 * this is a somewhat contrived variant constructor, one that throws an exception
```

```

* if things go wrong.
* @param ignored
*/
public AdminClient(boolean ignored) throws ServiceException {
    initAdminClient();
}

/**
* core initialisation routine
*
* @throws ServiceException
*/
private void initAdminClient() throws ServiceException {
    // Initialize our Service - allow the user to override the
    // default configuration with a thread-local version (see
    // setDefaultConfiguration() above)
    EngineConfiguration config =
        (EngineConfiguration) defaultConfiguration.get();
    Service service;
    if (config != null) {
        service = new Service(config);
    } else {
        service = new Service();
    }
    call = (Call) service.createCall();
}

/**
* External access to our <code>Call</code> object.
* This will be null if the non-exceptioning constructor was used
* and the construction failed.
* @return the <code>Call</code> object this instance uses
*/
public Call getCall()
{
    return call;
}

/**
* process the options then run a list call
* @param opts
* @return
* @throws Exception
*/
public String list(Options opts) throws Exception {
    processOpts( opts );
    return list();
}

```

```

}

/**
 * send a list command
 * @return the response from the call
 * @throws Exception
 */
public String list() throws Exception {
    log.debug( Messages.getMessage("doList00") );
    String      str = "<m:list xmlns:m=\"" + WSDDConstants.URI_WSDD + "\"/>";
    ByteArrayInputStream input = new ByteArrayInputStream(str.getBytes());
    return process(input);
}

/**
 * process the command line ops, then send a quit command
 * @param opts
 * @return
 * @throws Exception
 */
public String quit(Options opts) throws
Exception {
    processOpts( opts );
    return quit();
}

/**
 * root element of the undeploy request
 */
protected static final String ROOT_UNDEPLOY= WSDDConstants.QNAME_UNDEPLOY.getLocalPart();

/**
 * make a quit command
 * @return
 * @throws Exception
 */
public String quit() throws Exception {
    log.debug(Messages.getMessage("doQuit00"));
    String      str = "<m:quit xmlns:m=\"" + WSDDConstants.URI_WSDD + "\"/>";
    ByteArrayInputStream input = new ByteArrayInputStream(str.getBytes());
    return process(input);
}

/**
 * undeploy a handler
 * @param handlerName name of the handler to undeploy
 * @return
 * @throws Exception

```

```

*/
public String undeployHandler(String handlerName) throws Exception {
    log.debug(Messages.getMessage("doQuit00"));
    String      str = "<m:"+ROOT_UNDEPLOY +" xmlns:m=\""+ WSDDConstants.URI_WSDD
+ "\">" +
                "<handler name=\""+ handlerName + "\"/>" +
                "</m:"+ROOT_UNDEPLOY + ">" ;
    ByteArrayInputStream input = new ByteArrayInputStream(str.getBytes());
    return process(input);
}

/**
 * undeploy a service
 * @param serviceName name of service
 * @return
 * @throws Exception
 */
public String undeployService(String serviceName) throws Exception {
    log.debug(Messages.getMessage("doQuit00"));
    String      str = "<m:"+ROOT_UNDEPLOY +" xmlns:m=\""+ WSDDConstants.URI_WSDD + "\">" +
                "<service name=\""+ serviceName + "\"/>" +
                "</m:"+ROOT_UNDEPLOY + ">" ;
    ByteArrayInputStream input = new ByteArrayInputStream(str.getBytes());
    return process(input);
}

/**
 * <p>Processes a set of administration commands.</p>
 * <p>The
following commands are available:</p>
 * <ul>
 * <li><code>-l<i>url</i></code> sets the AxisServlet URL</li>
 * <li><code>-h<i>hostName</i></code> sets the AxisServlet host</li>
 * <li><code>-p<i>portNumber</i></code> sets the AxisServlet port</li>
 * <li><code>-s<i>servletPath</i></code> sets the path to the
AxisServlet</li>
 * <li><code>-f<i>fileName</i></code> specifies that a simple file
protocol should be used</li>
 * <li><code>-u<i>username</i></code> sets the username</li>
 * <li><code>-w<i>password</i></code> sets the password</li>
 * <li><code>-d</code> sets the debug flag (for instance, -ddd would
set it to 3)</li>
 * <li><code>-t<i>name</i></code> sets the transport chain touse</li>
 * <li><code>list</code> will list the currently deployed services</li>
 * <li><code>quit</code> will quit (???)</li>
 * <li><code>passwd <i>value</i></code>
changes the admin password</li>
 * <li><code><i>xmlConfigFile</i></code> deploys or undeploys

```

```

* Axis components and web services</li>
* </ul>
* <p>If <code>-l</code> or <code>-h -p -s</code> are not set, the
* AdminClient will invoke
* <code>http://localhost:8080/axis/servlet/AxisServlet</code>.</p>
*
* @param args Commands to process
* @return XML result or null in case of failure. In the case of multiple
* commands, the XML results will be concatenated, separated by \n
* @exception Exception Could be an IO exception, an AxisFault or something else
*/

```

```

public String process(String[] args) throws Exception {
    StringBuffer sb = new StringBuffer();

    Options opts = new Options( args );
    opts.setDefaultURL("http://localhost:8080/axis/services/AdminService");

    if (opts.isFlagSet('d') > 0) {
        // Set logger properties... !!!
    }

    args
= opts.getRemainingArgs();

    if ( args == null || opts.isFlagSet('?') > 0) {
        System.out.println(Messages.getMessage("usage00","AdminClient [Options] [list | <deployment-descriptor-
files>]"));
        System.out.println("");
        System.out.println(getUsageInfo());
        return null;
    }

    for ( int i = 0 ; i < args.length ; i++ ) {
        InputStream input = null;

        if ( args[i].equals("list") )
            sb.append( list(opts) );
        else if ( args[i].equals("quit") )
            sb.append( quit(opts) );
        else if ( args[i].equals("passwd") ) {
            System.out.println(Messages.getMessage("changePwd00"));
            if (args[i + 1] == null) {
                System.err.println(Messages.getMessage("needPwd00"));
                return null;
            }
            String str = "<m:passwd xmlns:m=\"http://xml.apache.org/axis/wsdd/\">";

```

```

str += args[i + 1];
    str += "</m:passwd>";
    input = new ByteArrayInputStream(str.getBytes());
    i++;
    sb.append( process(opts, input) );
}
else {
    if(args[i].indexOf(java.io.File.pathSeparatorChar)==-1){
        System.out.println( Messages.getMessage("processFile00", args[i]) );
        sb.append( process(opts, args[i] ) );
    } else {
        java.util.StringTokenizer tokenizer = null ;
        tokenizer = new java.util.StringTokenizer(args[i],
            java.io.File.pathSeparator);
        while(tokenizer.hasMoreTokens()) {
            String file = tokenizer.nextToken();
            System.out.println( Messages.getMessage("processFile00", file) );
            sb.append( process(opts, file) );
            if(tokenizer.hasMoreTokens())
                sb.append("\n");
        }
    }
}
}

return sb.toString();
}

/**
 * go from the (parsed) command line to setting properties on our call object.
 * @param opts
 * @throws Exception if call==null
 */
public void processOpts(Options opts) throws Exception {
    if (call == null) {
        throw new Exception(Messages.getMessage("nullCall00"));
    }

    URL address = new URL(opts.getURL());
    setTargetEndpointAddress(address);
    setLogin(opts.getUser(), opts.getPassword());

    String tName = opts.isValueSet( 't' );
    setTransport(tName);
}

/**
 * set the username and password

```



```

* requires that call!=null
* @param user username
* @param password password
*/
public void setLogin(String user, String password) {
    call.setUsername( user );
    call.setPassword( password );
}

/**
* set the URL to deploy to
* requires that call!=null
* @param address
*/
public void setTargetEndpointAddress(URL address) {
    call.setTargetEndpointAddress( address );
}

/**
* set the transport to deploy with.
* requires that call!=null
* @param transportName a null or empty value does not trigger a setting
*/
public void setTransport(String transportName) {
    if(transportName != null && !transportName.equals("")) {
        call.setProperty( Call.TRANSPORT_NAME, transportName );
    }
}

public String process(InputStream input) throws Exception {
    return process(null, input );
}

public String process(URL xmlURL) throws Exception {
    return process(null, xmlURL.openStream() );
}

/**
* process an XML file containing a pre-prepared admin message
* @param xmlFile file to load
* @return
* @throws Exception
*/
public String process(String xmlFile) throws Exception {
    FileInputStream in = new FileInputStream(xmlFile);
    String result = process(null, in );
    return result ;
}

```

```

public String process(Options opts, String xmlFile) throws Exception {
    processOpts( opts );
    return process( xmlFile );
}

/**
 * submit the input stream's contents to the endpoint, return the results as a string.
 * The input stream is always closed after the call, whether the request worked or not
 * @param opts options -can be null
 * @param input -input stream for request
 * @return
 * @throws Exception if the call was null
 * @throws AxisFault if the invocation returned an empty response
 */
public String process(Options opts, InputStream input) throws Exception {
    try {
        if (call == null) {
            //validate that the call is
not null
            throw new Exception(Messages.getMessage("nullCall00"));
        }

        if ( opts != null ) {
            //process options if supplied
            processOpts( opts );
        }

        call.setUseSOAPAction( true);
        call.setSOAPActionURI( "urn:AdminService");

        Vector result = null ;
        Object[] params = new Object[] { new SOAPBodyElement(input) };
        result = (Vector) call.invoke( params );

        if (result == null || result.isEmpty()) {
            throw new AxisFault(Messages.getMessage("nullResponse00"));
        }

        SOAPBodyElement body = (SOAPBodyElement) result.elementAt(0);
        return body.toString();
    } finally {
        input.close();
    }
}

/**
 * Creates in instance of <code>AdminClient</code> and

```

```

* invokes <code>process(args)</code>.
* <p>Diagnostic output goes to <code>log.info</code>.</p>
* @param args Commands to process
*/
public static void main (String[] args)
{
    try {
        AdminClient admin = new AdminClient();

        String result = admin.process(args);
        if (result != null) {
            System.out.println( StringUtils.unescapeNumericChar(result) );
        } else {
            System.exit(1);
        }
    } catch (AxisFault ae) {
        System.err.println(Messages.getMessage("exception00") + " " + ae.dumpToString());
        System.exit(1);
    } catch (Exception e) {
        System.err.println(Messages.getMessage("exception00") + " " + e.getMessage());
        System.exit(1);
    }
}
}
}

```

1.5 csrp 1.0

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1.6 grub 0.94

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FIRST NOTE:

This file contains some details about the copyright history of contributions to the UnZip project.

Additionally, it summarises some exceptions to the general BSD-like copyright found in LICENSE that covers our generic code and most of the system specific ports.

Please read LICENSE first to find out what is allowed to do with Info-ZIP's UnZip code.

There are currently two explicit copyrights on portions of UnZip code (at least, of which Info-ZIP is aware):

Jim Luther's Mac OS File Manager interface code; and Christopher Evans' MacBinaryIII coding code (for the MacOS port).. These copyrights are discussed in more detail below.

All remaining code is now (starting with UnZip version 5.41) covered by the new Info-ZIP license. For details, please read the accompanying file LICENSE. The terms and conditions in this license supersede the copyright conditions of the contributions by Igor Mandrichenko (vms/vms.c), Greg Roelofs (zipinfo.c, new version of unshrink.c), Mike White (Windows DLL code in "windll/*"), Steve P. Miller (Pocket UnZip GUI "wince/*"), and Mark Adler (inflate/explode decompression core routines, previously put into the public domain). All these Info-ZIP contributors (or "primary" authors) have permitted us to replace their copyright notes by the Info-ZIP License.

Frequently Asked Questions regarding (re)distribution of Zip and UnZip are near the end of this file.

There are no known patents on any of the code in UnZip. Unisys claims a patent on LZW encoding and on LZW decoding _in an apparatus that performs LZW encoding_, but the patent appears to exempt a stand-alone decoder (as in UnZip's unshrink.c). Unisys has publicly claimed otherwise, but the issue has never been tested in court. Since this point is unclear, unshrinking is not enabled by default. It is the responsibility of the user to make his or her peace with Unisys and

its licensing requirements. (unshrink.c may be removed from future releases altogether.)

The original unzip source code has been extensively modified and almost entirely rewritten (changes include random zipfile access rather than sequential; replacement of unimplode() with explode(); replacement of old unshrink() with new (unrelated) unshrink(); replacement of output routines; addition of inflate(), wildcards, filename-mapping, text translation, ...; etc.). As far as we can tell, only the core code of the unreduce method remained substantially similar to Mr. Smith's original source. As of UnZip

5.42, the complete

core code is now covered by the Info-ZIP Licence. Therefore, support for the reduce method has been removed.

The drop of the reduce method should only affect some test archives, reducing was never used in any publically distributed Zip program.

For pathologic cases where support for reduced archive entries is needed, the unreduce code copyrighted by Samuel H. Smith is available as a separate distribution (the restricted copyright of this code is cited below in the "historical" section).

The following copyright applies to the Mac OS File Manager interface code (macos/source/macstuff.[ch]), distributed with UnZip 5.4 and later:

```
* MoreFiles
*
* A collection of File Manager and related routines
*
* by Jim Luther (Apple Macintosh Developer Technical Support Emeritus)
* with significant code contributions by Nitin Ganatra
* (Apple Macintosh Developer Technical Support Emeritus)
* Copyright 1992-1998
```

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```
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*
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* redistribute the source, we require that you make
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```

- * from Apple Sample Code, but that you've made
- * changes."

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The following copyright applies to the Mac OS "macbin3" decoding code (extra field compatibility with ZipIt):

- * MacBinaryIII.h
- *
- * Copyright 1997 Christopher Evans (cevens@poppybank.com)
- *
- * Basic encoding and decoding of Macintosh files to the
- * MacBinary III spec.
- * -----
- * This source is copyrighted by Christopher Evans (cevens@poppybank.com)
- * (available at ftp://ftp.lazerware.com/MacBinaryIII_src_C.sit
- * homepage of Leonard Rosenthol leonardr@netcom.com)

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Regarding the first stipulation, Mr. Smith was tracked down in southern California some years back [Samuel H. Smith, The Tool Shop; as of mid-May 1994, (213) 851-9969 (voice), (213) 887-2127(?) (subscription BBS), 71150.2731@compuserve.com]:

"He says that he thought that whoever contacted him understood that

he has no objection to the Info-ZIP group's inclusion of his code. His primary concern is that it remain freely distributable, he said."

Despite the fact that our "normal" code has been entirely rewritten and by default no longer contains any of Mr. Smith's code, Info-ZIP remains indebted and grateful to him. We hope he finds our contributions as useful as we have his.

Note that the third and fourth stipulations still apply to any company that wishes to incorporate the unreduce code into its products; if you wish to do so, you must contact Mr. Smith directly regarding licensing.

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- * from Steve P. Miller.

The remaining code was written by many people associated with the Info-ZIP group, with large contributions from (but not limited to): Greg Roelofs (overall program logic, ZipInfo, unshrink, filename mapping/portability, etc.), Mark Adler (inflate, explode, funzip), Kai Uwe Rommel (OS/2), John Bush and Paul Kienitz (Amiga), Antoine Verheijen (Macintosh), Hunter Goatley (more VMS), Mike White (Windows DLLs), Christian Spieler (overall logic, optimization, VMS, etc.) and others. See the file CONTRIBS in the source distribution for a much more complete list of contributors.

The decompression core code for the deflate method (inflate.[ch], explode.c) was originally written by Mark Adler who submitted it as public domain code.

1.12 libusb-compat 0.1.5

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Version 2.1, February 1999

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1.15 lasso 2.5.1

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1.17 pam 1.3.0

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1.18 libpcap 1.8.1

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1.19 diffutils 3.6

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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a) Accompany

the work with the complete corresponding

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that

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The

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```
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1.24 libmnl 1.0.4

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```
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```
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```

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/* \$NetBSD: syscall.h,v 1.215 2008/06/17 16:07:57 tsutsui Exp \$ */

/*

* System call numbers.

*

* created from NetBSD: syscalls.master,v 1.204 2008/06/17 16:05:23 tsutsui Exp

*/

```
#define TARGET_NETBSD_NR_syscall 0
#define TARGET_NETBSD_NR_exit 1
#define TARGET_NETBSD_NR_fork 2
#define TARGET_NETBSD_NR_read 3
#define TARGET_NETBSD_NR_write 4
#define TARGET_NETBSD_NR_open 5
#define TARGET_NETBSD_NR_close 6
#define TARGET_NETBSD_NR_wait4 7
#define TARGET_NETBSD_NR_compat_43_ocreat 8
#define TARGET_NETBSD_NR_link 9
#define TARGET_NETBSD_NR_unlink 10
#define TARGET_NETBSD_NR_chdir 12
#define TARGET_NETBSD_NR_fchdir 13
#define TARGET_NETBSD_NR_mknod 14
#define TARGET_NETBSD_NR_chmod 15
#define TARGET_NETBSD_NR_chown 16
#define TARGET_NETBSD_NR_break 17
#define TARGET_NETBSD_NR_compat_20_getfsstat 18
#define TARGET_NETBSD_NR_compat_43_olseek 19
#define TARGET_NETBSD_NR_getpid 20
#define
TARGET_NETBSD_NR_getpid 20
#define TARGET_NETBSD_NR_compat_40_mount 21
#define TARGET_NETBSD_NR_unmount 22
#define TARGET_NETBSD_NR_setuid 23
#define TARGET_NETBSD_NR_getuid 24
#define TARGET_NETBSD_NR_getuid 24
#define TARGET_NETBSD_NR_geteuid 25
#define TARGET_NETBSD_NR_ptrace 26
#define TARGET_NETBSD_NR_recvmmsg 27
#define TARGET_NETBSD_NR_sendmsg 28
#define TARGET_NETBSD_NR_recvfrom 29
#define TARGET_NETBSD_NR_accept 30
#define TARGET_NETBSD_NR_getpeername 31
#define TARGET_NETBSD_NR_getsockname 32
#define TARGET_NETBSD_NR_access 33
#define TARGET_NETBSD_NR_chflags 34
#define TARGET_NETBSD_NR_fchflags 35
#define TARGET_NETBSD_NR_sync 36
#define TARGET_NETBSD_NR_kill 37
#define TARGET_NETBSD_NR_compat_43_stat43 38
#define TARGET_NETBSD_NR_getppid 39
#define TARGET_NETBSD_NR_compat_43_lstat43 40
```

```

#define TARGET_NETBSD_NR_dup 41
#define TARGET_NETBSD_NR_pipe 42
#define TARGET_NETBSD_NR_getegid
    43
#define TARGET_NETBSD_NR_profil 44
#define TARGET_NETBSD_NR_ktrace 45
#define TARGET_NETBSD_NR_compat_13_sigaction13 46
#define TARGET_NETBSD_NR_getgid 47
#define TARGET_NETBSD_NR_getgid 47
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```

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```
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```

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```
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```

<signature of Ty Coon>, 1 April 1989

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=====

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David Gibson <david@gibson.dropbear.id.au>
(principal original author of dtc and libfdt)
2 November 2007
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/*

* System call numbers.

*

* created from FreeBSD: releng/9.1/sys/kern/syscalls.master 229723

* 2012-01-06 19:29:16Z jhb

*/

```
#define TARGET_FREEBSD_NR_syscall 0
```

```
#define TARGET_FREEBSD_NR_exit 1
```

```
#define TARGET_FREEBSD_NR_fork 2
```

```
#define TARGET_FREEBSD_NR_read 3
```

```
#define TARGET_FREEBSD_NR_write 4
```

```

#define TARGET_FREEBSD_NR_open 5
#define TARGET_FREEBSD_NR_close 6
#define TARGET_FREEBSD_NR_wait4 7
    /* 8 is old creat */
#define TARGET_FREEBSD_NR_link 9
#define TARGET_FREEBSD_NR_unlink 10
    /* 11 is obsolete execv */
#define TARGET_FREEBSD_NR_chdir 12
#define TARGET_FREEBSD_NR_fchdir 13
#define TARGET_FREEBSD_NR_mknod 14
#define TARGET_FREEBSD_NR_chmod 15
#define TARGET_FREEBSD_NR_chown 16
#define TARGET_FREEBSD_NR_break 17
#define TARGET_FREEBSD_NR_freebsd4_getfsstat 18
    /* 19 is old lseek */
#define TARGET_FREEBSD_NR_getpid 20
#define TARGET_FREEBSD_NR_mount 21
#define TARGET_FREEBSD_NR_unmount 22
#define TARGET_FREEBSD_NR_setuid
    23
#define TARGET_FREEBSD_NR_getuid 24
#define TARGET_FREEBSD_NR_geteuid 25
#define TARGET_FREEBSD_NR_ptrace 26
#define TARGET_FREEBSD_NR_recvmsg 27
#define TARGET_FREEBSD_NR_sendmsg 28
#define TARGET_FREEBSD_NR_recvfrom 29
#define TARGET_FREEBSD_NR_accept 30
#define TARGET_FREEBSD_NR_getpeername 31
#define TARGET_FREEBSD_NR_getsockname 32
#define TARGET_FREEBSD_NR_access 33
#define TARGET_FREEBSD_NR_chflags 34
#define TARGET_FREEBSD_NR_fchflags 35
#define TARGET_FREEBSD_NR_sync 36
#define TARGET_FREEBSD_NR_kill 37
    /* 38 is old stat */
#define TARGET_FREEBSD_NR_getppid 39
    /* 40 is old lstat */
#define TARGET_FREEBSD_NR_dup 41
#define TARGET_FREEBSD_NR_pipe 42
#define TARGET_FREEBSD_NR_getegid 43
#define TARGET_FREEBSD_NR_profil 44
#define TARGET_FREEBSD_NR_ktrace 45
    /* 46 is old sigaction */
#define TARGET_FREEBSD_NR_getgid 47
    /* 48 is old sigprocmask */
#define TARGET_FREEBSD_NR_getlogin
    49
#define TARGET_FREEBSD_NR_setlogin 50

```

```

#define TARGET_FREEBSD_NR_acct 51
    /* 52 is old sigpending */
#define TARGET_FREEBSD_NR_sigaltstack 53
#define TARGET_FREEBSD_NR_ioctl 54
#define TARGET_FREEBSD_NR_reboot 55
#define TARGET_FREEBSD_NR_revoke 56
#define TARGET_FREEBSD_NR_symlink 57
#define TARGET_FREEBSD_NR_readlink 58
#define TARGET_FREEBSD_NR_execve 59
#define TARGET_FREEBSD_NR_umask 60
#define TARGET_FREEBSD_NR_chroot 61
    /* 62 is old fstat */
    /* 63 is old getkerninfo */
    /* 64 is old getpagesize */
#define TARGET_FREEBSD_NR_msync 65
#define TARGET_FREEBSD_NR_vfork 66
    /* 67 is obsolete vread */
    /* 68 is obsolete vwrite */
#define TARGET_FREEBSD_NR_sbrk 69
#define TARGET_FREEBSD_NR_sstk 70
    /* 71 is old mmap */
#define TARGET_FREEBSD_NR_vadvice 72
#define TARGET_FREEBSD_NR_munmap 73
#define TARGET_FREEBSD_NR_mprotect 74
#define
TARGET_FREEBSD_NR_madvise 75
    /* 76 is obsolete vhangup */
    /* 77 is obsolete vlimit */
#define TARGET_FREEBSD_NR_mincore 78
#define TARGET_FREEBSD_NR_getgroups 79
#define TARGET_FREEBSD_NR_setgroups 80
#define TARGET_FREEBSD_NR_getpgrp 81
#define TARGET_FREEBSD_NR_setpgid 82
#define TARGET_FREEBSD_NR_setitimer 83
    /* 84 is old wait */
#define TARGET_FREEBSD_NR_swapon 85
#define TARGET_FREEBSD_NR_getitimer 86
    /* 87 is old gethostname */
    /* 88 is old sethostname */
#define TARGET_FREEBSD_NR_getdtablesize 89
#define TARGET_FREEBSD_NR_dup2 90
#define TARGET_FREEBSD_NR_fcntl 92
#define TARGET_FREEBSD_NR_select 93
#define TARGET_FREEBSD_NR_fsync 95
#define TARGET_FREEBSD_NR_setpriority 96
#define TARGET_FREEBSD_NR_socket 97
#define TARGET_FREEBSD_NR_connect 98
    /* 99 is old accept */

```

```

#define TARGET_FREEBSD_NR_getpriority 100
    /* 101 is old send */

    /* 102 is old recv */
    /* 103 is old sigreturn */
#define TARGET_FREEBSD_NR_bind 104
#define TARGET_FREEBSD_NR_setsockopt 105
#define TARGET_FREEBSD_NR_listen 106
    /* 107 is obsolete vtimes */
    /* 108 is old sigvec */
    /* 109 is old sigblock */
    /* 110 is old sigsetmask */
    /* 111 is old sigsuspend */
    /* 112 is old sigstack */
    /* 113 is old recvmmsg */
    /* 114 is old sendmmsg */
    /* 115 is obsolete vtrace */
#define TARGET_FREEBSD_NR_gettimeofday 116
#define TARGET_FREEBSD_NR_getrusage 117
#define TARGET_FREEBSD_NR_getsockopt 118
#define TARGET_FREEBSD_NR_readv 120
#define TARGET_FREEBSD_NR_writev 121
#define TARGET_FREEBSD_NR_settimeofday 122
#define TARGET_FREEBSD_NR_fchown 123
#define TARGET_FREEBSD_NR_fchmod 124
    /* 125 is old recvfrom */
#define TARGET_FREEBSD_NR_setreuid 126
#define TARGET_FREEBSD_NR_setregid
    127
#define TARGET_FREEBSD_NR_rename 128
    /* 129 is old truncate */
    /* 130 is old ftruncate */
#define TARGET_FREEBSD_NR_flock 131
#define TARGET_FREEBSD_NR_mkfifo 132
#define TARGET_FREEBSD_NR_sendto 133
#define TARGET_FREEBSD_NR_shutdown 134
#define TARGET_FREEBSD_NR_socketpair 135
#define TARGET_FREEBSD_NR_mkdir 136
#define TARGET_FREEBSD_NR_rmdir 137
#define TARGET_FREEBSD_NR_utimes 138
    /* 139 is obsolete 4.2 sigreturn */
#define TARGET_FREEBSD_NR_adjtime 140
    /* 141 is old getpeername */
    /* 142 is old gethostid */
    /* 143 is old sethostid */
    /* 144 is old getrlimit */
    /* 145 is old setrlimit */
    /* 146 is old killpg */

```

```

#define TARGET_FREEBSD_NR_killpg 146 /* COMPAT */
#define TARGET_FREEBSD_NR_setsid 147
#define TARGET_FREEBSD_NR_quotactl 148
    /* 149 is old quota */
    /*
150 is old getsockname */
#define TARGET_FREEBSD_NR_nlm_syscall 154
#define TARGET_FREEBSD_NR_nfssvc 155
    /* 156 is old getdirentries */
#define TARGET_FREEBSD_NR_freebsd4_statfs 157
#define TARGET_FREEBSD_NR_freebsd4_fstatfs 158
#define TARGET_FREEBSD_NR_lgetfh 160
#define TARGET_FREEBSD_NR_getfh 161
#define TARGET_FREEBSD_NR_freebsd4_getdomainname 162
#define TARGET_FREEBSD_NR_freebsd4_setdomainname 163
#define TARGET_FREEBSD_NR_freebsd4_uname 164
#define TARGET_FREEBSD_NR_sysarch 165
#define TARGET_FREEBSD_NR_rtprio 166
#define TARGET_FREEBSD_NR_semsys 169
#define TARGET_FREEBSD_NR_msgsys 170
#define TARGET_FREEBSD_NR_shmsys 171
#define TARGET_FREEBSD_NR_freebsd6_pread 173
#define TARGET_FREEBSD_NR_freebsd6_pwrite 174
#define TARGET_FREEBSD_NR_setfib 175
#define TARGET_FREEBSD_NR_ntp_adjtime 176
#define TARGET_FREEBSD_NR_setgid 181
#define TARGET_FREEBSD_NR_setegid 182
#define TARGET_FREEBSD_NR seteuid 183
#define TARGET_FREEBSD_NR_stat
188
#define TARGET_FREEBSD_NR_fstat 189
#define TARGET_FREEBSD_NR_lstat 190
#define TARGET_FREEBSD_NR_pathconf 191
#define TARGET_FREEBSD_NR_fpathconf 192
#define TARGET_FREEBSD_NR_getrlimit 194
#define TARGET_FREEBSD_NR_setrlimit 195
#define TARGET_FREEBSD_NR_getdirentries 196
#define TARGET_FREEBSD_NR_freebsd6_mmap 197
#define TARGET_FREEBSD_NR___syscall 198
#define TARGET_FREEBSD_NR_freebsd6_lseek 199
#define TARGET_FREEBSD_NR_freebsd6_truncate 200
#define TARGET_FREEBSD_NR_freebsd6_ftruncate 201
#define TARGET_FREEBSD_NR___sysctl 202
#define TARGET_FREEBSD_NR_mlock 203
#define TARGET_FREEBSD_NR_munlock 204
#define TARGET_FREEBSD_NR_undelete 205
#define TARGET_FREEBSD_NR_futimes 206
#define TARGET_FREEBSD_NR_getpgid 207

```

```

#define TARGET_FREEBSD_NR_poll 209
#define TARGET_FREEBSD_NR_freebsd7___semctl 220
#define TARGET_FREEBSD_NR_semget 221
#define TARGET_FREEBSD_NR_semop 222
#define TARGET_FREEBSD_NR_freebsd7_msgctl 224
#define TARGET_FREEBSD_NR_msgget
    225
#define TARGET_FREEBSD_NR_msgsnd 226
#define TARGET_FREEBSD_NR_msgrcv 227
#define TARGET_FREEBSD_NR_shmat 228
#define TARGET_FREEBSD_NR_freebsd7_shmctl 229
#define TARGET_FREEBSD_NR_shmdt 230
#define TARGET_FREEBSD_NR_shmget 231
#define TARGET_FREEBSD_NR_clock_gettime 232
#define TARGET_FREEBSD_NR_clock_settime 233
#define TARGET_FREEBSD_NR_clock_getres 234
#define TARGET_FREEBSD_NR_ktimer_create 235
#define TARGET_FREEBSD_NR_ktimer_delete 236
#define TARGET_FREEBSD_NR_ktimer_settime 237
#define TARGET_FREEBSD_NR_ktimer_gettime 238
#define TARGET_FREEBSD_NR_ktimer_getoverrun 239
#define TARGET_FREEBSD_NR_nanosleep 240
#define TARGET_FREEBSD_NR_ntp_gettime 248
#define TARGET_FREEBSD_NR_minherit 250
#define TARGET_FREEBSD_NR_rfork 251
#define TARGET_FREEBSD_NR_openbsd_poll 252
#define TARGET_FREEBSD_NR_issetugid 253
#define TARGET_FREEBSD_NR_lchown 254
#define TARGET_FREEBSD_NR_aio_read 255
#define TARGET_FREEBSD_NR_aio_write 256
#define TARGET_FREEBSD_NR_lio_listio
    257
#define TARGET_FREEBSD_NR_getdents 272
#define TARGET_FREEBSD_NR_lchmod 274
#define TARGET_FREEBSD_NR_netbsd_lchown 275
#define TARGET_FREEBSD_NR_lutimes 276
#define TARGET_FREEBSD_NR_netbsd_msync 277
#define TARGET_FREEBSD_NR_nstat 278
#define TARGET_FREEBSD_NR_nfstat 279
#define TARGET_FREEBSD_NR_nlstat 280
#define TARGET_FREEBSD_NR_preadv 289
#define TARGET_FREEBSD_NR_pwritev 290
#define TARGET_FREEBSD_NR_freebsd4_fhstatfs 297
#define TARGET_FREEBSD_NR_fhopen 298
#define TARGET_FREEBSD_NR_fhstat 299
#define TARGET_FREEBSD_NR_modnext 300
#define TARGET_FREEBSD_NR_modstat 301
#define TARGET_FREEBSD_NR_modfnxt 302

```

```

#define TARGET_FREEBSD_NR_modfind 303
#define TARGET_FREEBSD_NR_kldload 304
#define TARGET_FREEBSD_NR_kldunload 305
#define TARGET_FREEBSD_NR_kldfind 306
#define TARGET_FREEBSD_NR_kldnext 307
#define TARGET_FREEBSD_NR_kldstat 308
#define TARGET_FREEBSD_NR_kldfirstmod 309
#define TARGET_FREEBSD_NR_getsid
    310
#define TARGET_FREEBSD_NR_setresuid 311
#define TARGET_FREEBSD_NR_setresgid 312
    /* 313 is obsolete signanosleep */
#define TARGET_FREEBSD_NR_aio_return 314
#define TARGET_FREEBSD_NR_aio_suspend 315
#define TARGET_FREEBSD_NR_aio_cancel 316
#define TARGET_FREEBSD_NR_aio_error 317
#define TARGET_FREEBSD_NR_oaio_read 318
#define TARGET_FREEBSD_NR_oaio_write 319
#define TARGET_FREEBSD_NR_olio_listio 320
#define TARGET_FREEBSD_NR_yield 321
    /* 322 is obsolete thr_sleep */
    /* 323 is obsolete thr_wakeup */
#define TARGET_FREEBSD_NR_mlockall 324
#define TARGET_FREEBSD_NR_munlockall 325
#define TARGET_FREEBSD_NR___getcwd 326
#define TARGET_FREEBSD_NR_sched_setparam 327
#define TARGET_FREEBSD_NR_sched_getparam 328
#define TARGET_FREEBSD_NR_sched_setscheduler 329
#define TARGET_FREEBSD_NR_sched_getscheduler 330
#define TARGET_FREEBSD_NR_sched_yield 331
#define TARGET_FREEBSD_NR_sched_get_priority_max 332
#define
    TARGET_FREEBSD_NR_sched_get_priority_min 333
#define TARGET_FREEBSD_NR_sched_rr_get_interval 334
#define TARGET_FREEBSD_NR_utrace 335
#define TARGET_FREEBSD_NR_freebsd4_sendfile 336
#define TARGET_FREEBSD_NR_kldsym 337
#define TARGET_FREEBSD_NR_jail 338
#define TARGET_FREEBSD_NR_nnpfs_syscall 339
#define TARGET_FREEBSD_NR_sigprocmask 340
#define TARGET_FREEBSD_NR_sigsuspend 341
#define TARGET_FREEBSD_NR_freebsd4_sigaction 342
#define TARGET_FREEBSD_NR_sigpending 343
#define TARGET_FREEBSD_NR_freebsd4_sigreturn 344
#define TARGET_FREEBSD_NR_sigtimedwait 345
#define TARGET_FREEBSD_NR_sigwaitinfo 346
#define TARGET_FREEBSD_NR___acl_get_file 347
#define TARGET_FREEBSD_NR___acl_set_file 348

```



```

#define TARGET_FREEBSD_NR___acl_get_fd 349
#define TARGET_FREEBSD_NR___acl_set_fd 350
#define TARGET_FREEBSD_NR___acl_delete_file 351
#define TARGET_FREEBSD_NR___acl_delete_fd 352
#define TARGET_FREEBSD_NR___acl_aclcheck_file 353
#define TARGET_FREEBSD_NR___acl_aclcheck_fd
354
#define TARGET_FREEBSD_NR_extattrctl 355
#define TARGET_FREEBSD_NR_extattr_set_file 356
#define TARGET_FREEBSD_NR_extattr_get_file 357
#define TARGET_FREEBSD_NR_extattr_delete_file 358
#define TARGET_FREEBSD_NR_aio_waitcomplete 359
#define TARGET_FREEBSD_NR_getresuid 360
#define TARGET_FREEBSD_NR_getresgid 361
#define TARGET_FREEBSD_NR_kqueue 362
#define TARGET_FREEBSD_NR_kevent 363
#define TARGET_FREEBSD_NR_extattr_set_fd 371
#define TARGET_FREEBSD_NR_extattr_get_fd 372
#define TARGET_FREEBSD_NR_extattr_delete_fd 373
#define TARGET_FREEBSD_NR___setugid 374
#define TARGET_FREEBSD_NR_eaccess 376
#define TARGET_FREEBSD_NR_afs3_syscall 377
#define TARGET_FREEBSD_NR_nmount 378
#define TARGET_FREEBSD_NR___mac_get_proc 384
#define TARGET_FREEBSD_NR___mac_set_proc 385
#define TARGET_FREEBSD_NR___mac_get_fd 386
#define TARGET_FREEBSD_NR___mac_get_file 387
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#define
TARGET_FREEBSD_NR_kenv 390
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#define TARGET_FREEBSD_NR_ksem_close 400
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#define TARGET_FREEBSD_NR_ksem_init 404
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#define TARGET_FREEBSD_NR_ksem_unlink 406
#define TARGET_FREEBSD_NR_ksem_getvalue 407
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#define TARGET_FREEBSD_NR__mac_get_link 410
#define TARGET_FREEBSD_NR__mac_set_link 411
#define TARGET_FREEBSD_NR_extattr_set_link 412
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#define TARGET_FREEBSD_NR__mac_execve 415
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#define TARGET_FREEBSD_NR_getcontext 421
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#define TARGET_FREEBSD_NR_swapcontext 423
#define TARGET_FREEBSD_NR_swapoff 424
#define TARGET_FREEBSD_NR__acl_get_link 425
#define TARGET_FREEBSD_NR__acl_set_link 426
#define TARGET_FREEBSD_NR__acl_delete_link 427
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#define TARGET_FREEBSD_NR_thr_create 430
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#define TARGET_FREEBSD_NR_extattr_list_fd 437
#define TARGET_FREEBSD_NR_extattr_list_file 438
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#define
TARGET_FREEBSD_NR_ksem_timedwait 441
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#define TARGET_FREEBSD_NR_kmq_timedreceive 459
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#define TARGET_FREEBSD_NR_jail_set 507
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#define TARGET_FREEBSD_NR_cap_new 514
#define TARGET_FREEBSD_NR_cap_getrights 515
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#define TARGET_FREEBSD_NR_cap_getmode 517
#define TARGET_FREEBSD_NR_pfork 518
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#define TARGET_FREEBSD_NR_rctl_get_racct 525
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#define TARGET_FREEBSD_NR_posix_fallocate 530
#define TARGET_FREEBSD_NR_posix_fadvise 531
#define TARGET_FREEBSD_NR_MAXSYSCALL 532
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Any executables

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<signature of Ty Coon>, 1 April 1990

Ty Coon, President of Vice

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Index: debug.c

```
=====  
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/debug.c,v  
retrieving revision 1.1  
retrieving revision 1.3  
diff -u -u -r1.1 -r1.3  
--- debug.c 7 Sep 2007 10:01:21 -0000 1.1  
+++ debug.c 15 Jan 2008 13:49:25 -0000 1.3  
@@ -52,7 +52,11 @@  
void X86EMU_trace_regs (void)  
{  
    if (DEBUG_TRACE()) {  
-    x86emu_dump_regs();  
+ if (M.x86.mode & (SYSMODE_PREFIX_DATA | SYSMODE_PREFIX_ADDR)) {  
+    x86emu_dump_xregs();  
+ } else {  
+    x86emu_dump_regs();  
+ }  
    }  
    if (DEBUG_DECODE() && !DEBUG_DECODE_NOPRINT()) {  
        printk("%04x:%04x ",M.x86.saved_cs, M.x86.saved_ip);  
@@ -185,7 +189,7 @@  
        for (i=0; i< M.x86.enc_pos; i++) {  
            sprintf(buf1+2*i,"%02x", fetch_data_byte_abs(s,o+i));  
        }  
-    printk("%-20s",buf1);  
+    printk("%-20s ",buf1);  
    }  
  
static void print_decoded_instruction (void)  
Index: ops2.c
```

```
=====  
RCS file:
```



```

/cvs/osdf/cvs/host/other-licence/x86emu/ops2.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- ops2.c 7 Sep 2007 10:01:21 -0000 1.1
+++ ops2.c 20 Mar 2008 15:48:34 -0000 1.3
@@ -149,8 +149,69 @@
     target += (s16) M.x86.R_IP;
     DECODE_PRINTF2("%04x\n", target);
     TRACE_AND_STEP();
-   if (cond)
+   if (cond) {
+       M.x86.R_IP = (u16)target;
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " LONG COND ");
+   }
+   DECODE_CLEAR_SEGOVR();
+   END_OF_INSTR();
+}
+
+/******
+REMARKS:
+Handles opcode 0x0f,0xC8-0xCF
+*****/
+s32 x86emu_bswap(s32 reg)
+{
+   // perform the byte swap
+   s32 temp = reg;
+   reg = (temp & 0xFF000000) >> 24;
+   reg |= (temp & 0xFF0000) >> 8;
+   reg |= (temp & 0xFF00) << 8;
+   reg |= (temp & 0xFF) << 24;
+   return reg;
+}
+
+void x86emuOp2_bswap(u8 op2)
+{
+   /* byte swap 32 bit register */
+   START_OF_INSTR();
+   DECODE_PRINTF("BSWAP\t");
+   switch (op2) {
+   case 0xc8:
+       DECODE_PRINTF("EAX\n");
+       M.x86.R_EAX = x86emu_bswap(M.x86.R_EAX);
+       break;
+   case 0xc9:
+       DECODE_PRINTF("ECX\n");
+       M.x86.R_ECX = x86emu_bswap(M.x86.R_ECX);

```

```

+   break;
+   case 0xca:
+     DECODE_PRINTF("EDX\n");
+     M.x86.R_EDX = x86emu_bswap(M.x86.R_EDX);
+     break;
+   case 0xcb:
+     DECODE_PRINTF("EBX\n");
+     M.x86.R_EBX = x86emu_bswap(M.x86.R_EBX);
+     break;
+   case 0xcc:
+     DECODE_PRINTF("ESP\n");
+     M.x86.R_ESP = x86emu_bswap(M.x86.R_ESP);
+     break;
+   case 0xcd:
+     DECODE_PRINTF("EBP\n");
+     M.x86.R_EBP = x86emu_bswap(M.x86.R_EBP);
+     break;
+   case 0xce:
+     DECODE_PRINTF("ESI\n");
+     M.x86.R_ESI = x86emu_bswap(M.x86.R_ESI);
+     break;
+   case 0xcf:
+     DECODE_PRINTF("EDI\n");
+     M.x86.R_EDI
+     = x86emu_bswap(M.x86.R_EDI);
+     break;
+   }
+   TRACE_AND_STEP();
+   DECODE_CLEAR_SEGOVR();
+   END_OF_INSTR();
+ }
@@ -1702,14 +1763,14 @@
/* 0xc5 */ x86emuOp2_illegal_op,
/* 0xc6 */ x86emuOp2_illegal_op,
/* 0xc7 */ x86emuOp2_illegal_op,
/* 0xc8 */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xc9 */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xca */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcb */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcc */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcd */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xce */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcf */ x86emuOp2_illegal_op, /* TODO: bswap */
+/* 0xc8 */ x86emuOp2_bswap,
+/* 0xc9 */ x86emuOp2_bswap,
+/* 0xca */ x86emuOp2_bswap,
+/* 0xcb */ x86emuOp2_bswap,
+/* 0xcc */ x86emuOp2_bswap,

```

```
+/* 0xcd */ x86emuOp2_bswap,
```

```
+/* 0xce */ x86emuOp2_bswap,
```

```
+/* 0xcf */ x86emuOp2_bswap,
```

```
/* 0xd0 */ x86emuOp2_illegal_op,
```

```
/* 0xd1 */ x86emuOp2_illegal_op,
```

Index:

ops.c

```
=====
```

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops.c,v

retrieving revision 1.1

diff -u -u -r1.1 ops.c

--- ops.c 7 Sep 2007 10:01:21 -0000 1.1

+++ ops.c 20 Mar 2008 16:52:00 -0000

@@ -1061,7 +1061,11 @@

```
    imm = (s8)fetch_byte_imm();
```

```
    DECODE_PRINTF2("PUSH\t%d\n", imm);
```

```
    TRACE_AND_STEP();
```

```
-    push_word(imm);
```

```
+    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
```

```
+        push_long(imm);
```

```
+    } else {
```

```
+        push_word(imm);
```

```
+    }
```

```
    DECODE_CLEAR_SEGOVR();
```

```
    END_OF_INSTR();
```

```
}
```

@@ -1256,8 +1260,10 @@

```
    target = (u16)(M.x86.R_IP + (s16)offset);
```

```
    DECODE_PRINTF2("%x\n", target);
```

```
    TRACE_AND_STEP();
```

```
-    if (cond)
```

```
+    if (cond) {
```

```
        M.x86.R_IP = target;
```

```
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " NEAR COND ");
```

```
+    }
```

```
    DECODE_CLEAR_SEGOVR();
```

```
    END_OF_INSTR();
```

```
}
```

@@ -2516,9 +2522,11 @@

```
    count = 1;
```

```
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE))
```

```
{
```

```
    /* dont care whether REPE or REPNE */
```

```
-    /* move them until CX is ZERO. */
```

```
-    count = M.x86.R_CX;
```

```
+    /* move them until (E)CX is ZERO. */
```

```
+    count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
```

```

    M.x86.R_CX = 0;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+     M.x86.R_ECX = 0;
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
}
while (count--) {
@@ -2526,6 +2534,8 @@
    store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, val);
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
+   if (M.x86.intr & INTR_HALTED)
+       break;
}
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2559,9 +2569,11 @@
count = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
-   /* move them until CX is ZERO. */
-   count = M.x86.R_CX;
+   /* move them until (E)CX is ZERO. */
+   count = (M.x86.mode
& SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
    M.x86.R_CX = 0;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+     M.x86.R_ECX = 0;
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
}
while (count--) {
@@ -2574,6 +2586,8 @@
}
M.x86.R_SI += inc;
M.x86.R_DI += inc;
+   if (M.x86.intr & INTR_HALTED)
+       break;
}
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2598,16 +2612,21 @@

if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* REPE */
-   /* move them until CX is ZERO. */
-   while (M.x86.R_CX != 0) {
+   /* move them until (E)CX is ZERO. */
+   while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        val1 = fetch_data_byte(M.x86.R_SI);

```

```

    val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_byte(val1, val2);
-   M.x86.R_CX -- 1;
+   if (M.x86.mode & SYSMODE_32BIT_REP)
+
        M.x86.R_ECX -- 1;
+   else
+       M.x86.R_CX -- 1;
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
    if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && (ACCESS_FLAG(F_ZF) == 0) ) break;
    if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
+   if (M.x86.intr & INTR_HALTED)
+       break;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
@@ -2644,8 +2663,8 @@
    TRACE_AND_STEP();
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* REPE */
-       /* move them until CX is ZERO. */
-       while (M.x86.R_CX != 0) {
+       /* move them until (E)CX is ZERO. */
+       while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
            if (M.x86.mode & SYSMODE_PREFIX_DATA) {
                val1 = fetch_data_long(M.x86.R_SI);
                val2 = fetch_data_long_abs(M.x86.R_ES,
M.x86.R_DI);
@@ -2655,11 +2674,16 @@
                val2 = fetch_data_word_abs(M.x86.R_ES, M.x86.R_DI);
                cmp_word((u16)val1, (u16)val2);
            }
-           M.x86.R_CX -- 1;
+           if (M.x86.mode & SYSMODE_32BIT_REP)
+               M.x86.R_ECX -- 1;
+           else
+               M.x86.R_CX -- 1;
            M.x86.R_SI += inc;
            M.x86.R_DI += inc;
            if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && ACCESS_FLAG(F_ZF) == 0 ) break;
            if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
+           if (M.x86.intr & INTR_HALTED)
+               break;
        }
        M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    } else {
@@ -2741,11 +2765,16 @@

```

```

TRACE_AND_STEP();
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
-   /* move them until CX is ZERO. */
-   while (M.x86.R_CX != 0) {
+   /*
move them until (E)CX is ZERO. */
+   while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AL);
-       M.x86.R_CX -- 1;
+       if (M.x86.mode & SYSMODE_32BIT_REP)
+           M.x86.R_ECX -- 1;
+       else
+           M.x86.R_CX -- 1;
        M.x86.R_DI += inc;
+       if (M.x86.intr & INTR_HALTED)
+           break;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
@@ -2783,9 +2812,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-       /* move them until CX is ZERO. */
-       count = M.x86.R_CX;
+       /* move them until (E)CX is ZERO. */
+       count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+     M.x86.R_ECX = 0;

        M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
    while (count--) {
@@ -2795,6 +2826,8 @@
        store_data_word_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AX);
    }
    M.x86.R_DI += inc;
+   if (M.x86.intr & INTR_HALTED)
+       break;
}
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2817,11 +2850,16 @@
    inc = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */

```

```

- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    M.x86.R_AL = fetch_data_byte(M.x86.R_SI);
-    M.x86.R_CX -= 1;
+    if (M.x86.mode & SYSMODE_32BIT_REP)
+        M.x86.R_ECX -= 1;
+    else
+        M.x86.R_CX -= 1;
    M.x86.R_SI += inc;
+    if (M.x86.intr & INTR_HALTED)
+
        break;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
@@ -2859,9 +2897,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* move them until CX is ZERO. */
-        count = M.x86.R_CX;
+        /* move them until (E)CX is ZERO. */
+        count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
+    if (M.x86.mode & SYSMODE_32BIT_REP)
+        M.x86.R_ECX = 0;
        M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
    while (count--) {
@@ -2871,6 +2911,8 @@
        M.x86.R_AX = fetch_data_word(M.x86.R_SI);
    }
    M.x86.R_SI += inc;
+    if (M.x86.intr & INTR_HALTED)
+        break;
}
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -2894,26 +2936,36 @@
    inc = 1;
    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
        /*
REPE */
-        /* move them until CX is ZERO. */
-        while (M.x86.R_CX != 0) {
+        /* move them until (E)CX is ZERO. */

```

```

+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
+   val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
+   cmp_byte(M.x86.R_AL, val2);
-   M.x86.R_CX -= 1;
+   if (M.x86.mode & SYSMODE_32BIT_REP)
+     M.x86.R_ECX -= 1;
+   else
+     M.x86.R_CX -= 1;
+   M.x86.R_DI += inc;
+   if (ACCESS_FLAG(F_ZF) == 0)
+     break;
+   if (M.x86.intr & INTR_HALTED)
+     break;
+ }
+ M.x86.mode &= ~SYSMODE_PREFIX_REPE;
+ } else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
+   /* REPNE */
-   /* move them until CX is ZERO. */
-   while (M.x86.R_CX != 0) {
+   /* move them until (E)CX is ZERO. */
+   while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {

+   val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
+   cmp_byte(M.x86.R_AL, val2);
-   M.x86.R_CX -= 1;
+   if (M.x86.mode & SYSMODE_32BIT_REP)
+     M.x86.R_ECX -= 1;
+   else
+     M.x86.R_CX -= 1;
+   M.x86.R_DI += inc;
+   if (ACCESS_FLAG(F_ZF))
+     break;      /* zero flag set means equal */
+   if (M.x86.intr & INTR_HALTED)
+     break;
+ }
+ M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
+ } else {
@@ -2951,8 +3003,8 @@
+ TRACE_AND_STEP();
+ if (M.x86.mode & SYSMODE_PREFIX_REPE) {
+   /* REPE */
-   /* move them until CX is ZERO. */
-   while (M.x86.R_CX != 0) {
+   /* move them until (E)CX is ZERO. */
+   while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
+     if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+       val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
+       cmp_long(M.x86.R_EAX,

```



```

val);
@@ -2960,16 +3012,21 @@
    val = fetch_data_word_abs(M.x86.R_ES, M.x86.R_DI);
    cmp_word(M.x86.R_AX, (u16)val);
}
-   M.x86.R_CX -- 1;
+   if (M.x86.mode & SYSMODE_32BIT_REP)
+       M.x86.R_ECX -- 1;
+   else
+       M.x86.R_CX -- 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF) == 0)
        break;
+   if (M.x86.intr & INTR_HALTED)
+       break;
}
M.x86.mode &= ~SYSMODE_PREFIX_REPE;
} else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
    /* REPNE */
-   /* move them until CX is ZERO. */
-   while (M.x86.R_CX != 0) {
+   /* move them until (E)CX is ZERO. */
+   while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        if (M.x86.mode & SYSMODE_PREFIX_DATA) {
            val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
            cmp_long(M.x86.R_EAX,
val);
@@ -2977,10 +3034,15 @@
    val = fetch_data_word_abs(M.x86.R_ES, M.x86.R_DI);
    cmp_word(M.x86.R_AX, (u16)val);
}
-   M.x86.R_CX -- 1;
+   if (M.x86.mode & SYSMODE_32BIT_REP)
+       M.x86.R_ECX -- 1;
+   else
+       M.x86.R_CX -- 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF))
        break;    /* zero flag set means equal */
+   if (M.x86.intr & INTR_HALTED)
+       break;
}
M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
} else {
@@ -3238,9 +3300,9 @@
    DECODE_PRINTF("RET\t");
    imm = fetch_word_imm();
    DECODE_PRINTF2("%x\n", imm);

```

```

- RETURN_TRACE("RET",M.x86.saved_cs,M.x86.saved_ip);
TRACE_AND_STEP();
  M.x86.R_IP = pop_word();
+ RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
  M.x86.R_SP += imm;
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
@@ -3254,9 +3316,9
@@
{
  START_OF_INSTR();
  DECODE_PRINTF("RET\n");
- RETURN_TRACE("RET",M.x86.saved_cs,M.x86.saved_ip);
TRACE_AND_STEP();
  M.x86.R_IP = pop_word();
+ RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
}
@@ -3471,10 +3533,10 @@
  DECODE_PRINTF("RETF\t");
  imm = fetch_word_imm();
  DECODE_PRINTF2("%x\n", imm);
- RETURN_TRACE("RETF",M.x86.saved_cs,M.x86.saved_ip);
TRACE_AND_STEP();
  M.x86.R_IP = pop_word();
  M.x86.R_CS = pop_word();
+ RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
  M.x86.R_SP += imm;
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
@@ -3488,10 +3550,10 @@
{
  START_OF_INSTR();
  DECODE_PRINTF("RETF\n");
- RETURN_TRACE("RETF",M.x86.saved_cs,M.x86.saved_ip);
TRACE_AND_STEP();
  M.x86.R_IP = pop_word();
  M.x86.R_CS = pop_word();
+ RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
}
@@ -4020,8 +4082,11 @@
  ip += (s16) M.x86.R_IP;
  DECODE_PRINTF2("%04x\n", ip);
  TRACE_AND_STEP();
- M.x86.R_CX -= 1;

```

```

- if (M.x86.R_CX != 0 && !ACCESS_FLAG(F_ZF)) /* CX != 0 and !ZF */
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+   M.x86.R_ECX -= 1;
+ else
+   M.x86.R_CX -= 1;
+ if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
!ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and !ZF */
    M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4041,8 +4106,11 @@
    ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
- M.x86.R_CX -= 1;
- if (M.x86.R_CX != 0 && ACCESS_FLAG(F_ZF)) /* CX != 0 and ZF */
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+   M.x86.R_ECX -= 1;
+ else
+   M.x86.R_CX -= 1;
+ if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and ZF */
    M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4062,8 +4130,11 @@
    ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
- M.x86.R_CX -= 1;
- if (M.x86.R_CX != 0)
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+   M.x86.R_ECX -= 1;
+ else
+   M.x86.R_CX -= 1;
+ if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0) /* (E)CX != 0 */
    M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4085,8 +4156,10 @@
    target = (u16)(M.x86.R_IP + offset);
    DECODE_PRINTF2("%x\n", target);
    TRACE_AND_STEP();
- if (M.x86.R_CX == 0)
+ if (M.x86.R_CX == 0) {
    M.x86.R_IP = target;
+ JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " CXZ ");
+ }

```

```

    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -4213,6 +4286,7 @@
    ip = (s16)fetch_word_imm();
    ip += (s16)M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
+   JMP_TRACE(M.x86.saved_cs,
M.x86.saved_ip, M.x86.R_CS, ip, " NEAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = (u16)ip;
    DECODE_CLEAR_SEGOVR();
@@ -4233,6 +4307,7 @@
    cs = fetch_word_imm();
    DECODE_PRINTF2("%04x:", cs);
    DECODE_PRINTF2("%04x\n", ip);
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, cs, ip, " FAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = ip;
    M.x86.R_CS = cs;
@@ -4254,6 +4329,7 @@
    offset = (s8)fetch_byte_imm();
    target = (u16)(M.x86.R_IP + offset);
    DECODE_PRINTF2("%x\n", target);
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, target, " BYTE ");
    TRACE_AND_STEP();
    M.x86.R_IP = target;
    DECODE_CLEAR_SEGOVR();
@@ -4357,6 +4433,8 @@
    DECODE_PRINTF("REPNE\n");
    TRACE_AND_STEP();
    M.x86.mode |= SYSMODE_PREFIX_REPNE;
+   if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+   M.x86.mode |= SYSMODE_32BIT_REP;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -4371,6 +4449,8 @@
    DECODE_PRINTF("REPE\n");
    TRACE_AND_STEP();
    M.x86.mode |= SYSMODE_PREFIX_REPE;
+   if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+   M.x86.mode |= SYSMODE_32BIT_REP;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -5013,12 +5093,14 @@
    break;

```

```

case 4:    /* jmp word ptr ... */
    destval = fetch_data_word(destoffset);
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, destval, " WORD ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    break;
case 5:    /* jmp far ptr ... */
    destval = fetch_data_word(destoffset);
    destval2 = fetch_data_word(destoffset + 2);
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, destval2, destval, " FAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    M.x86.R_CS = destval2;

```

Index: prim_ops.c

=====

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/prim_ops.c,v

retrieving revision

1.1

retrieving revision 1.3

diff -u -u -r1.1 -r1.3

--- prim_ops.c 7 Sep 2007 10:01:21 -0000 1.1

+++ prim_ops.c 16 Jan 2008 14:18:15 -0000 1.3

@@ -1921,7 +1921,7 @@

void imul_long_direct(u32 *res_lo, u32* res_hi,u32 d, u32 s)

{

#ifdef __HAS_LONG_LONG__

- s64 res = (s64)d * (s64)s;

+ s64 res = (s64)(s32)d * (s64)(s32)s;

 *res_lo = (u32)res;

 *res_hi = (u32)(res >> 32);

@@ -2013,7 +2013,7 @@

void mul_long(u32 s)

{

#ifdef __HAS_LONG_LONG__

- u64 res = (u32)M.x86.R_EAX * (u32)s;

+ u64 res = (u64)M.x86.R_EAX * s;

 M.x86.R_EAX = (u32)res;

 M.x86.R_EDX = (u32)(res >> 32);

@@ -2312,16 +2312,15 @@

}

if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {

 /* dont care whether REPE or REPNE */

- /* in until CX is ZERO. */

- u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?

+ /* in until (E)CX is ZERO. */

+ u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?

```

        M.x86.R_ECX : M.x86.R_CX);
-
    while (count--) {
        single_in(size);
        M.x86.R_DI += inc;
    }
    M.x86.R_CX = 0;
-   if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+   if (M.x86.mode & SYSMODE_32BIT_REP) {
        M.x86.R_ECX = 0;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
@@ -2355,15 +2354,15 @@
    }
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* out until CX is ZERO. */
-        u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
+        /* out until (E)CX is ZERO. */
+        u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
            M.x86.R_ECX : M.x86.R_CX);
        while (count--) {
            single_out(size);
            M.x86.R_SI += inc;
        }
        M.x86.R_CX = 0;
-        if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+        if (M.x86.mode & SYSMODE_32BIT_REP) {
            M.x86.R_ECX = 0;
        }
        M.x86.mode
        &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);

```

Index: sys.c

```
=====
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/sys.c,v
```

```
retrieving revision 1.1
```

```
retrieving revision 1.2
```

```
diff -u -u -r1.1 -r1.2
```

```
--- sys.c 7 Sep 2007 10:01:21 -0000 1.1
```

```
+++ sys.c 7 Sep 2007 10:03:13 -0000 1.2
```

```
@@ -45,11 +45,6 @@
```

```
#include <x86emu/regs.h>
```

```
#include "debug.h"
```

```
#include "prim_ops.h"
```

```
ifndef LINUXBIOS_VERSION
```

```
#include "io.h"
```

```
else
```

```
#include <sys/io.h>
```

```

-#endif

#ifdef IN_MODULE
#include "xf86_ansi.h"
@@ -220,7 +215,7 @@
{
DB(if (DEBUG_IO_TRACE())
printk("inb %#04x \n", addr);
- return inb(addr);
+ return 0;
}

/*****
@@ -235,7 +230,7 @@
{
DB(if (DEBUG_IO_TRACE())
printk("inw %#04x \n", addr);
- return inw(addr);
+ return 0;
}

/*****
@@ -250,7 +245,7 @@
{
DB(if (DEBUG_IO_TRACE())
printk("inl %#04x \n", addr);
- return inl(addr);
+ return 0;
}

/*****
@@ -264,7 +259,6 @@
{
DB(if (DEBUG_IO_TRACE())
printk("outb %#02x -> %#04x \n", val, addr);
- outb(val, addr);
return;
}

@@ -279,7 +273,6 @@
{
DB(if (DEBUG_IO_TRACE())
printk("outw %#04x -> %#04x \n", val, addr);
- outw(val, addr);
return;
}

@@ -295,7 +288,6 @@

```

```

DB(if (DEBUG_IO_TRACE())
    printk("outl %#08x -> %#04x \n", val, addr);

- outl(val, addr);
return;
}

```

@@ -405,6 +397,6 @@

```

void X86EMU_setMemBase(void *base, size_t size)
{
- M.mem_base = (int) base;
+ M.mem_base = (unsigned long) base;
  M.mem_size = size;
}

```

Index: include/x86emu/debug.h

=====

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/debug.h,v

retrieving revision 1.1

retrieving revision 1.4

diff -u -u -r1.1 -r1.4

--- include/x86emu/debug.h 7

Sep 2007 10:01:21 -0000 1.1

+++ include/x86emu/debug.h 20 Mar 2008 15:25:27 -0000 1.4

@@ -40,8 +40,6 @@

#ifndef __X86EMU_DEBUG_H

#define __X86EMU_DEBUG_H

//#define DEBUG 0

#undef DEBUG

/*----- Macros and type definitions -----*/

/* checks to be enabled for "runtime" */

@@ -78,6 +76,8 @@

define DEBUG_SYSINT() (M.x86.debug & DEBUG_SYSINT_F)

define DEBUG_TRACECALL() (M.x86.debug & DEBUG_TRACECALL_F)

define DEBUG_TRACECALLREGS() (M.x86.debug & DEBUG_TRACECALL_REGS_F)

+# define DEBUG_TRACEJMP() (M.x86.debug & DEBUG_TRACEJMP_F)

+# define DEBUG_TRACEJMPREGS() (M.x86.debug & DEBUG_TRACEJMP_REGS_F)

define DEBUG_SYS() (M.x86.debug & DEBUG_SYS_F)

define DEBUG_MEM_TRACE() (M.x86.debug & DEBUG_MEM_TRACE_F)

define DEBUG_IO_TRACE() (M.x86.debug & DEBUG_IO_TRACE_F)

@@ -96,6 +96,8 @@

define DEBUG_SYSINT() 0

define DEBUG_TRACECALL() 0

define DEBUG_TRACECALLREGS() 0

+# define DEBUG_TRACEJMP()


```

0
+# define DEBUG_TRACEJMPREGS() 0
# define DEBUG_SYS() 0
# define DEBUG_MEM_TRACE() 0
# define DEBUG_IO_TRACE() 0
@@ -169,14 +171,20 @@
x86emu_dump_regs(); \
if (DEBUG_TRACECALL()) \
    printk("%04x:%04x: CALL %s%04x:%04x\n", u, v, s, w, x);
-# define RETURN_TRACE(n,u,v) \
+# define RETURN_TRACE(u,v,w,x,s) \
if (DEBUG_TRACECALLREGS()) \
    x86emu_dump_regs(); \
if (DEBUG_TRACECALL()) \
- printk("%04x:%04x: %s\n",u,v,n);
+ printk("%04x:%04x: RET %s %04x:%04x\n",u,v,s,w,x);
+# define JMP_TRACE(u,v,w,x,s) \
+ if (DEBUG_TRACEJMPREGS()) \
+     x86emu_dump_regs(); \
+ if (DEBUG_TRACEJMP()) \
+     printk("%04x:%04x: JMP %s%04x:%04x\n", u, v, s, w, x);
#else
# define CALL_TRACE(u,v,w,x,s)
-#
define RETURN_TRACE(n,u,v)
+# define RETURN_TRACE(u,v,w,x,s)
+# define JMP_TRACE(u,v,w,x,s)
#endif

#ifndef DEBUG
Index: include/x86emu/regs.h
=====
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/regs.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/regs.h 7 Sep 2007 10:01:21 -0000 1.1
+++ include/x86emu/regs.h 15 Jan 2008 13:46:40 -0000 1.4
@@ -231,6 +231,9 @@
#define SYSMODE_PREFIX_REPNE 0x00000100
#define SYSMODE_PREFIX_DATA 0x00000200
#define SYSMODE_PREFIX_ADDR 0x00000400
+//phueper: for REP(E|NE) Instructions, we need to decide wether it should be using
+//the 32bit ECX register as or the 16bit CX register as count register
+#define SYSMODE_32BIT_REP 0x00000800
#define SYSMODE_INTR_PENDING 0x10000000
#define SYSMODE_EXTRN_INTR 0x20000000
#define SYSMODE_HALTED 0x40000000

```

```

@@ -250,7 +253,8 @@
    SYSMODE_SEGOVR_GS   |\
    SYSMODE_SEGOVR_SS   |\
    SYSMODE_PREFIX_DATA |\
-   SYSMODE_PREFIX_ADDR)
+   SYSMODE_PREFIX_ADDR  |\
+   SYSMODE_32BIT_REP)

#define INTR_SYNCH      0x1
#define INTR_ASYNCH     0x2
@@ -274,9 +278,9 @@
    */
    u32                mode;
    volatile int        intr; /* mask of pending interrupts */
- int                  debug;
+ volatile int         debug;
#ifdef DEBUG
- int                  check;
+ int                  check;
    u16                saved_ip;
    u16                saved_cs;
    int                 enc_pos;
Index: include/x86emu/x86emu.h
=====
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/x86emu.h,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- include/x86emu/x86emu.h 7 Sep 2007 10:01:21
-0000 1.1
+++ include/x86emu/x86emu.h 19 Oct 2007 08:42:15 -0000 1.3
@@ -47,6 +47,7 @@
#include <console.h>
#define printk(x...) printk(BIOS_DEBUG, x)
#else
+#include <stdio.h>
#define printk printf
#endif

@@ -189,6 +181,8 @@
#define DEBUG_TRACECALL_REGS_F 0x004000
#define DEBUG_DECODE_NOPRINT_F 0x008000
#define DEBUG_SAVE_IP_CS_F    0x010000
+#define DEBUG_TRACEJMP_F     0x020000
+#define DEBUG_TRACEJMP_REGS_F 0x040000
#define DEBUG_SYS_F           (DEBUG_SVC_F|DEBUG_FS_F|DEBUG_PROC_F)

void X86EMU_trace_regs(void);

```

```
@@ -200,5 +194,4 @@
#ifdef __cplusplus
}          /* End of "C" linkage for C++ */
#endif
-
#endif /* __X86EMU_X86EMU_H */
```

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-- Wolfgang Denk

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```
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```

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```

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Full name	SPDX Identifier	OSI Approved	File name	URI
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=====

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---	-----------	---	--------------	---

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# *****/  
#!/bin/bash  
  
#set -x  
#set -e  
  
SVN=`which svn`  
PATCH=`which patch`
```



```

DIFF_FILE=./x86emu_changes.diff

# check wether svn, patch, ... is available...

if [ ! -x $SVN ]; then
    echo "subversion executable not found!"
    exit -1
fi
if [ ! -x $PATCH ]; then
    echo "patch executable not found!"
    exit -1
fi
if [ ! -r $DIFF_FILE ]; then
    echo "diff file $DIFF_FILE not found!"
    exit -1
fi

# download the x86emu sources from LinuxBIOS subversion

#revision known to work...
REV=496

echo "Checking
out x86emu from coreboot-v3 repository revision $REV"
$SVN co svn://coreboot.org/repository/coreboot-v3/util/x86emu -r $REV

echo "Copying files..."

mkdir -p include/x86emu
cp -v x86emu/x86emu/*.c .
cp -v x86emu/x86emu/*.h include/x86emu
cp -v x86emu/include/x86emu/*.h include/x86emu

echo "Removing checkedout subversion director..."

rm -rf x86emu

echo "Patching files..."

$PATCH -p0 < x86emu_changes.diff

echo "done"
exit 0

```

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```
make bin/rtl8139.rom.licence
```

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```

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*

* @(#)mman.h 8.2 (Berkeley) 1/9/95

* \$FreeBSD: src/sys/sys/mman.h,v 1.42 2008/03/28 04:29:27 ps Exp \$

*/

#define TARGET_FREEBSD_MAP_RESERVED0080 0x0080 /* previously misimplemtened MAP_INHERIT */

#define TARGET_FREEBSD_MAP_RESERVED0100 0x0100 /* previously unimplemtened MAP_NOEXTEND

*/

```

#define TARGET_FREEBSD_MAP_STACK    0x0400 /* region grows down, like a stack */
#define
TARGET_FREEBSD_MAP_NOSYNC    0x0800 /* page to but do not sync underlying file */

#define TARGET_FREEBSD_MAP_FLAGMASK    0x1ff7

/*   $NetBSD: mman.h,v 1.42 2008/11/18 22:13:49 ad Exp $   */

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 *
 *   @(#)mman.h    8.2 (Berkeley) 1/9/95
 */
#define TARGET_NETBSD_MAP_INHERIT    0x0080 /* region is retained after exec */
#define TARGET_NETBSD_MAP_TRYFIXED    0x0400 /* attempt
 * hint address, even within break */
#define TARGET_NETBSD_MAP_WIRED    0x0800 /* mlock() mapping when it is established */

#define TARGET_NETBSD_MAP_STACK    0x2000 /* allocated from memory, swap space (stack) */

#define TARGET_NETBSD_MAP_FLAGMASK    0x3ff7

```

/* \$OpenBSD: mman.h,v 1.18 2003/07/21 22:52:19 tedu Exp \$ */

/* \$NetBSD: mman.h,v 1.11 1995/03/26 20:24:23 jtc Exp \$ */

/*-

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*

* @(#)mman.h 8.1 (Berkeley) 6/2/93

*/

#define TARGET_OPENBSD_MAP_INHERIT 0x0080 /* region is retained after exec */

#define TARGET_OPENBSD_MAP_NOEXTEND 0x0100 /* for MAP_FILE, don't change file size */

#define TARGET_OPENBSD_MAP_TRYFIXED 0x0400 /* attempt hint address, even within heap */

#define TARGET_OPENBSD_MAP_FLAGMASK 0x17f7

// XXX

#define TARGET_BSD_MAP_FLAGMASK 0x3ff7

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Default configuration for sparc-bsd-user

/* \$OpenBSD: errno.h,v 1.20 2007/09/03 14:37:52 millert Exp \$ */

/* \$NetBSD: errno.h,v 1.10 1996/01/20 01:33:53 jtc Exp \$ */

/*

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*

* @(#)errno.h 8.5 (Berkeley) 1/21/94

*/

```
#define TARGET_EPERM      1      /* Operation not permitted */
#define TARGET_ENOENT    2      /* No such file or directory */
#define TARGET_ESRCH     3      /* No such process */
#define TARGET_EINTR     4      /* Interrupted system call */
#define TARGET_EIO       5      /* Input/output error */
#define TARGET_ENXIO     6      /* Device not configured */
#define TARGET_E2BIG     7      /* Argument list too long */
#define TARGET_ENOEXEC   8      /* Exec format error */
#define TARGET_EBADF     9      /* Bad file descriptor */
#define TARGET_ECHILD   10     /* No child processes */
#define TARGET_EDEADLK  11     /* Resource deadlock avoided */
                                /* 11 was EAGAIN */
#define
TARGET_ENOMEM          12     /* Cannot allocate memory */
#define TARGET_EACCES   13     /* Permission denied */
#define TARGET_EFAULT   14     /* Bad address */
#define TARGET_ENOTBLK  15     /* Block device required */
#define TARGET_EBUSY    16     /* Device busy */
#define TARGET_EEXIST    17     /* File exists */
#define TARGET_EXDEV     18     /* Cross-device link */
#define TARGET_ENODEV   19     /* Operation not supported by device */
#define TARGET_ENOTDIR  20     /* Not a directory */
#define TARGET_EISDIR   21     /* Is a directory */
#define TARGET_EINVAL   22     /* Invalid argument */
#define TARGET_ENFILE   23     /* Too many open files in system */
#define TARGET_EMFILE   24     /* Too many open files */
#define TARGET_ENOTTY   25     /*
Inappropriate ioctl for device */
#define TARGET_ETXTBSY  26     /* Text file busy */
#define TARGET_EFBIG    27     /* File too large */
#define TARGET_ENOSPC   28     /* No space left on device */
#define TARGET_ESPIPE   29     /* Illegal seek */
#define TARGET_EROFS    30     /* Read-only file system */
#define TARGET_EMLINK   31     /* Too many links */
#define TARGET_EPIPE    32     /* Broken pipe */

/* math software */
#define TARGET_EDOM      33     /* Numerical argument out of domain */
```

```

#define TARGET_ERANGE      34      /* Result too large */

/* non-blocking and interrupt i/o */
#define TARGET_EAGAIN      35      /* Resource temporarily unavailable */
#define TARGET_EWOULDBLOCK EAGAIN  /* Operation would block */
#define TARGET_EINPROGRESS 36      /* Operation now in progress */
#define TARGET_EALREADY
37      /* Operation already in progress */

/* ipc/network software -- argument errors */
#define TARGET_ENOTSOCK 38      /* Socket operation on non-socket */
#define TARGET_EDESTADDRREQ 39      /* Destination address required */
#define TARGET EMSGSIZE 40      /* Message too long */
#define TARGET_EPROTOTYPE 41      /* Protocol wrong type for socket */
#define TARGET_ENOPROTOOPT 42      /* Protocol not available */
#define TARGET_EPROTONOSUPPORT 43      /* Protocol not supported */
#define TARGET_ESOCKTNOSUPPORT 44      /* Socket type not supported */
#define TARGET_EOPNOTSUPP 45      /* Operation not supported */
#define TARGET_EPFNOSUPPORT 46      /* Protocol family not supported */
#define TARGET_EAFNOSUPPORT 47      /* Address family not supported by protocol family */
#define TARGET_EADDRINUSE 48      /* Address already in use */
#define
TARGET_EADDRNOTAVAIL 49      /* Can't assign requested address */

/* ipc/network software -- operational errors */
#define TARGET_ENETDOWN 50      /* Network is down */
#define TARGET_ENETUNREACH 51      /* Network is unreachable */
#define TARGET_ENETRESET 52      /* Network dropped connection on reset */
#define TARGET_ECONNABORTED 53      /* Software caused connection abort */
#define TARGET_ECONNRESET 54      /* Connection reset by peer */
#define TARGET_ENOBUFS 55      /* No buffer space available */
#define TARGET_EISCONN 56      /* Socket is already connected */
#define TARGET_ENOTCONN 57      /* Socket is not connected */
#define TARGET_ESHUTDOWN 58      /* Can't send after socket shutdown */
#define TARGET_ETOOMANYREFS 59      /* Too many references: can't splice */
#define TARGET_ETIMEDOUT 60      /* Operation timed
out */
#define TARGET_ECONNREFUSED 61      /* Connection refused */

#define TARGET_ELOOP      62      /* Too many levels of symbolic links */
#define TARGET_ENAMETOOLONG 63      /* File name too long */

/* should be rearranged */
#define TARGET_EHOSTDOWN 64      /* Host is down */
#define TARGET_EHOSTUNREACH 65      /* No route to host */
#define TARGET_ENOTEMPTY 66      /* Directory not empty */

/* quotas & mush */

```

```

#define TARGET_EPROCLIM 67      /* Too many processes */
#define TARGET_EUSERS      68      /* Too many users */
#define TARGET_EDQUOT      69      /* Disk quota exceeded */

/* Network File System */
#define TARGET_ESTALE      70      /* Stale NFS file handle */
#define TARGET_EREMOTE      71      /* Too many levels of remote in path */
#define TARGET_EBADRPC      72      /* RPC struct is bad */
#define TARGET_ERPCMISMATCH
    73      /* RPC version wrong */
#define TARGET_EPROGUNAVAIL 74      /* RPC prog. not avail */
#define TARGET_EPROGMISMATCH 75      /* Program version wrong */
#define TARGET_EPROCUNAVAIL 76      /* Bad procedure for program */

#define TARGET_ENOLCK      77      /* No locks available */
#define TARGET_ENOSYS      78      /* Function not implemented */

#define TARGET_EFTYPE      79      /* Inappropriate file type or format */
#define TARGET_EAUTH      80      /* Authentication error */
#define TARGET_ENEEDAUTH    81      /* Need authenticator */
#define TARGET_EIPSEC      82      /* IPsec processing failure */
#define TARGET_ENOATTR      83      /* Attribute not found */
#define TARGET_EILSEQ      84      /* Illegal byte sequence */
#define TARGET_ENOMEDIUM    85      /* No medium found */
#define TARGET_EMEDIUMTYPE 86      /* Wrong Medium Type */
#define TARGET_EOVERFLOW    87      /* Conversion overflow */
#define TARGET_ECANCELED    88      /* Operation canceled */
#define TARGET_EIDRM      89      /* Identifier removed */
#define TARGET_ENOMSG      90      /* No message of desired type */
#define TARGET_ELAST      90      /* Must be equal largest errno */

```

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```

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/*

* FreeBSD strace list

*

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```
{ TARGET_FREEBSD_NR__acl_aclcheck_fd, "__acl_aclcheck_fd", "%s(%d, %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR__acl_aclcheck_file, "__acl_aclcheck_file", "%s(\"%s\", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR__acl_aclcheck_link, "__acl_aclcheck_link", "%s(\"%s\", %d, %#x)", NULL, NULL
},
{ TARGET_FREEBSD_NR__acl_delete_fd,
```

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__acl_delete_fd", "%s(%d, %d)", NULL, NULL },
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{ TARGET_FREEBSD_NR__acl_delete_link, "__acl_delete_link", "%s(\"%s\", %d)", NULL, NULL },
{ TARGET_FREEBSD_NR__acl_get_fd, "__acl_get_fd", "%s(%d, %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR__acl_get_file, "__acl_get_file", "%s(\"%s\", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR__acl_get_link, "__acl_get_link", "%s(\"%s\", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR__acl_set_fd, "__acl_set_fd", "%s(%d, %d, %#x)", NULL, NULL },
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{ TARGET_FREEBSD_NR__semctl, "__semctl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR__syscall, "__syscall", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR__sysctl, "__sysctl", NULL, print_sysctl, NULL },
{ TARGET_FREEBSD_NR__umtx_op,
  "_umtx_op", "%s(%#x, %d, %d, %#x, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_accept, "accept", "%s(%d, %#x, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_access, "access", "%s(\"%s\", %o)", NULL, NULL },
{ TARGET_FREEBSD_NR_acct, "acct", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_adjtime, "adjtime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_bind, "bind", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_break, "break", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chdir, "chdir", "%s(\"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_chflags, "chflags", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chmod, "chmod", "%s(\"%s\", %o)", NULL, NULL },
{ TARGET_FREEBSD_NR_chown, "chown", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chroot, "chroot", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_getres, "clock_getres", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_gettime, "clock_gettime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_settime, "clock_settime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_close, "close", "%s(%d)", NULL,
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{ TARGET_FREEBSD_NR_dup, "dup", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_dup2, "dup2", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_eaccess, "eaccess", "%s(\"%s\", %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_execve, "execve", NULL, print_execve, NULL },
{ TARGET_FREEBSD_NR_exit, "exit", "%s(%d)\n", NULL, NULL },
{ TARGET_FREEBSD_NR_extattrctl, "extattrctl", "%s(\"%s\", %d, \"%s\", %d, \"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_delete_fd, "extattr_delete_fd", "%s(%d, %d, \"%s\")", NULL, NULL },
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{ TARGET_FREEBSD_NR_extattr_delete_link, "extattr_delete_link", "%s(\"%s\", %d, \"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_get_fd, "extattr_get_fd", "%s(%d, %d, \"%s\", %#x, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_get_file, "extattr_get_file", "%s(\"%s\", %d, \"%s\", %#x, %d)", NULL, NULL
},
{ TARGET_FREEBSD_NR_extattr_get_file,
  "extattr_get_link", "%s(\"%s\", %d, \"%s\", %#x, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_list_fd, "extattr_list_fd", "%s(%d, %d, %#x, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_list_file, "extattr_list_file", "%s(\"%s\", %d, %#x, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_list_link, "extattr_list_link", "%s(\"%s\", %d, %#x, %d)", NULL, NULL
},

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{ TARGET_FREEBSD_NR_extattr_set_fd, "extattr_set_fd", "%s(%d, %d, \"%s\", %#x, %d)", NULL, NULL },
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{ TARGET_FREEBSD_NR_fchflags, "fchflags", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_fchown, "fchown", "%s(%d,%d,%d)", NULL, NULL },
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{ TARGET_FREEBSD_NR_fexecve, "fexecve", NULL, print_execve, NULL },
{ TARGET_FREEBSD_NR_fhopen, "fhopen", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fhstat, "fhstat", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_flock, "flock", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fork, "fork", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_fpathconf, "fpathconf", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fstat, "fstat", "%s(%d,%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_fstatat, "fstatat", "%s(%d,\"%s\", %#x, %d)", NULL, NULL },
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{ TARGET_FREEBSD_NR_fsync, "fsync", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_futimes, "futimes", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getcontext, "getcontext", "%s(%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_getdirentries, "getdirentries", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_freebsd6_mmap,
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{ TARGET_FREEBSD_NR_getegid, "getegid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_geteuid, "geteuid", "%s()", NULL, NULL },
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{ TARGET_FREEBSD_NR_getpgid, "getpgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getpgrp, "getpgrp", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_getpid, "getpid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_getppid, "getppid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_getpriority, "getpriority", "%s(%#x,%#x)", NULL, NULL },
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{ TARGET_FREEBSD_NR_getsockname, "getsockname", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getsockopt, "getsockopt", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_gettimeofday, "gettimeofday", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getuid, "getuid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_ioctl, "ioctl", NULL, print_ioctl, NULL },
{ TARGET_FREEBSD_NR_issetugid, "issetugid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_kevent, "kevent", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_kill, "kill", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_kqueue, "kqueue", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_ktrace, "ktrace", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_lchown, "lchown", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_link, "link", "%s(\"%s\", \"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_listen,
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{ TARGET_FREEBSD_NR_lpathconf, "lpathconf", "%s(\"%s\", %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_lseek, "lseek", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_lstat, "lstat", "%s(\"%s\", %p)", NULL, NULL },
{ TARGET_FREEBSD_NR_madvise, "madvise", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mincore, "mincore", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_minherit, "minherit", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mkdir, "mkdir", "%s(\"%s\", %o)", NULL, NULL },
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{ TARGET_FREEBSD_NR_mknod, "mknod", "%s(\"%s\", %o, %x)", NULL, NULL },
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{ TARGET_FREEBSD_NR_mmap, "mmap", NULL, NULL, print_syscall_ret_addr },
{ TARGET_FREEBSD_NR_mount, "mount", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mprotect, "mprotect", "%s(%x, %x, %d)", NULL, NULL },
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{ TARGET_FREEBSD_NR_munlockall, "munlockall", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_munmap, "munmap", "%s(%p, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_nanosleep, "nanosleep", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_nfssvc, "nfssvc", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_open, "open", "%s(\"%s\", %x, %o)", NULL, NULL },
{ TARGET_FREEBSD_NR_openat, "openat", "%s(%d, \"%s\", %x, %o)", NULL, NULL },
{ TARGET_FREEBSD_NR_pathconf, "pathconf", "%s(\"%s\", %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_pipe, "pipe", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_poll, "poll", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_pread, "pread", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_preadv, "preadv", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_profil,

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"profil", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_ptrace, "ptrace", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_pwritev, "pwritev", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_quotactl, "quotactl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_read, "read", "%s(%d,%#x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_readlink, "readlink", "%s(\"%s\",%p,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_readv, "readv", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_reboot, "reboot", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_recvfrom, "recvfrom", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_recvmsg, "recvmsg", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rename, "rename", "%s(\"%s\", \"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_revoke, "revoke", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rfork, "rfork", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rmdir, "rmdir", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rtprio_thread, "rtprio_thread", "%s(%d, %d, %p)", NULL, NULL },
{ TARGET_FREEBSD_NR_sbrk,
"sbrk", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sched_yield, "sched_yield", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_select, "select", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_semget, "semget", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_semop, "semop", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sendmsg, "sendmsg", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sendto, "sendto", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setcontext, "setcontext", "%s(%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_setegid, "setegid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR seteuid, "seteuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setgid, "setgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setgroups, "setgroups", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setitimer, "setitimer", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setlogin, "setlogin", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setpgid, "setpgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setpriority, "setpriority", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setregid,
"setregid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setresgid, "setresgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setresuid, "setresuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setreuid, "setreuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setrlimit, "setrlimit", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setsid, "setsid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setsockopt, "setsockopt", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR settimeofday, "settimeofday", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR setuid, "setuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR shmat, "shmat", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR shmctl, "shmctl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR shmdt, "shmdt", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR shmget, "shmget", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR shutdown, "shutdown", NULL, NULL, NULL },

```



```

{ TARGET_FREEBSD_NR_sigaction, "sigaction", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sigaltstack, "sigaltstack", "%s(%p,%p)", NULL, NULL },
{ TARGET_FREEBSD_NR_sigpending,
"sigpending", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sigprocmask, "sigprocmask", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sigreturn, "sigreturn", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sigsuspend, "sigsuspend", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_socket, "socket", "%s(%d,%d,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_socketpair, "socketpair", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sstk, "sstk", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_stat, "stat", "%s(\"%s\",%p)", NULL, NULL },
{ TARGET_FREEBSD_NR_statfs, "statfs", "%s(\"%s\",%p)", NULL, NULL },
{ TARGET_FREEBSD_NR_symlink, "symlink", "%s(\"%s\", \"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_sync, "sync", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sysarch, "sysarch", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_syscall, "syscall", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_thr_create, "thr_create", "%s(%#x, %#x, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_exit, "thr_exit", "%s(%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_kill, "thr_kill", "%s(%d,
%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_kill2, "thr_kill2", "%s(%d, %d, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_new, "thr_new", "%s(%#x, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_self, "thr_self", "%s(%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_set_name, "thr_set_name", "%s(%d, \"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_suspend, "thr_suspend", "%s(%d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_thr_wake, "thr_wake", "%s(%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_truncate, "truncate", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_umask, "umask", "%s(%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_unlink, "unlink", "%s(\"%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_unmount, "unmount", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_utimes, "utimes", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_vfork, "vfork", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_wait4, "wait4", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_write, "write", "%s(%d,%#x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_writev, "writev", "%s(%d,%p,%#x)",
NULL, NULL },

```

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```

```
<signature of Ty Coon>, 1 April 1989
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# *****

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# *
# * Contributors:
# *   IBM Corporation - initial implementation
# *****

ifndef TOP
TOP = $(shell while ! test -e make.rules; do cd .. ; done; pwd)
export TOP
endif
include $(TOP)/make.rules

SUBDIRS=
ifeq ($(SNK_BIOSEMU_APPS), 1)
SUBDIRS += x86emu
endif
CLEANSUBDIRS = $(SUBDIRS)

all :
for subdir in $(SUBDIRS) ; do $(MAKE) -C $$subdir || exit 1 ; done

# Common targets for all subdirectories:
clean distclean depend:
for subdir in $(CLEANSUBDIRS) ; do $(MAKE) -C $$subdir $$@ ; done
# Default configuration for sparc64-bsd-user
# *****

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# *
# * Contributors:
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# *****

ifndef TOP
TOP = $(shell while ! test -e make.rules; do cd .. ; done; pwd)

```

```

export TOP
endif
include $(TOP)/make.rules

ROOTDIR ?= ../..

LDFLAGS =
ASFLAGS = -I./include -Wa,-mregnames

#NOTE: -DDEBUG only needed for debugging/tracing...
CFLAGS = -UDEBUG -m64 -I. -I./include -I./include/x86emu \
-I$(TOP)/clients/net-snk/include -I$(ROOTDIR)/include \
-I$(ROOTDIR)/lib/libc/include -O3 -nostdinc -fno-builtin \
-ffreestanding -Wall -Wno-unused

X86EMU_OBJS =
debug.o decode.o fpu.o ops2.o ops.o prim_ops.o sys.o

%.o: %.S
$(CC) $(ASFLAGS) -c -o $@ $^

%.o: %.c
$(CC) $(CFLAGS) -c -o $@ $^

all: libx86emu.a

libx86emu.a: $(X86EMU_OBJS)
$(AR) -rc $@ $^
$(RANLIB) $@

clean:
$(RM) *.o *.i *.s libx86emu.a

distclean: clean
/* $OpenBSD: syscall.h,v 1.101 2008/03/16 19:43:41 otto Exp $ */

/*
 * System call numbers.
 *
 * created from; OpenBSD: syscalls.master,v 1.90 2008/03/16 19:42:57 otto Exp
 */

#define TARGET_OPENBSD_NR_syscall 0
#define TARGET_OPENBSD_NR_exit 1
#define TARGET_OPENBSD_NR_fork 2
#define TARGET_OPENBSD_NR_read 3
#define TARGET_OPENBSD_NR_write 4
#define TARGET_OPENBSD_NR_open 5

```

```

#define TARGET_OPENBSD_NR_close      6
#define TARGET_OPENBSD_NR_wait4     7
#define TARGET_OPENBSD_NR_link      9
#define TARGET_OPENBSD_NR_unlink    10
#define TARGET_OPENBSD_NR_chdir     12
#define TARGET_OPENBSD_NR_fchdir    13
#define TARGET_OPENBSD_NR_mknod     14
#define TARGET_OPENBSD_NR_chmod     15
#define TARGET_OPENBSD_NR_chown     16
#define TARGET_OPENBSD_NR_break     17
#define TARGET_OPENBSD_NR_getpid    20
#define TARGET_OPENBSD_NR_mount     21
#define TARGET_OPENBSD_NR_unmount   22
#define TARGET_OPENBSD_NR_setuid
    23
#define TARGET_OPENBSD_NR_getuid    24
#define TARGET_OPENBSD_NR_geteuid   25
#define TARGET_OPENBSD_NR_ptrace    26
#define TARGET_OPENBSD_NR_recvmmsg  27
#define TARGET_OPENBSD_NR_sendmsg   28
#define TARGET_OPENBSD_NR_recvfrom  29
#define TARGET_OPENBSD_NR_accept    30
#define TARGET_OPENBSD_NR_getpeername 31
#define TARGET_OPENBSD_NR_getsockname 32
#define TARGET_OPENBSD_NR_access    33
#define TARGET_OPENBSD_NR_chflags   34
#define TARGET_OPENBSD_NR_fchflags  35
#define TARGET_OPENBSD_NR_sync      36
#define TARGET_OPENBSD_NR_kill      37
#define TARGET_OPENBSD_NR_getppid   39
#define TARGET_OPENBSD_NR_dup       41
#define TARGET_OPENBSD_NR_opipe     42
#define TARGET_OPENBSD_NR_getegid   43
#define TARGET_OPENBSD_NR_profil    44
#define TARGET_OPENBSD_NR_ktrace    45
#define TARGET_OPENBSD_NR_sigaction  46
#define TARGET_OPENBSD_NR_getgid    47
#define TARGET_OPENBSD_NR_sigprocmask 48
#define TARGET_OPENBSD_NR_getlogin  49
#define TARGET_OPENBSD_NR_setlogin
    50
#define TARGET_OPENBSD_NR_acct      51
#define TARGET_OPENBSD_NR_sigpending 52
#define TARGET_OPENBSD_NR_osigaltstack 53
#define TARGET_OPENBSD_NR_ioctl     54
#define TARGET_OPENBSD_NR_reboot    55
#define TARGET_OPENBSD_NR_revoke    56
#define TARGET_OPENBSD_NR_symlink   57

```

```

#define TARGET_OPENBSD_NR_readlink 58
#define TARGET_OPENBSD_NR_execve 59
#define TARGET_OPENBSD_NR_umask 60
#define TARGET_OPENBSD_NR_chroot 61
#define TARGET_OPENBSD_NR_vfork 66
#define TARGET_OPENBSD_NR_sbrk 69
#define TARGET_OPENBSD_NR_sstk 70
#define TARGET_OPENBSD_NR_munmap 73
#define TARGET_OPENBSD_NR_mprotect 74
#define TARGET_OPENBSD_NR_madvise 75
#define TARGET_OPENBSD_NR_mincore 78
#define TARGET_OPENBSD_NR_getgroups 79
#define TARGET_OPENBSD_NR_setgroups 80
#define TARGET_OPENBSD_NR_getpgrp 81
#define TARGET_OPENBSD_NR_setpgid 82
#define TARGET_OPENBSD_NR_setitimer 83
#define TARGET_OPENBSD_NR_getitimer
86
#define TARGET_OPENBSD_NR_dup2 90
#define TARGET_OPENBSD_NR_fcntl 92
#define TARGET_OPENBSD_NR_select 93
#define TARGET_OPENBSD_NR_fsync 95
#define TARGET_OPENBSD_NR_setpriority 96
#define TARGET_OPENBSD_NR_socket 97
#define TARGET_OPENBSD_NR_connect 98
#define TARGET_OPENBSD_NR_getpriority 100
#define TARGET_OPENBSD_NR_sigreturn 103
#define TARGET_OPENBSD_NR_bind 104
#define TARGET_OPENBSD_NR_setsockopt 105
#define TARGET_OPENBSD_NR_listen 106
#define TARGET_OPENBSD_NR_sigsuspend 111
#define TARGET_OPENBSD_NR_gettimeofday 116
#define TARGET_OPENBSD_NR_getrusage 117
#define TARGET_OPENBSD_NR_getsockopt 118
#define TARGET_OPENBSD_NR_readv 120
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#define TARGET_OPENBSD_NR_lchown   254
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#define TARGET_OPENBSD_NR_fhopen   264
#define TARGET_OPENBSD_NR_preadv   267
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#define TARGET_OPENBSD_NR_mlockall 271
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#define TARGET_OPENBSD_NR_getpeereid 273
#define TARGET_OPENBSD_NR_getresuid 281
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#define TARGET_OPENBSD_NR_getresgid 283
#define TARGET_OPENBSD_NR_setresgid
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#define TARGET_OPENBSD_NR_closefrom 287
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#define TARGET_OPENBSD_NR_fstat    292
#define TARGET_OPENBSD_NR_lstat    293
#define TARGET_OPENBSD_NR_fhstat   294
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#define TARGET_OPENBSD_NR_msgctl   297
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#define TARGET_OPENBSD_NR_getthrid 299
#define TARGET_OPENBSD_NR_thrslp   300
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#define TARGET_OPENBSD_NR_threxit  302
#define TARGET_OPENBSD_NR_thrsigdivert 303
#define TARGET_OPENBSD_NR___getcwd 304
#define TARGET_OPENBSD_NR_adjfreq  305
#define TARGET_OPENBSD_NR_getfsstat 306
#define TARGET_OPENBSD_NR_statfs   307
#define TARGET_OPENBSD_NR_fstatfs  308
#define TARGET_OPENBSD_NR_fhstatfs
    309

```

```

/* syscall flags from machine/trap.h */

```

/* \$OpenBSD: trap.h,v 1.4 2008/07/04 22:04:37 kettenis Exp \$ */

/* \$NetBSD: trap.h,v 1.4 1999/06/07 05:28:04 eeh Exp \$ */

/*

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*

*/

#define TARGET_OPENBSD_SYSCALL_G2RFLAG 0x400 /* on success, return to %g2 rather than npc */

#define TARGET_OPENBSD_SYSCALL_G7RFLAG 0x800 /* use %g7 as above (deprecated) */

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Default configuration for x86_64-bsd-user

1.27 open-ldap 2.4.46

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1.28 gawk 4.2.1

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Ty Coon, President of Vice

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Version 3, 29 June 2007

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The

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1.29 libtool 2.4.6

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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Version 3, 29 June 2007

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Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer

can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand

ready to extend this provision to those domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The

"System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

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THE BASIC LIBRARY FUNCTIONS

Written by: Philip Hazel
Email local part: ph10
Email domain: cam.ac.uk

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The following individuals contributed in part to the Network Time Protocol Distribution

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1. [1]Takao Abe <takao_abe@xurb.jp> Clock driver for JJY receivers
2. [2]Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller
3. [3]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
4. [4]Viraj Bais <vbais@mailman1.intel.com> and [5]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
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6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
9. [10]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support
10. [11]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in sntp
11. [12]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support

12. [13]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
13. [14]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
14. [15]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
15. [16]Sven Dietrich <sven_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
16. [17]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port
17. [18]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
18. [19]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
19. [20]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
20. [21]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite
21. [22]Claas Hilbrecht <neoclock4x@linum.com>
NeoClock4X clock driver
22. [23]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
23. [24]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
24. [25]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
25. [26]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
26. [27]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or [28]<H.Lambermont@chello.nl> ntpsweep
27. [29]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
28. [30]Frank Kardel [31]<kardel (at) ntp (dot) org> PARSE <GENERIC> (driver 14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
29. [32]Johannes Maximilian Kuehn <kuehn@ntp.org> Rewrote sntp to comply with NTPv4 specification, ntpq saveconfig
30. [33]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
31. [34]Dave Katz <dkatz@cisco.com> RS/6000 AIX port

32. [35]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
33. [36]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
34. [37]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
35. [38]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
36. [39]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
37. [40]David L. Mills <mills@udel.edu> Version 4 foundation, precision kernel; clock drivers: 1, 3, 4, 6, 7, 11, 13, 18, 19, 22, 36
38. [41]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
39. [42]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
40. [43]Tom Moore <tmoore@fieval.daytonoh.ncr.com> i386 svr4 port

41. [44]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
 42. [45]Derek Mulcahy <derek@toybox.demon.co.uk> and [46]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
 43. [47]Rob Neal <neal@ntp.org>
Bancomm refclock and config/parse code
maintenance
 44. [48]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de>
monitoring/trap scripts, statistics file handling
 45. [49]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
 46. [50]Wilfredo Snchez <wsanchez@apple.com> added support for
NetInfo
 47. [51]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
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space on the stuff in the html/pic/ subdirectory
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 50. [54]Michael Shields <shields@tembel.org> USNO clock driver
 51. [55]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock
driver
 52. [56]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure
makeover, various other bits (see the ChangeLog)
 53. [57]Kenneth Stone <ken@sdd.hp.com> HP-UX port
 54. [58]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast
support
 55. [59]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock
driver
 56. [60]Brian Utterback <brian.utterback@oracle.com> General codebase,
Solaris issues
 57. [61]Loganaden Velvindron <loganaden@gmail.com> Sandboxing
(libseccomp) support
 58. [62]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic
TrueTime clock driver
 59. [63]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and
validated HTML documents according to the HTML DTD
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References

1. mailto:%20takao_abe@xurb.jp
2. mailto:%20mark_andrews@isc.org
3. <mailto:%20altmeier@atlsoft.de>
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17. [mailto:%20dundas@salt.jpl.nasa.gov](mailto:dundas@salt.jpl.nasa.gov)
18. [mailto:%20duwe@immd4.informatik.uni-erlangen.de](mailto:duwe@immd4.informatik.uni-erlangen.de)
19. [mailto:%20dennis@mrbill.canet.ca](mailto:dennis@mrbill.canet.ca)
20. [mailto:%20jhay@icomtek.csir.co.za](mailto:jhay@icomtek.csir.co.za)
21. [mailto:%20davehart@davehart.com](mailto:davehart@davehart.com)
22. [mailto:%20neoclock4x@linum.com](mailto:neoclock4x@linum.com)
23. [mailto:%20glenn@herald.usask.ca](mailto:glenn@herald.usask.ca)
24. [mailto:%20iglesias@uci.edu](mailto:iglesias@uci.edu)
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27. [mailto:%20Hans.Lambermont@nl.origin-it.com](mailto:Hans.Lambermont@nl.origin-it.com)
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39. [mailto:%20mayer@ntp.org](mailto:mayer@ntp.org)
40. [mailto:%20mills@udel.edu](mailto:mills@udel.edu)
41. [mailto:%20moeller@gwdgv1.dnet.gwdg.de](mailto:moeller@gwdgv1.dnet.gwdg.de)
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45. [mailto:%20derek@toybox.demon.co.uk](mailto:derek@toybox.demon.co.uk)
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52. [mailto:%20jack@innovativeinternet.com](mailto:jack@innovativeinternet.com)
53. [mailto:%20schnitz@unipress.com](mailto:schnitz@unipress.com)
54. [mailto:%20shields@tembel.org](mailto:shields@tembel.org)
55. [mailto:%20pebbles.jpl.nasa.gov](mailto:pebbles.jpl.nasa.gov)
56. [mailto:%20harlan@pfcs.com](mailto:harlan@pfcs.com)
57. [mailto:%20ken@sdd.hp.com](mailto:ken@sdd.hp.com)
58. [mailto:%20ajit@ee.udel.edu](mailto:ajit@ee.udel.edu)
59. [mailto:%20tsuruoka@nc.fukuoka-u.ac.jp](mailto:tsuruoka@nc.fukuoka-u.ac.jp)

```
60. mailto:%20brian.utterback@oracle.com
61. mailto:%20loganaden@gmail.com
62. mailto:%20vixie@vix.com
63. mailto:%20Ulrich.Windl@rz.uni-regensburg.de
/* -*- Mode: Text -*- */
```

```
copyright = {
  date = "1992-2017";
  owner = "The University of Delaware and Network Time Foundation";
  eaddr = "http://bugs.ntp.org, bugs@ntp.org";
  type = ntp;
};
```

```
long-opts;
config-header = config.h;
environrc;
no-misuse-usage;
version-proc = ntpOptionPrintVersion;
```

```
version      = `
eval VERSION=\`sed -e 's/.*\\|\\|' -e 's/^\\.*/' < ../sntp/m4/version.m4\`
[ -z "${VERSION}" ] && echo "Cannot determine VERSION" && kill -TERM $AG_pid
echo $VERSION`;
```

```
version-value = "; /* Don't use -v as a shortcut for --version */
```

```
/*
* HMS: man pages already have this, and texi pages can use the
* values in sntp/include/version.texi .
*/
```

```
// doc-sub = {
//   sub-name = version;
//   sub-text = 's/#VERSION#/<<version>>/g';
// };
```

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```
<!DOCTYPE HTML PUBLIC "-//W3C//DTD HTML 4.01 Transitional//EN">
```

```
<html>
```

```
<head>
```

```
<meta http-equiv="content-type" content="text/html; charset=iso-8859-1">
```

```
<title>Copyright Notice</title>
```

```
<!-- Changed by: Harlan Stenn, 10-Mar-2014 -->
```

```
<link href="scripts/style.css" type="text/css" rel="stylesheet">
```

```
</head>
```

```
<body>
```

```
<h3>Copyright Notice</h3>
```

```
 "Clone me," says Dolly sheepishly.
```

```
<p>Last update:
```

```
<!-- #BeginDate format:En2m -->2-Jan-2017 11:58<!-- #EndDate -->
```

```
UTC</p>
```

```
<br clear="left">
```

```
</p>
```

```
<hr>
```

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```
<pre>
```

```
*****
```

```
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```

```
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```
1992-2015 *
```

```
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*                                                                     *
*****
```

</pre>

<p>Content

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<pre>

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*****
```

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```

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*
*****
```

```
*****
```

</pre>

<p>The following individuals contributed in part to the Network Time Protocol Distribution Version 4 and are acknowledged as authors of this work.</p>

Takao Abe <takao_abe@xurb.jp> Clock driver for JJY receivers

Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller

Bernd Altmeier <altmeier@atsoft.de> hopf Elektronik serial line and PCI-bus devices

Viraj Bais <vbais@mailman1.intel.com> and Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5

Michael Barone <michael,barone@lmco.com> GPSVME fixes

Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option

Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.

Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver

Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support

Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in sntp

Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support

Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)

Steve Clift <clift@ml.csiro.au> OMEGA clock driver

Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration

Sven Dietrich <sven_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.

John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port

Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port

Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119

John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing

Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite

Claas Hilbrecht <neoclock4x@linum.com> NeoClock4X clock driver

Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver

Mike Iglesias <iglesias@uci.edu> DEC Alpha port

Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port

Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul

Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or <H.Lambermont@chello.nl> ntpswEEP

Poul-Henning

Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)

Frank Kardel kardel (at) ntp (dot) org> PARSE <GENERIC> (driver 14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling

Johannes Maximilian Kuehn <kuehn@ntp.org> Rewrote <tt>sntp</tt> to comply with NTPv4 specification, <tt>ntp saveconfig</tt>

William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications

Dave Katz <dkatz@cisco.com> RS/6000 AIX port

Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver

George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port

Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication

Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305

Danny Mayer <mayer@ntp.org> Network I/O, Windows Port, Code Maintenance

David L. Mills <mills@udel.edu> Version 4 foundation, precision kernel; clock drivers: 1, 3, 4, 6, 7, 11, 13, 18, 19, 22, 36

Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port

Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility

Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port

Kamal A Mostafa <kamal@whence.com> SCO OpenServer port

Derek Mulcahy <derek@toybox.demon.co.uk> and Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver

Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code maintenance

Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling

Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port

Wilfredo Sßnchez <wsanchez@apple.com> added support for NetInfo

Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules

Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory

Ray Schnitzler <schnitz@unipress.com> Unixware1 port

Michael Shields <shields@tembel.org> USNO clock driver

Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock

driver
Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure
makeover, various other bits (see the ChangeLog)
Kenneth Stone <ken@sdd.hp.com> HP-UX port
Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast
support
Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-
u.ac.jp>TRAK clock driver
Brian Utterback <brian.utterback@oracle.com>
General codebase, Solaris issues
Loganaden Velvindron <loganaden@gmail.com>
Sandboxing (libseccomp) support
Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic
TrueTime clock driver
Ulrich Windl <Ulrich.Windl@rz.uni-
regensburg.de> corrected and validated HTML documents according to the HTML DTD

<hr>
</body>
</html>
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1.34 rsync 3.1.3

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1.35 apache-http-server 2.4.34

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```
<?xml version="1.0" encoding="ISO-8859-1"?>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<html xmlns="http://www.w3.org/1999/xhtml" lang="en" xml:lang="en"><head>
<meta content="text/html; charset=ISO-8859-1" http-equiv="Content-Type" />
<!--
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
X
    This file is generated from xml source: DO NOT EDIT
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
X
    -->
<title>The Apache License, Version 2.0 - Apache HTTP Server Version 2.4</title>
<link href="/style/css/manual.css" rel="stylesheet" media="all" type="text/css" title="Main stylesheet" />
<link href="/style/css/manual-loose-100pc.css" rel="alternate stylesheet" media="all" type="text/css" title="No
Sidebar - Default font size" />
<link href="/style/css/manual-print.css" rel="stylesheet" media="print" type="text/css" /><link rel="stylesheet"
type="text/css" href="/style/css/prettify.css"
/>
<script src="/style/scripts/prettify.min.js" type="text/javascript">
</script>

<link href="/images/favicon.ico" rel="shortcut icon" /></head>
<body id="manual-page" class="no-sidebar"><div id="page-header">
<p class="menu"><a href="/mod/">Modules</a> | <a href="/mod/directives.html">Directives</a> | <a
href="http://wiki.apache.org/httpd/FAQ">FAQ</a> | <a href="/glossary.html">Glossary</a> | <a
```

```
href="/sitemap.html">Sitemap</a></p>
<p class="apache">Apache HTTP Server Version 2.4</p>
</div>
<div class="up"><a href="/"></a></div>
<div id="path">
<a href="http://www.apache.org">Apache</a> > <a href="http://httpd.apache.org">HTTP Server</a> > <a
href="http://httpd.apache.org/docs/">Documentation</a> > <a href="/">Version 2.4</a></div><div id="page-
content"><div id="preamble"><h1>The Apache License, Version 2.0</h1>
<div class="toplang">
<p><span>Available Languages:
</span><a href="/en/license.html" title="English">&nbsp;en&nbsp;</a></p>
</div>
```

```
<p class="centered">Apache License<br />
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<a href="http://www.apache.org/licenses/">http://www.apache.org/licenses/</a><br /><br />
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<ol>
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[!\[\]\(0d5ec72f61334709c3fc9450209b754f_img.jpg\)](#page-header)

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```

channel, #httpd, on Freenode, or sent to our <a href="http://httpd.apache.org/lists.html">mailing lists</a>.</div>
<script type="text/javascript"><!--><![CDATA[//><!--
var comments_shortname = 'httpd';
var comments_identifier = 'http://httpd.apache.org/docs/2.4/license.html';
(function(w, d) {
  if (w.location.hostname.toLowerCase() == "httpd.apache.org") {
    d.write('<div id="comments_thread"></div>');
    var s = d.createElement('script');
    s.type = 'text/javascript';
    s.async = true;
    s.src = 'https://comments.apache.org/show_comments.lua?site=' + comments_shortname + '&page=' +
comments_identifier;

    (d.getElementsByTagName('head')[0] || d.getElementsByTagName('body')[0]).appendChild(s);
  }
  else {
    d.write('<div id="comments_thread">Comments are disabled for this page at the moment.</div>');
  }
})(window, document);
//--><![></script></div><div id="footer">
<p class="apache">Copyright 2018 The Apache Software Foundation.<br />Licensed under the <a
href="http://www.apache.org/licenses/LICENSE-2.0">Apache License, Version 2.0</a>.</p>
<p class="menu"><a href="/mod/">Modules</a> | <a href="/mod/directives.html">Directives</a> | <a
href="http://wiki.apache.org/httpd/FAQ">FAQ</a> | <a href="/glossary.html">Glossary</a> | <a
href="/sitemap.html">Sitemap</a></p></div><script type="text/javascript"><!--><![CDATA[//><!--
if (typeof(prettyPrint) !== 'undefined') {
  prettyPrint();
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</body></html>
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* 605 E. Springfield, Champaign, IL 61820
* httpd@ncsa.uiuc.edu
*
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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:

Mike Belshe (mbelshe@netscape.com)

Michael Campanella (campanella@stevms.enet.dec.com)

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@subheading END OF TERMS AND CONDITIONS

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smallexample
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@var{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice
@end smallexample
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```
/* _setjmp is implemented in setjmp.S */
```

```
/* setjmp is implemented in setjmp.S */
```

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```
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```

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```
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```

```
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Ty Coon, President of Vice
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1.38 gzip 1.9

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1.39 python 2.7.15

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History and License

History of the software

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <https://www.cwi.nl/>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National

Research Initiatives (CNRI, see <https://www.cnri.reston.va.us/>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see <http://www.zope.com/>). In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Mersenne Twister

The `:mod:`_random`` module includes code based on a download from <http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program
for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`
or `init_by_array(init_key, key_length)`.

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Sockets

The `:mod:`socket`` module uses the functions `:func:`getaddrinfo``, and `:func:`getnameinfo``, which are coded in separate source files from the WIDE Project, <http://www.wide.ad.jp/>. ::

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MD5 message digest algorithm

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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

<http://www.ietf.org/rfc/rfc1321.txt>

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of

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The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.
1999-11-04 lpd Edited comments slightly for automatic TOC extraction.
1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5); added conditionalization for C++ compilation from Martin Purschke <purschke@bnl.gov>.
1999-05-03 lpd Original version.

Asynchronous socket services

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Modified by Jack Jansen, CWI, July 1995:

- Use `binascii` module to do the actual line-by-line conversion between `ascii` and `binary`. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

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zlib

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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1.6.1	1.6	2001	CNRI	yes (2)
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2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
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A C-program
for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`
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Sockets

The `:mod:`socket`` module uses the functions, `:func:`getaddrinfo``, and `:func:`getnameinfo``, which are coded in separate source files from the WIDE Project, <http://www.wide.ad.jp/>. ::

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MD5 message digest algorithm

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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

<http://www.ietf.org/rfc/rfc1321.txt>

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of

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The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

- 2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.
- 1999-11-04 lpd Edited comments slightly for automatic TOC extraction.
- 1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5); added conditionalization for C++ compilation from Martin Purschke <purschke@bnl.gov>.
- 1999-05-03 lpd Original version.

Asynchronous socket services

The `:mod:`asynchat`` and `:mod:`asyncore`` modules contain the following notice::

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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

XML Remote Procedure Calls

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strtod
and dtoa

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1.40 zlib 1.2.3

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

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1.41 net-snmp 5.8

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#

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# All lines in this file are patterns, including the comment lines; this
# means that e.g. `FTL.TXT' matches all files that have this string in
# the file name (including the path relative to the current directory,
# always starting with `./').
#
# Don't put empty lines into this file!
#
.gitignore
#
builds/unix/pkg.m4
#
docs/FTL.TXT
docs/GPLv2.TXT
#
include/freetype/internal/fthash.h
#
src/base/fthash.c
src/base/md5.c
src/base/md5.h
#
src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
src/bdf/rules.mk
#
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
src/pcf/pcfutil.c
src/pcf/pcfutil.h
src/pcf/README
src/pcf/rules.mk
#
src/gzip/adler32.c
src/gzip/infblock.c
src/gzip/infblock.h
src/gzip/infcodes.c
src/gzip/infcodes.h
```

```
src/gzip/infixed.h
src/gzip/inflate.c
src/gzip/inftrees.c
src/gzip/inftrees.h
src/gzip/infutil.c
src/gzip/infutil.h
src/gzip/zconf.h
src/gzip/zlib.h
src/gzip/zutil.c
src/gzip/zutil.h
#
src/tools/apinames.c
src/tools/ftrandom/ftrandom.c
#
#
EOF
```

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1.43 bind 9.11.4

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* `configure.ac`, `Makefile.am`: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

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* `atf-c/ui.c`: The `format_paragraph` and `format_text` functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

* `atf-c++/detail/io.hpp`, `atf-c++/detail/io.cpp`, `atf-c++/detail/io_test.cpp`: These files were derived from the `file_handle`, `systembuf`, `pipe` and `pistream` classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* `admin/check-style.sh`,
`admin/check-style-common.awk`,
`admin/check-style-cpp.awk`, `admin/check-style-shell.awk`: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

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=====
vim: filetype=text:textwidth=75:expandtab:shiftwidth=2:softtabstop=2
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    <xsl:value-of select="$isc.copyright.leader"/>
    <xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
    <xsl:text>&#10;</xsl:text>
    <xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
    <xsl:if test="translate($rest, '&#9;&#32;', '')">

      <xsl:call-template name="isc.copyright.format">
        <xsl:with-param name="text" select="$rest"/>
      </xsl:call-template>
    </xsl:if>
  </xsl:template>

  <xsl:variable name="isc.copyright.text">
    <xsl:text>
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      License, v. 2.0. If a copy of the MPL was not distributed with this
      file, You can obtain one at http://mozilla.org/MPL/2.0/.
    </xsl:text>
  </xsl:variable>

  <xsl:variable name="isc.copyright">
    <xsl:call-template name="isc.copyright.format">
      <xsl:with-param name="text">
        <xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">
          <xsl:text>Copyright (C) </xsl:text>
          <xsl:call-template name="copyright.years">
            <xsl:with-param name="years" select="year"/>
          </xsl:call-template>
          <xsl:text> </xsl:text>
          <xsl:value-of select="holder"/>
          <xsl:value-of select="$isc.copyright.breakline"/>
          <xsl:text>&#10;</xsl:text>
        </xsl:for-each>
        <xsl:value-of
          select="$isc.copyright.text"/>
      </xsl:with-param>
    </xsl:call-template>
  </xsl:variable>
</xsl:stylesheet>

<!--

```


- Local variables:

- mode: sgml

- End:

-->

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1.44 zlib 1.2.8

1.44.1 Available under license :

```
/* zlib.h -- interface of the 'zlib' general purpose compression library
   version 1.2.11, January 15th, 2017
```

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1.45 cpio 2.12

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1.46 zlib 1.2.5

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1.50 berkeley-db 6.0.30

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*

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* them if you wish), that you receive source code or can get it if you
* want it,

that you can change the software or use pieces of it in new
* free programs, and that you know you can do these things.

*

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* with two steps: (1) assert copyright on the software, and (2) offer
* you this License which gives you legal permission to copy, distribute
* and/or modify the software.

*

* A secondary benefit of defending all users' freedom is that
* improvements made in alternate versions of the program, if they
* receive widespread use, become available for other developers to
* incorporate. Many developers of free software are heartened and
* encouraged by the resulting cooperation. However, in the case of
* software used on network servers, this result may fail to come about.
* The GNU General Public License permits making a modified version and
* letting the public access it on a server without ever releasing its
* source code to the public.

*

* The GNU Affero General Public License
is designed specifically to
* ensure that, in such cases, the modified source code becomes available
* to the community. It requires the operator of a network server to
* provide the source code of the modified version running there to the
* users of that server. Therefore, public use of a modified version, on
* a publicly accessible server, gives the public access to the source
* code of the modified version.

*

* An older license, called the Affero General Public License and
* published by Affero, was designed to accomplish similar goals. This is
* a different license, not a version of the Affero GPL, but Affero has
* released a new version of the Affero GPL which permits relicensing under
* this license.

*

* The precise terms and conditions for copying, distribution and
* modification follow.

*

TERMS AND CONDITIONS

*

* 0. Definitions.

*

* "This License" refers to version 3 of the GNU Affero General Public License.

*

* "Copyright"

also means copyright-like laws that apply to other kinds of

* works, such as semiconductor masks.

*

* "The Program" refers to any copyrightable work licensed under this

* License. Each licensee is addressed as "you". "Licensees" and

* "recipients" may be individuals or organizations.

*

* To "modify" a work means to copy from or adapt all or part of the work

* in a fashion requiring copyright permission, other than the making of an

* exact copy. The resulting work is called a "modified version" of the

* earlier work or a work "based on" the earlier work.

*

* A "covered work" means either the unmodified Program or a work based

* on the Program.

*

* To "propagate" a work means to do anything with it that, without

* permission, would make you directly or secondarily liable for

* infringement under applicable copyright law, except executing it on a

* computer or modifying a private copy. Propagation includes copying,

* distribution (with or without modification),

making available to the

* public, and in some countries other activities as well.

*

* To "convey" a work means any kind of propagation that enables other

* parties to make or receive copies. Mere interaction with a user through

* a computer network, with no transfer of a copy, is not conveying.

*

* An interactive user interface displays "Appropriate Legal Notices"

* to the extent that it includes a convenient and prominently visible

* feature that (1) displays an appropriate copyright notice, and (2)

* tells the user that there is no warranty for the work (except to the

* extent that warranties are provided), that licensees may convey the

* work under this License, and how to view a copy of this License. If
* the interface presents a list of user commands or options, such as a
* menu, a prominent item in the list meets this criterion.

*

* 1. Source Code.

*

* The "source code" for a work means the preferred form of the work
* for making modifications to it. "Object
code" means any non-source
* form of a work.

*

* A "Standard Interface" means an interface that either is an official
* standard defined by a recognized standards body, or, in the case of
* interfaces specified for a particular programming language, one that
* is widely used among developers working in that language.

*

* The "System Libraries" of an executable work include anything, other
* than the work as a whole, that (a) is included in the normal form of
* packaging a Major Component, but which is not part of that Major
* Component, and (b) serves only to enable use of the work with that
* Major Component, or to implement a Standard Interface for which an
* implementation is available to the public in source code form. A
* "Major Component", in this context, means a major essential component
* (kernel, window system, and so on) of the specific operating system
* (if any) on which the executable work runs, or a compiler used to
* produce the work, or an object
code interpreter used to run it.

*

* The "Corresponding Source" for a work in object code form means all
* the source code needed to generate, install, and (for an executable
* work) run the object code and to modify the work, including scripts to
* control those activities. However, it does not include the work's
* System Libraries, or general-purpose tools or generally available free
* programs which are used unmodified in performing those activities but
* which are not part of the work. For example, Corresponding Source
* includes interface definition files associated with source files for
* the work, and the source code for shared libraries and dynamically
* linked subprograms that the work is specifically designed to require,
* such as by intimate data communication or control flow between those
* subprograms and other parts of the work.

*

* The Corresponding Source need not include anything that users
* can regenerate automatically from other parts of the
Corresponding
* Source.

*

* The Corresponding Source for a work in source code form is that
* same work.

*

* 2. Basic Permissions.

*

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* with facilities for running those works, provided that you comply
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*

* Conveying under any other circumstances is permitted solely under
* the conditions stated below. Sublicensing is not allowed; section 10
* makes it unnecessary.

*

* 3. Protecting Users' Legal Rights From Anti-Circumvention Law.

*

* No covered work shall be deemed part of an effective technological
* measure under any applicable law fulfilling obligations under article
* 11 of the WIPO copyright treaty adopted on 20 December 1996, or
* similar laws prohibiting or restricting circumvention of such
* measures.

*

* When you convey a covered work, you waive any legal power to forbid
* circumvention of technological
measures to the extent such circumvention
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* the covered work, and you disclaim any intention to limit operation or
* modification of the work as a means of enforcing, against the work's
* users, your or third parties' legal rights to forbid circumvention of
* technological measures.

*

* 4. Conveying Verbatim Copies.

*

* You may convey verbatim copies of the Program's source code as you
* receive it, in any medium, provided that you conspicuously and

- * appropriately publish on each copy an appropriate copyright notice;
- * keep intact all notices stating that this License and any
- * non-permissive terms added in accord with section 7 apply to the code;
- * keep intact all notices of the absence of any warranty; and give all
- * recipients a copy of this License along with the Program.

*

- * You may charge any price or no price for each copy that you convey,
- * and you may offer support or warranty protection for a fee.

*

* 5. Conveying Modified Source Versions.

*

- * You may convey a work based on the Program, or the modifications to
- * produce it from the Program, in the form of source code under the
- * terms of section 4, provided that you also meet all of these conditions:

*

- * a) The work must carry prominent notices stating that you modified
- * it, and giving a relevant date.

*

- * b) The work must carry prominent notices stating that it is
- * released under this License and any conditions added under section
- * 7. This requirement modifies the requirement in section 4 to
- * "keep intact all notices".

*

- * c) You must license the entire work, as a whole, under this
- * License to anyone who comes into possession of a copy. This
- * License will therefore apply, along with any applicable section 7
- * additional terms, to the whole of the work, and all its parts,
- * regardless of how they are packaged. This License

gives no

- * permission to license the work in any other way, but it does not
- * invalidate such permission if you have separately received it.

*

- * d) If the work has interactive user interfaces, each must display
- * Appropriate Legal Notices; however, if the Program has interactive
- * interfaces that do not display Appropriate Legal Notices, your
- * work need not make them do so.

*

- * A compilation of a covered work with other separate and independent
- * works, which are not by their nature extensions of the covered work,
- * and which are not combined with it such as to form a larger program,
- * in or on a volume of a storage or distribution medium, is called an
- * "aggregate" if the compilation and its resulting copyright are not
- * used to limit the access or legal rights of the compilation's users
- * beyond what the individual works permit. Inclusion of a covered work
- * in an aggregate does not cause this License to apply to the other
- * parts of the

aggregate.

*

* 6. Conveying Non-Source Forms.

*

* You may convey a covered work in object code form under the terms
* of sections 4 and 5, provided that you also convey the
* machine-readable Corresponding Source under the terms of this License,
* in one of these ways:

*

* a) Convey the object code in, or embodied in, a physical product
* (including a physical distribution medium), accompanied by the
* Corresponding Source fixed on a durable physical medium
* customarily used for software interchange.

*

* b) Convey the object code in, or embodied in, a physical product
* (including a physical distribution medium), accompanied by a
* written offer, valid for at least three years and valid for as
* long as you offer spare parts or customer support for that product
* model, to give anyone who possesses the object code either (1) a
* copy of the Corresponding Source for all the software in the
* product that is covered

by this License, on a durable physical

* medium customarily used for software interchange, for a price no
* more than your reasonable cost of physically performing this
* conveying of source, or (2) access to copy the
* Corresponding Source from a network server at no charge.

*

* c) Convey individual copies of the object code with a copy of the
* written offer to provide the Corresponding Source. This
* alternative is allowed only occasionally and noncommercially, and
* only if you received the object code with such an offer, in accord
* with subsection 6b.

*

* d) Convey the object code by offering access from a designated
* place (gratis or for a charge), and offer equivalent access to the
* Corresponding Source in the same way through the same place at no
* further charge. You need not require recipients to copy the
* Corresponding Source along with the object code. If the place to
* copy the object

code is a network server, the Corresponding Source

* may be on a different server (operated by you or a third party)
* that supports equivalent copying facilities, provided you maintain
* clear directions next to the object code saying where to find the
* Corresponding Source. Regardless of what server hosts the
* Corresponding Source, you remain obligated to ensure that it is
* available for as long as needed to satisfy these requirements.

*

* e) Convey the object code using peer-to-peer transmission, provided

- * you inform other peers where the object code and Corresponding
- * Source of the work are being offered to the general public at no
- * charge under subsection 6d.
- *
- * A separable portion of the object code, whose source code is excluded
- * from the Corresponding Source as a System Library, need not be
- * included in conveying the object code work.
- *
- * A "User Product" is either (1) a "consumer product", which means any
- * tangible personal property which is normally used for personal, family,
- * or household purposes, or (2) anything designed or sold for incorporation
- * into a dwelling. In determining whether a product is a consumer product,
- * doubtful cases shall be resolved in favor of coverage. For a particular
- * product received by a particular user, "normally used" refers to a
- * typical or common use of that class of product, regardless of the status
- * of the particular user or of the way in which the particular user
- * actually uses, or expects or is expected to use, the product. A product
- * is a consumer product regardless of whether the product has substantial
- * commercial, industrial or non-consumer uses, unless such uses represent
- * the only significant mode of use of the product.
- *
- * "Installation Information" for a User Product means any methods,
- * procedures, authorization keys, or other information required to install
- * and execute modified versions of a covered work
- in that User Product from
- * a modified version of its Corresponding Source. The information must
- * suffice to ensure that the continued functioning of the modified object
- * code is in no case prevented or interfered with solely because
- * modification has been made.
- *
- * If you convey an object code work under this section in, or with, or
- * specifically for use in, a User Product, and the conveying occurs as
- * part of a transaction in which the right of possession and use of the
- * User Product is transferred to the recipient in perpetuity or for a
- * fixed term (regardless of how the transaction is characterized), the
- * Corresponding Source conveyed under this section must be accompanied
- * by the Installation Information. But this requirement does not apply
- * if neither you nor any third party retains the ability to install
- * modified object code on the User Product (for example, the work has
- * been installed in ROM).
- *
- * The requirement to provide Installation
- Information does not include a
- * requirement to continue to provide support service, warranty, or updates
- * for a work that has been modified or installed by the recipient, or for
- * the User Product in which it has been modified or installed. Access to a
- * network may be denied when the modification itself materially and
- * adversely affects the operation of the network or violates the rules and

- * protocols for communication across the network.
- *
- * Corresponding Source conveyed, and Installation Information provided,
- * in accord with this section must be in a format that is publicly
- * documented (and with an implementation available to the public in
- * source code form), and must require no special password or key for
- * unpacking, reading or copying.
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*

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*

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*

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<title>Berkeley DB: Berkeley DB Product License</title>
<meta name="description" content="Berkeley DB: An embedded database programmatic toolkit.">
<meta name="keywords"
content="embedded,database,programmatic,toolkit,btree,hash,hashing,transaction,transactions,locking,logging,access
method,access methods,Java,C,C++">
</head>
<body bgcolor=white>
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</body>
</html>

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This package was debianized by Sam Clegg <samo@debian.org> on Tue, 25 Jul 2006 11:43:45 +0100.

It was downloaded from <<http://www.ch-werner.de/sqliteodbc/>>

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```
<?xml version="1.0" encoding="UTF-8" standalone="no"?>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Transitional//EN"
"http://www.w3.org/TR/xhtml1/DTD/xhtml1-transitional.dtd">
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
  <meta http-equiv="Content-Type" content="text/html; charset=UTF-8" />
  <title>License Change</title>
  <link rel="stylesheet" href="gettingStarted.css" type="text/css" />
  <meta name="generator" content="DocBook XSL Stylesheets V1.73.2" />
  <link rel="start" href="index.html" title="Berkeley DB Installation and Build Guide" />
  <link rel="up" href="upgrade_60_toc.html" title="Chapter9. Upgrading Berkeley DB 11.2.5.3 applications to Berkeley DB 12.1.6.0" />
  <link rel="prev" href="compare_func.html" title="Comparison Function Signature Change" />
```

```

<link rel="next" href="sqlite_ver60.html" title="Updated SQLite Version" />
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<div xmlns="" class="navheader">
<div class="libver">
<p>Library Version 12.1.6.0</p>
</div>
<table width="100%" summary="Navigation header">
<tr>
<th colspan="3" align="center">License Change</th>
</tr>
<tr>
<td width="20%" align="left"><a accesskey="p" href="compare_func.html">Prev</a></td>
<th width="60%" align="center">Chapter9.
Upgrading Berkeley DB 11.2.5.3 applications to Berkeley DB 12.1.6.0
</th>
<td width="20%" align="right"><a accesskey="n" href="sqlite_ver60.html">Next</a></td>
</tr>
</table>
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</div>
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<div class="titlepage">
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</div>
</div>
</div>
<p>
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will continue to be distributed under the Sleepycat license.
</p>
</div>
<div class="navfooter">
<hr />
<table width="100%" summary="Navigation footer">
<tr>
<td width="40%" align="left"><a accesskey="p" href="compare_func.html">Prev</a></td>
<td width="20%" align="center">

```

```

        <a accesskey="u" href="upgrade_60_toc.html">Up</a>
    </td>
    <td width="40%" align="right"><a accesskey="n" href="sqlite_ver60.html">Next</a></td>
</tr>
<tr>
    <td width="40%"
align="left" valign="top">Comparison Function Signature Change</td>
    <td width="20%" align="center">
        <a accesskey="h" href="index.html">Home</a>
    </td>
    <td width="40%" align="right" valign="top">Updated SQLite Version</td>
</tr>
</table>
</div>
</body>
</html>

```

1.51 parted 3.2

1.51.1 Available under license :

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```
@c Local Variables:  
@c ispell-local-pdict: "ispell-dict"  
@c End:
```

1.52 classmate 1.0.0

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Other developers who have contributed code are:

* Brian Langel

1.53 kerberos 1.16

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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
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lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
lib/libgss/g_delete_sec_context.c
lib/libgss/g_dsp_name.c
lib/libgss/g_dsp_status.c
lib/libgss/g_dup_name.c
lib/libgss/g_exp_sec_context.c
lib/libgss/g_export_name.c
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lib/libgss/g_imp_name.c
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lib/libgss/g_init_sec_context.c
lib/libgss/g_initialize.c
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```
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"http://www.w3.org/TR/xhtml1/DTD/xhtml1-transitional.dtd">
```

```
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
  <meta http-equiv="Content-Type" content="text/html; charset=utf-8" />
  <title>MIT Kerberos License information &#8212; MIT Kerberos Documentation</title>
  <link rel="stylesheet" href="_static/agogo.css" type="text/css" />
  <link rel="stylesheet" href="_static/pygments.css" type="text/css" />
  <link rel="stylesheet" href="_static/kerb.css" type="text/css" />
  <script type="text/javascript">
    var DOCUMENTATION_OPTIONS = {
      URL_ROOT:    './',
      VERSION:    '1.17',
      COLLAPSE_INDEX: false,
      FILE_SUFFIX: '.html',
      HAS_SOURCE:  true,
```

```

SOURCELINK_SUFFIX: '.txt'
};
</script>
<script type="text/javascript" src="_static/jquery.js"></script>
<script type="text/javascript" src="_static/underscore.js"></script>
<script type="text/javascript"
src="_static/doctools.js"></script>
<link rel="author" title="About these documents" href="about.html" />
<link rel="index" title="Index" href="genindex.html" />
<link rel="search" title="Search" href="search.html" />
<link rel="copyright" title="Copyright" href="copyright.html" />
<link rel="next" title="Copyright" href="copyright.html" />
<link rel="prev" title="MIT Kerberos features" href="mitK5features.html" />
</head>
<body>
<div class="header-wrapper">
  <div class="header">

    <h1><a href="index.html">MIT Kerberos Documentation</a></h1>

    <div class="rel">

      <a href="index.html" title="Full Table of Contents"
        accesskey="C">Contents</a> |
      <a href="mitK5features.html" title="MIT Kerberos features"
        accesskey="P">previous</a> |
      <a href="copyright.html" title="Copyright"
        accesskey="N">next</a> |
      <a href="genindex.html"
title="General Index"
        accesskey="I">index</a> |
      <a href="search.html" title="Enter search criteria"
        accesskey="S">Search</a> |
      <a href="mailto:krb5-bugs@mit.edu?subject=Documentation__MIT Kerberos License
information">feedback</a>
    </div>
  </div>
</div>

<div class="content-wrapper">
  <div class="content">
    <div class="document">

      <div class="documentwrapper">
        <div class="bodywrapper">
          <div class="body" role="main">

```

```
<div class="section" id="mit-kerberos-license-information">
<span id="mitk5license"></span><h1>MIT Kerberos License information<a class="headerlink" href="#mit-
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`gssapi_err_generic`
`et`

`lib/gssapi/mechglue`
`g_accept_sec_context`
`c`

`lib/gssapi/mechglue`
`g_acquire_cred`
`c`

`lib/gssapi/mechglue`
`g_canon_name``c`

`lib/gssapi/mechglue`
`g_compare_name`
`c`

`lib/gssapi/mechglue`
`g_context_time`
`c`

`lib/gssapi/mechglue`
`g_delete_sec_context`
`c`

`lib/gssapi/mechglue`
`g_dsp_name`
`c`

`lib/gssapi/mechglue`
`g_dsp_status`
`c`

`lib/gssapi/mechglue`
`g_dup_name`
`c`

`lib/gssapi/mechglue`
`g_exp_sec_context`
`c`

`lib/gssapi`


```
class="n">kdb_log</span><span class="o">.</span><span class="n">c</span>
<span class="n">lib</span><span class="o"></span><span class="n">kdb</span><span class="o"></span><span class="n">kdb_log</span><span class="o">.</span><span class="n">h</span>
<span class="n">lib</span><span class="o"></span><span class="n">krb5</span><span class="o"></span><span class="n">error_tables</span><span class="o"></span><span class="n">kdb5_err</span><span class="o">.</span><span class="n">et</span>
<span class="n">kprop</span><span class="o"></span><span class="n">kpropd_rpc</span><span class="o">.</span><span class="n">c</span>
<span class="n">kprop</span><span class="o"></span><span class="n">kproplog</span><span class="o">.</span><span class="n">c</span>
</pre></div>
```

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```
src/plugins/preauth/spake/edwards25519.c
```

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```
Documentation</a></li>
<li
class="toctree-l1"><a class="reference internal" href="resources.html">Resources</a></li>
</ul>
```

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  <input type="submit" value="Go" />
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  <input type="hidden" name="area" value="default" />
</form>
</div>
<div class="clearer"></div>
</div>
</div>
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  <div class="footer" >
    <div class="right" ><i>Release: 1.17</i><br />
      &copy; <a href="copyright.html">Copyright</a> 1985-2019, MIT.
    </div>
    <div class="left">

      <a href="index.html" title="Full Table of Contents"
        >Contents</a> |
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        >previous</a> |
      <a href="copyright.html" title="Copyright"
        >next</a> |
      <a href="genindex.html" title="General Index"
        >index</a> |
      <a href="search.html" title="Enter search criteria"
        >Search</a> |
      <a href="mailto:krb5-bugs@mit.edu?subject=Documentation__MIT Kerberos License
information">feedback</a>
    </div>
  </div>
</div>
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</html>
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.. parsed-literal::

```
lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
```

lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:

.. parsed-literal::

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c

kprop/kproplog.c

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  <title>Copyright &#8212; MIT Kerberos Documentation</title>
  <link rel="stylesheet" href="_static/agogo.css" type="text/css" />
  <link rel="stylesheet" href="_static/pygments.css" type="text/css" />
  <link rel="stylesheet" href="_static/kerb.css" type="text/css" />
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      FILE_SUFFIX: '.html',
      HAS_SOURCE: true,
      SOURCELINK_SUFFIX: '.txt'
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    </div>
  </div>
</div>

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  <div class="content">
    <div class="document">

      <div class="documentwrapper">
        <div class="bodywrapper">
          <div class="body" role="main">

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              <h1>Copyright<a class="headerlink" href="#copyright" title="Permalink to this headline"></a></h1>
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Copyright

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<li class="toctree-l1">Resources

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<input type="submit" value="Go" />

<input type="hidden" name="check_keywords" value="yes" />

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</form>

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- lib/gssapi/mechglue/g_accept_sec_context.c
- lib/gssapi/mechglue/g_acquire_cred.c
- lib/gssapi/mechglue/g_canon_name.c
- lib/gssapi/mechglue/g_compare_name.c
- lib/gssapi/mechglue/g_context_time.c
- lib/gssapi/mechglue/g_delete_sec_context.c
- lib/gssapi/mechglue/g_dsp_name.c
- lib/gssapi/mechglue/g_dsp_status.c
- lib/gssapi/mechglue/g_dup_name.c
- lib/gssapi/mechglue/g_exp_sec_context.c
- lib/gssapi/mechglue/g_export_name.c
- lib/gssapi/mechglue/g_glue.c
- lib/gssapi/mechglue/g_imp_name.c
- lib/gssapi/mechglue/g_imp_sec_context.c
- lib/gssapi/mechglue/g_init_sec_context.c
- lib/gssapi/mechglue/g_initialize.c
- lib/gssapi/mechglue/g_inquire_context.c
- lib/gssapi/mechglue/g_inquire_cred.c
- lib/gssapi/mechglue/g_inquire_names.c
- lib/gssapi/mechglue/g_process_context.c
- lib/gssapi/mechglue/g_rel_buffer.c
- lib/gssapi/mechglue/g_rel_cred.c
- lib/gssapi/mechglue/g_rel_name.c
- lib/gssapi/mechglue/g_rel_oid_set.c
- lib/gssapi/mechglue/g_seal.c
- lib/gssapi/mechglue/g_sign.c
- lib/gssapi/mechglue/g_store_cred.c
- lib/gssapi/mechglue/g_unseal.c
- lib/gssapi/mechglue/g_userok.c
- lib/gssapi/mechglue/g_utils.c

lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation,
including
the following new or changed files:

include/ipropr_hdr.h
kadmin/server/iproprd_svc.c
lib/kdb/ipropr.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c
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```
import os
```

```
import sys
```

```
import re
```

```
def warn(fname, ln, msg):
```

```
    print '%s: %d: %s' % (fname, ln + 1, msg)
```

```
def indicates_license(line):
```

```
    return 'Copyright' in line or 'COPYRIGHT' in line or 'License' in line
```

```
# Check a comment for boilerplate violations. Return true if the comment
```

```
# is a license statement.
```

```
def check_comment(comment, fname, ln, code_seen, nonlicense_seen):
```



```

text_seen = False
is_license = False
for line in comment:
    if not is_license and indicates_license(line):
        is_license = True
        if text_seen:
            warn(fname, ln, 'License begins after first
line of comment')
        elif code_seen:
            warn(fname, ln, 'License after code')
        elif nonlicense_seen:
            warn(fname, ln, 'License after non-license comments')
        break
    # DB2 licenses start with '/*-' and we don't want to change them.
    if line != " and line != '-':
        text_seen = True
return is_license

```

```

def check_file(lines, fname):
    # Skip emacs mode line if present.
    ln = 0
    if '-*- mode: c;' in lines[ln]:
        ln += 1

    # Check filename comment if present.
    m = re.match(r'^\* ([^ ]*)( - .*)? \*/', lines[ln])
    if m:
        if m.group(1) != fname:
            warn(fname, ln, 'Wrong filename in comment')
        ln += 1

    # Scan for license statements.
    in_comment = False
    code_seen = False
    nonlicense_seen = False
    for line in lines[ln:]:
        # Strip out whitespace and comments contained within a line.
        if not in_comment:
            line = re.sub(r'^\*.*?\*/',
", line)
            line = line.strip()

        if not in_comment and '/*' in line:
            (line, sep, comment_part) = line.partition('/*')
            comment = [comment_part.strip()]
            comment_starts_at = ln
            in_comment = True
        elif in_comment and '*/' not in line:

```

```

        comment.append(line.lstrip('*').rstrip())
    elif in_comment:
        (comment_part, sep, line) = line.partition('*/')
        comment.append(comment_part.strip())
        is_license = check_comment(comment, fname, comment_starts_at,
                                   code_seen, nonlicense_seen)
        nonlicense_seen = nonlicense_seen or not is_license
        in_comment = False
    elif line.strip() != "":
        code_seen = True

    ln += 1

for fname in sys.argv[1:]:
    if fname.startswith('./'):
        fname = fname[2:]
    f = open(fname)
    lines = f.readlines()
    f.close()
    check_file(lines, fname)
/* -*- mode: c; c-basic-offset: 4; indent-tabs-mode: nil -*- */
/*

```

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

@item

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@enumerate a

@item

Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable ``work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood

that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

@item

Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

@item

Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

@item

If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

@end enumerate

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@item

You may place library facilities that are a work based on the

Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

@enumerate a

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@item

Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

@end enumerate

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@subheading END OF TERMS AND CONDITIONS

@page

@subheading How to Apply These Terms to Your New Libraries

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@smallexample

@var{one line to give the library's name and an idea of what it does.}

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smallexample

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@smallexample

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@var{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice
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@end enumerate

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@ifinfo

@center

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@iftex

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@end iftex

@ifinfo

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@page

@heading Appendix: How to Apply These Terms to Your New Programs

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@smallexample

@var{one line to give the program's name

and a brief idea of what it does.}

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@end smallexample

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@smallexample

Gnomovision version

69, Copyright (C) @var{year} @var{name of author}

Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.

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under certain conditions; type `show c' for details.

@end smallexample

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@example

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@var{signature of Ty Coon}, 1 April 1989

Ty Coon, President of Vice

@end example

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Version 2.1, February 1999

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means

all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form

under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.
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6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work

under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for

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1.56 libdaemon 0.14

1.56.1 Available under license :

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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

- b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.
- c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
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1.58 json-c 0.13.1

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1.59 sudo 1.8.23

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The following list of people, sorted by last name, have contributed code or patches to this implementation of sudo since I began maintaining it in 1993. This list is known to be incomplete--if you believe you should be listed, please send a note to sudo@sudo.ws.

Ackeret, Matt
Adler, Mark
Allbery, Russ
Anderson, Jamie
Andrew, Nick
Andric, Dimitry
Barron, Danny
Bates, Tom
Behan, Zdenk
Bellis, Ray
Benali, Elias
Beverly, Jamie
Boardman, Spider

Bostley, P.J.
Bowes, Keith
Boyce, Keith Garry
Brantley, Michael
Braun, Rob
Bezina, Pavel
Brooks, Piete
Brown, Jerry
Burr, Michael E
Burton, Ross
Bussjaeger, Andreas
Calvin, Gary
Campbell, Aaron
Chazelas, Stephane
ek, Vtzslav
Coleman, Chris
Corzine, Deven T.
Cusack, Frank
Dai, Wei
Dill, David
Earickson, Jeff
Eckhardt, Drew
Edgington, Ben
Esipovich, Marc
Espie, Marc
Faigon, Ariel
Farrell, Brian
Fobes, Steve
Frysinger, Mike
G., Daniel Richard
Gailly, Jean-loup
Gelman, Stephen
Gerraty, Simon J.
Graber, Stephane
Guillory, B.
Hayman, Randy M.
Henke, Joachim
Hideaki, Yoshifuji
Hieb, Dave
Holloway, Nick
Hoover, Adam
Hunter, Michael T.
Hutchings, Ben
Irrgang, Eric
Jackson, Brian
Jackson, John R.
Jackson, Richard L., Jr.
Janssen, Mark

Jepeway, Chris
Jorge, Joel Pele
Juhani, Timo
Kikuchi, Ayamura
Kadow, Kevin
Kasal, Stepan
Kienenberger, Mike
King, Dale
King, Michael
Klyachkin, Andrey
Knoble, Jim
Knox, Tim
Komarnitsky, Alek O.
Kondrashov, Nikolai
Kopeek, Daniel
Kranenburg, Paul
Krause, David
Lakin, Eric
Larsen, Case
Levin, Dmitry V.
Libby, Kendall
Lobbess, Phillip E.
McIntyre, Jason
MacKenzie, David J.
McLaughlin, Tom
Makey, Jeff
Marchionna, Michael D.
Markham, Paul
Martinian, Emin
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Michael
Michael, David
Miller, Todd C.
Minier, Loc
Moffat, Darren
Moldung, Jan Thomas
Morris, Charles
Mueller, Andreas
Mller, Dworkin
Nieusma, Jeff
Nikitser, Peter A.
Nussel, Ludwig
Ouellet, Jean-Philippe
Paquet, Eric
Paradis, Chantal
Percival, Ted
Perera, Andres
Peron, Christian S.J.

Peschel, Aaron
Peslyak, Alexander
Peterson, Toby
Petten, Diego Elio
Pickett, Joel
Plotnick, Alex
de Raadt, Theo
Rasch, Gudleik
Reid, Steve
Richards, Matt
Rossum, Guido van
Rouillard, John P.
Rowe, William A., Jr.
Roy, Alain
Ruusame, Elan
Ryabinkin, Eygene
Sato, Yuichi
Snchez, Wilfredo
Saucier, Jean-Francois
Schoenfeld, Patrick
Schuring, Arno
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Scott, Dougal
Sieger, Nick
Simon, Thor Lancelot
Slemko, Marc
Smith, Andy
Sobrado, Igor
Soulen, Steven
Spangler, Aaron
Spradling, Cloyce D.
Stier, Matthew
Stoekmann, Tobias
Street, Russell
Stritzky, Tilo
Stroucken, Michael
Tarrall, Robert
Thomas, Matthew
Todd, Giles
Toft, Martin
Torek, Chris
Tucker, Darren
Uhl, Robert
Uzel, Petr
Valery, Reznic
Van Dinter, Theo
Venckus, Martynas
de Vries, Maarten

Wagner, Klaus
Walsh, Dan
Warburton, John
Webb, Kirk
Wetzel, Timm
Wieringen, Marco van
Wilk, Jakub
Winiger, Gary
Wood, David
Zacarias, Gustavo
Zolnowsky, John

The following people have worked to translate sudo into other languages:

Blttermann, Mario
Bogusz, Jakub
Buo-ren, Lin
Casagrande, Milo
Castro, Felipe
Cho, Seong-ho
Chornoivan, Yuri
Diguez, Francisco
Fontenelle, Rafael
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Hamasaki, Takeshi
Hamming, Peter
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Keeci, Mehmet
Koir, Klemen
Kozlov, Yuri
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Psa, Petr
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Qun, Trn Ngc
Rasmussen, Sebastian
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Spingos, Dimitris
Taniguchi, Yasuaki
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Uranga, Mikel Olasagasti
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1.60 net-tools 1.60

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```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

```
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it under the terms of the GNU General Public License as published by  
the Free Software Foundation; either version 2 of  
the License, or  
(at your option) any later version.
```

```
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Also add information on how to contact you by electronic and paper mail.

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```
Gnomovision version 69, Copyright (C) 19yy name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
```

The hypothetical commands ``show w'` and ``show c'` should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than ``show w'` and ``show c'`; they could even be mouse-clicks or menu items--whatever suits your program.

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```
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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1.61 libjpeg 6b

1.61.1 Notifications :

This software is based in part on the work of the Independent JPEG Group.

1.61.2 Available under license :

The Independent JPEG Group's JPEG software

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This distribution contains the sixth public release of the Independent JPEG Group's free JPEG software. You are welcome to redistribute this software and to use it for any purpose, subject to the conditions under LEGAL ISSUES, below.

Serious users of this software (particularly those incorporating it into larger programs) should contact IJG at jpeg-info@uunet.uu.net to be added to our electronic mailing list. Mailing list members are notified of updates and have a chance to participate in technical discussions, etc.

This software is the work of Tom Lane, Philip Gladstone, Jim Boucher, Lee Crocker, Julian Minguillon, Luis Ortiz, George Phillips, Davide Rossi, Guido Vollbeding, Ge' Weijers, and other members of the Independent JPEG Group.

IJG is not affiliated with the official ISO JPEG standards committee.

DOCUMENTATION ROADMAP

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This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:

install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran,
 rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info
 as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.

Programmer and internal documentation:

libjpeg.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.

filelist.doc Road map of IJG files.
coderrules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW

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This package contains C software to implement JPEG image compression and decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images. JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included

"jpegtran", a utility for lossless transcoding between different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

We welcome the use of this software as a component of commercial products. No royalty is required, but we do ask for an acknowledgement in product documentation, as described under LEGAL ISSUES.

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The Unix configuration script "configure" was produced with GNU Autoconf. It is copyright by the Free Software Foundation but is freely distributable. The same holds for its supporting scripts (config.guess, config.sub, ltconfig, ltmain.sh). Another support script, install-sh, is copyright by M.I.T. but is also freely distributable.

It appears that the arithmetic coding option of the JPEG spec is covered by patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot legally be used without obtaining one or more licenses. For this reason, support for arithmetic coding has been removed from the free JPEG software. (Since arithmetic coding provides only a marginal gain over the unpatented Huffman mode, it is unlikely that very many implementations will support it.) So far as we are aware, there are no patent restrictions on the remaining code.

The IJG distribution formerly included code to read and write GIF files.

To avoid entanglement with the Unisys LZW patent, GIF reading support has been removed altogether, and the GIF writer has been simplified to produce "uncompressed GIFs". This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

We are required to state that

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REFERENCES

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We highly recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is available at <ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz>. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T

Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US\$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1 and draft DIS 10918-2). This is by far the most complete exposition of JPEG in existence, and we highly recommend it.

The JPEG standard itself is not available electronically; you must order a

paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.)

In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of 1992, ANSI was charging \$95 for Part 1 and \$47 for Part 2, plus 7% shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department
C-Cube Microsystems, Inc.
1778 McCarthy Blvd.
Milpitas, CA 95035
phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at <ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz>. There is also a plain text version at <ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz>, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from <ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz>.

The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from <ftp.sgi.com> or from <ftp://ftp.uu.net/graphics/jpeg/>. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from <ftp://ftp.sgi.com/graphics/tiff/>.

ARCHIVE LOCATIONS

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The "official" archive site for this software is ftp.uu.net (Internet address 192.48.96.9). The most recent released version can always be found there in directory graphics/jpeg. This particular version will be archived as ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only ftp.uu.net is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the ftp.uu.net release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups comp.graphics.misc, news.answers, and other groups. It is available on the World Wide Web at <http://www.faqs.org/faqs/jpeg-faq/> and other news.answers archive sites, including the official news.answers archive at rtfm.mit.edu: <ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/>. If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body
send usenet/news.answers/jpeg-faq/part1
send usenet/news.answers/jpeg-faq/part2

RELATED SOFTWARE

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Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBMPLUS software, which provides many useful operations on PPM-format image files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest

version is distributed by the NetPBM group, and is available from numerous sites, notably <ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/>. Unfortunately PBMPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from <ftp://havefun.stanford.edu/pub/jpeg/>. This program is designed for research and experimentation rather than production use; it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS

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Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read. (For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force

convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO

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The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.62 gmp 6.1.2

1.62.1 Available under license :

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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively state the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the program's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

If the program does terminal interaction, make it output a short notice like this when it starts in an interactive mode:

```
<program> Copyright (C) <year> <name of author>  
This program comes with ABSOLUTELY NO WARRANTY; for details type `show w'.  
This is free software, and you are welcome to redistribute it  
under certain conditions; type `show c' for details.
```

The hypothetical commands `show w' and `show c' should show the appropriate parts of the General Public License. Of course, your program's commands might be different; for a GUI interface, you would use an "about box".

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Version 3, 29 June 2007

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Version 3, 29 June 2007

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```

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From dso/aix/dso.c:

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From strings/apr_strnatcmp.c,
include/apr_strings.h:

strnatcmp.c -- Perform 'natural order' comparisons of strings in C.
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From
strings/apr_snprintf.c:

- *
- * cvt - IEEE floating point formatting routines.
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- *

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1.64 mod-security 2.9.0

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1.68 mime-pull 1.8

1.69 libpcap 1.8.1

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1.72 ncurses 6.1

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1.82 less 530

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The

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1.83 libusb 1.0.22

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Version 2.1, February 1999

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package cmd

```
func initGpl2() {
```

```
    Licenses["gpl2"] = License{
```

```
        Name:      "GNU General Public License 2.0",
```

```
        PossibleMatches: []string{"gpl2", "gnu gpl2", "gplv2"},
```

```
        Header: `
```

```
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```
`  
}  
}
```

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*/

```
// Package legal provides in-process storage for compiled-in licenses.
package legal
```

```
var licenses []string
```

```
// RegisterLicense stores the license text.
// It doesn't check whether the text was already present.
```

```
func RegisterLicense(text string) {
    licenses = append(licenses, text)
    return
}
```

```
// Licenses returns a slice of the licenses.
```

```
func Licenses() []string {
    return licenses
}
```

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package cmd

```
func initBsdClause2() {
```

```
    Licenses["freebsd"] = License{
```

```
        Name: "Simplified BSD License",
```

```
        PossibleMatches: []string{"freebsd", "simpbsd", "simple bsd", "2-clause bsd",  
            "2 clause bsd", "simplified bsd license"},
```

```
        Header: `All rights reserved.
```

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*/

package legal

```
import (  
    "testing"  
)
```

```
func TestRegisterLicense(t *testing.T) {  
    initial := len(licenses)  
    RegisterLicense("dummy")  
    if initial+1 != len(licenses) {  
        t.Fatal("didn't add a license")  
    }  
}
```

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package cmd

```
func initMit() {
```

```
    Licenses["mit"] = License{
```

```
        Name:      "MIT License",
```

```
        PossibleMatches: []string{"mit"},
```

```
        Header: `
```

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`
}
}

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```
package cmd
```

```
func initBsdClause3() {
```

```
    Licenses["bsd"] = License{
```

```
        Name:      "NewBSD",
```

```
        PossibleMatches: []string{"bsd", "newbsd", "3 clause bsd", "3-clause bsd"},
```

```
        Header: `All rights reserved.
```

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```
package cmd
```

```
func initApache2() {
```

```
    Licenses["apache"] = License{
```

```
        Name:      "Apache 2.0",
```

```
        PossibleMatches: []string{"apache", "apache20", "apache 2.0", "apache2.0", "apache-2.0"},
```

```
        Header: `
```

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Text: `
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        Version 2.0, January 2004
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}
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mgo - MongoDB driver for Go

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Paul Borman <borman@google.com>

package cmd

```
func initLgpl() {
    Licenses["lgpl"] = License{
        Name: "GNU Lesser General Public License",
        PossibleMatches: []string{"lgpl", "lesser gpl", "gnu lgpl"},
        Header: `
```

```
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the Free Software Foundation, either version 3 of the License, or
(at your option) any later version.
```

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```
Text: `          GNU LESSER GENERAL PUBLIC LICENSE
          Version 3, 29 June 2007
```

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package cmd

```
func initGpl3() {
```

```
    Licenses["gpl3"] = License{
```

```
        Name:      "GNU General Public License 3.0",
```

```
        PossibleMatches: []string{"gpl3", "gplv3", "gpl", "gnu gpl3", "gnu gpl"},
```

```
        Header: `
```

```
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```

```
it under the terms of the GNU General Public License as published by
```

```
the
```

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```
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```
`  
}
```

```
}  
package cmd
```

```
func initAgpl() {  
Licenses["agpl"] = License{  
Name: "GNU Affero General Public License",  
PossibleMatches: []string{"agpl", "affero gpl", "gnu agpl"},  
Header: `  
This program is free software: you can redistribute it and/or modify  
it under the terms of the GNU Affero General Public License as published by  
the Free Software Foundation, either version 3 of the License, or  
(at your option) any later version.
```

```
`  
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```
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package cmd

import (
    "strings"
    "time"

    "github.com/spf13/viper"
)

// Licenses contains all possible licenses a user can choose from.
var Licenses = make(map[string]License)

// License represents a software license agreement, containing the Name of
// the license, its possible matches (on the command line as given to cobra),
// the
// header to be used with each file on the file's creating, and the text
// of the license
type License struct {
    Name      string // The type of license in use
    PossibleMatches []string // Similar names to guess
    Text      string // License text data
    Header    string // License header for source files
}

func init() {
    // Allows a user to not use a license.
    Licenses["none"] = License{"None", []string{"none", "false"}, "", ""}

    initApache2()
    initMit()
}

```

```

initBsdClause3()
initBsdClause2()
initGpl2()
initGpl3()
initLgpl()
initAgpl()
}

// getLicense returns license specified by user in flag or in config.
// If user didn't specify the license, it returns Apache License 2.0.
//
// TODO: Inspect project for existing license
func getLicense() License {
    // If explicitly flagged, use that.
    if userLicense != "" {
        return findLicense(userLicense)
    }

    // If user wants to have custom license, use that.
    if viper.IsSet("license.header") || viper.IsSet("license.text")
    {
        return License{Header: viper.GetString("license.header"),
            Text: viper.GetString("license.text")}
    }

    // If user wants to have built-in license, use that.
    if viper.IsSet("license") {
        return findLicense(viper.GetString("license"))
    }

    // If user didn't set any license, use Apache 2.0 by default.
    return Licenses["apache"]
}

func copyrightLine() string {
    author := viper.GetString("author")

    year := viper.GetString("year") // For tests.
    if year == "" {
        year = time.Now().Format("2006")
    }

    return "Copyright " + year + " " + author
}

// findLicense looks for License object of built-in licenses.
// If it didn't find license, then the app will be terminated and
// error will be printed.

```

```

func findLicense(name string) License {
    found := matchLicense(name)
    if found == "" {
        er("unknown license: " + name)
    }
    return Licenses[found]
}

// matchLicense compares the given a license name
// to PossibleMatches of all built-in licenses.
// It returns blank string, if name is blank
// string or it didn't find
// then appropriate match to name.
func matchLicense(name string) string {
    if name == "" {
        return ""
    }

    for key, lic := range Licenses {
        for _, match := range lic.PossibleMatches {
            if strings.EqualFold(name, match) {
                return key
            }
        }
    }

    return ""
}

```

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- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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This option is useful when you wish to copy part of the code of the

Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy

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d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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END OF TERMS AND CONDITIONS

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```
<one line to give the library's name and a brief idea of what it does.>  
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```

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<signature of Ty Coon>, 1 April 1990

Ty Coon, President of Vice

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Version 3, 29 June 2007

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the

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d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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1.90 tcl 8.6.8

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1.92 python 3.7.4

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.. highlightlang:: none

.. _history-and-license:

History and License

History of the software

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <https://www.cwi.nl/>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <https://www.cnri.reston.va.us/>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see <http://www.zope.com/>). In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Release	Derived from	Year	Owner	GPL compatible?
0.9.0 thru 1.2	n/a	1991-1995	CWI	yes
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1.6	1.5.2	2000	CNRI	no

2.0	1.6	2000	BeOpen.com	no	
1.6.1	1.6	2001	CNRI	no	
2.1	2.0+1.6.1	2001	PSF	no	
2.0.1	2.0+1.6.1	2001	PSF	yes	
2.1.1	2.1+2.0.1	2001	PSF	yes	
2.1.2	2.1.1	2002	PSF	yes	
2.1.3	2.1.2	2002	PSF	yes	
2.2 and above	2.1.1	2001-now	PSF	yes	

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Mersenne Twister

The `:mod:`_random`` module includes code based on a download from <http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`
or `init_by_array(init_key, key_length)`.

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<http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/emt.html>

email: m-mat @ math.sci.hiroshima-u.ac.jp (remove space)

Sockets

The `:mod:`socket``

module uses the functions, `:func:`getaddrinfo``, and

`:func:`getnameinfo``, which are coded in separate source files from the WIDE

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Asynchronous socket services

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UUencode and UUdecode functions

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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

XML Remote Procedure Calls

The `:mod:`xmlrpc.client`` module contains the following notice::

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Select kqueue

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SipHash24

The file :file:`Python/pyhash.c` contains Marek

Majkowski' implementation of

Dan Bernstein's SipHash24 algorithm. It contains the following note::

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Original location:

<https://github.com/majek/csiphash/>

Solution inspired by code from:

Samuel Neves (supercop/crypto_auth/siphash24/little)

djb (supercop/crypto_auth/siphash24/little2)

Jean-Philippe Aumasson

(<https://131002.net/siphash/siphash24.c>)

strtod and dtoa

The file `:file:\Python\dtoa.c`, which supplies C functions `dtoa` and `strtod` for conversion of C doubles to and from strings, is derived from the file of the same name by David M. Gay, currently available from <http://www.netlib.org/fp/>. The original file, as retrieved on March 16, 2009, contains the following copyright and licensing notice::

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cfuhash

The implementation of the hash table used by the :mod:`tracemalloc` is based on the cfuhash project::

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

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1.93 xerces-j 2.11.0

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1.94 libxml 2.9.8

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1.95 file 5.34

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```
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## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)  
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## Commands to generate dependency files  
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)  
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)  
  
## Flags for position independent code  
SHAREDLIBCFLAGS = -fPIC  
SHAREDLIBCXXFLAGS = -fPIC  
SHAREDLIBCPPFLAGS = -DPIC  
  
## Additional flags when building libraries and with threads  
THREADSCPPFLAGS = -D_REENTRANT  
LIBCPPFLAGS =  
  
## Compiler switch to embed a runtime search path  
LD_RPATH=  
LD_RPATH_PRE= -Wl,-rpath,  
  
## Compiler switch to embed a library name  
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))  
  
## Shared library options  
LD_SOOPTIONS= -Wl,-Bsymbolic  
  
## Shared object suffix  
SO = so  
## Non-shared intermediate object suffix  
STATIC_O = ao  
  
## Compilation rules  
%. $(STATIC_O): $(srcdir)/%.c  
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<  
%.o: $(srcdir)/%.c
```

```

$(COMPILE.c)
$(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[ :]*/1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[ :]*/1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

## Versioned libraries rules

%. $(SO).$(SO_TARGET_VERSION_MAJOR): %. $(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%. $(SO): %. $(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}. $(SO).$(SO_TARGET_VERSION) $@

```

End FreeBSD-specific setup

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1.96 acl 2.2.52

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1.97 protobuf 3.6.1

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This file contains a list of people who have made large contributions
to the public version of Protocol Buffers.

Original Protocol Buffers design and implementation:

Sanjay Ghemawat <sanjay@google.com>

Jeff Dean <jeff@google.com>

Daniel Dulitz <daniel@google.com>

Craig Silverstein

Paul Haahr <haahr@google.com>

Corey Anderson <corin@google.com>

(and many others)

Proto2 C++ and Java primary author:

Kenton Varda <kenton@google.com>

Proto2 Python primary authors:

Will Robinson <robinson@google.com>

Petar Petrov <petar@google.com>

Java Nano primary authors:

Brian Duff <bduff@google.com>
Tom Chao <chaot@google.com>
Max Cai <maxtroy@google.com>
Ulas Kirazci <ulas@google.com>

Large code contributions:

Jason Hsueh <jasonh@google.com>
Joseph Schorr <jschorr@google.com>
Wenbo Zhu <wenboz@google.com>

Large quantity of code reviews:

Scott Bruce <sbruce@google.com>
Frank Yellin
Neal Norwitz <nnorwitz@google.com>
Jeffrey Yasskin <jyasskin@google.com>
Ambrose
Feinstein <ambrose@google.com>

Documentation:

Lisa Carey <lcarey@google.com>

Maven packaging:

Gregory Kick <gak@google.com>

Patch contributors:

Kevin Ko <kevin.s.ko@gmail.com>
* Small patch to handle trailing slashes in --proto_path flag.

Johan Euphrosine <proppy@aminche.com>
* Small patch to fix Python CallMethod().

Ulrich Kunitz <kune@deine-taler.de>
* Small optimizations to Python serialization.

Leandro Lucarella <llucax@gmail.com>
* VI syntax highlighting tweaks.
* Fix compiler to not make output executable.

Dilip Joseph <dilip.antony.joseph@gmail.com>
* Heuristic detection of sub-messages when printing unknown fields in text format.

Brian Atkinson <nairb774@gmail.com>
* Added @Override annotation to generated Java code where appropriate.

Vincent Choinire <Choiniere.Vincent@hydro.qc.ca>
* Tru64 support.

Monty Taylor <monty.taylor@gmail.com>
* Solaris 10 + Sun Studio fixes.

Alek Storm <alek.storm@gmail.com>

* Slicing support for repeated scalar fields for the Python API.

Oleg Smolsky <oleg.smolsky@gmail.com>
* MS Visual Studio error format option.

* Detect unordered_map in stl_hash.m4.
Brian Olson <brianolson@google.com>
* gzip/zlib I/O support.
Michael Poole <mdpoole@troilus.org>
* Fixed warnings about generated constructors not explicitly initializing all fields (only present with certain compiler settings).
* Added generation of field number constants.
Wink Saville <wink@google.com>
* Fixed initialization ordering problem in logging code.
Will Pierce <willp@nuclei.com>
* Small patch improving performance of in Python serialization.
Alexandre Vassalotti <alexandre@peadrop.com>
* Emacs mode for Protocol Buffers (editors/protobuf-mode.el).
Scott Stafford <scott.stafford@gmail.com>
* Added Swap(), SwapElements(), and RemoveLast() to Reflection interface.
Alexander Melnikov <alm@sibmail.ru>
* HPUX support.
Oliver Jowett <oliver.jowett@gmail.com>
* Detect whether zlib is new enough in configure script.
* Fixes for Solaris 10 32/64-bit confusion.
Evan Jones <evanj@mit.edu>
* Optimize Java serialization code when writing a small message to a stream.
* Optimize Java serialization of strings so that UTF-8 encoding happens only once per string per serialization call.
* Clean up some Java warnings.
* Fix bug with permanent callbacks that delete themselves when run.
Michael Kucharski <m.kucharski@gmail.com>
* Added CodedInputStream.getTotalBytesRead().
Kacper Kowalik <xarthisius.kk@gmail.com>
* Fixed m4/acx_pthread.m4 problem for some Linux distributions.
William Orr <will@worrbase.com>
* Fixed detection of sched_yield on Solaris.
* Added atomicops for Solaris
Andrew Paprocki <andrew@ishiboo.com>
* Fixed minor IBM xLC compiler build issues
* Added atomicops for AIX (POWER)
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1.98 acpid 2.0.30

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```
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```
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```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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1.99 elfutils 0.175

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1.102 inotify-tools 3.14+git0+1df9af4d6c

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1.103 rrdtool 1.7.0

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Bill Fenner <fenner with research.att.com>

Blair Zajac <bzajac with geostaff.com>

Bruce Campbell <bruce.campbell with apnic.net>

Chin-A-Young <china with thewrittenword.com>

Christophe VG <Christophe.VanGinneken with ubizen.com>

Christophe Van Ginneken <Christophe.VanGinneken with ubizen.com> (--no-legend)

Dan Dunn <dandunn with computer.org>

Daniel

Pocock <daniel in pocock.com.au> rrd_create to mmap port

Dave Bodenstab <dave@bodenstab.org> AT style time in update, tclfixes

David Grimes <dgrimes with navisite.com> SQRT/SORT/REV/SHIFT/TREND

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Jeff R. Allen <jeff.allen with acm.org> (autoconfigure, portability)
Jeremy Fischer <jeremy with pobox.com> (Makefile changes & RPM builds)
Jess Couto Fandio
Joel Becker <jlbec with raleigh.ibm.com> AIX
Joey Miller <joeym with inficad.com>php3 and php4 bindings
Jost.Krieger
<Jost.Krieger with ruhr-uni-bochum.de>
Kai Siering <kai.siering with mediaways.net>
Kevin Brintnall <kbrint with rufus.net> bugfixes in and additions to rrdcached, including journaling support
Larry Leszczynski <larryl with furph.com>
Mark Plaksin <happy@usg.edu> rrd_graph_v
Matt Chambers <matthew.chambers with vanderbilt.edu> --full-size-mode for rrdgraph
Melchior Rabe <rrdtool at mrab.de> -- legend position patch
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Otmr Lendl <O.Lendl with Austria.EU.net> (lots of bugfixes)
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Patrick Cherry <patrick with bytemark.co.uk>
Paul Joslin <Paul.Joslin with sdrc.com>
Peter Speck <speck with vitality.dk> eps/svg/pdf file format code in rrdtool-1.x
Peter Stamfest <peter with stamfest.at> initial multi-thread support
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Philippe.Simonet <Philippe.Simonet with swisscom.com> (NT porting)
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REIBENSCHUH Alfred <alfred.reibenschuh with it-austria.com> AIX
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Roman Hoogant <rhoogant with ee.ethz.ch>
Ronan Mullally <ronan in 4L.ie>
Roger J. Meier <roger.meier in terreactive.ch> (arbitrary linelength in rrdtool)
Russ Wright <rwwright with home.com>
Sean Summers <sean with Fenstermaker.com> (RPM .spec)
Selena M Brewington <smbrewin with ichips.intel.com> add_ds
Shane O'Donnell <shaneo with opennms.org>
Simon Leinen <simon with switch.ch>
Steen Linden <Steen.Linden with ebone.net>
Sebastian Harl <sh at tokkee.org> debian packaging, rrdcached fixes and enhancements
Stefan Ludewig <Stefan.Ludewig at exitgames.com> 1.3 WIN32 Port
Stefan Mueller <s.mueller with computer.org> HPUX 11
Steve Harris <steveh with wesley.com.au>
AIX portability
Steve Rader <rader with teak.wiscnet.net> (rrd_cgi debugging and LAST)
Terminator rAT <karl_schilke with eli.net>
Tobias Weingartner <weingart with cs.ualberta.ca>
Thomas Gutzler <thomas.gutzler with gmail.com> dashed lines

Tom Crawley <Tom.Crawley with hi.riotinto.com.au> (GCC&HP configuration)
Travis Brown <tebrown with csh.rit.edu>
Tuc <ttsg with ttsg.com>
Ulf Lilleengen <lulf with pvv.ntnu.no> Python binding for 'rrdtool first'
Ulrich Schilling <schilling with netz.uni-essen.de> AIX
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Vytautas Zdanavicius <vytaszd@yahoo.com> -- python argument list expander
Martin Sperl <rrdtool martin.sperl.org> (CDEF prediction functions, libdbi)
Philippe Simonet <philippe.simonet with swisscom.ch> (Windows Binaries)
Alexander Lucke (lucke with dns-net.de) of DNS:NET Internet
Services (www.dns-net.de) <http://rrdtool.org>
Hedley Simons <heds@metahusky.net>
Nicola Worthington <nicolaw@cpan.org>
Wegmann, Christof <Christof.Wegmann@exitgames.com> 1.3/trunk win32 port

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1.105 free-type 1.0

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2006-Jan-27

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#
src/base/fthash.c
src/base/md5.c
src/base/md5.h
#
src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
src/bdf/rules.mk
#
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
src/pcf/pcfutil.c
src/pcf/pcfutil.h
src/pcf/README
src/pcf/rules.mk
#
src/gzip/adler32.c
src/gzip/infblock.c
src/gzip/infblock.h
src/gzip/infcodes.c
src/gzip/infcodes.h
src/gzip/inffixed.h
src/gzip/inflate.c
src/gzip/inftrees.c
src/gzip/inftrees.h
src/gzip/infutil.c
src/gzip/infutil.h
src/gzip/zconf.h
src/gzip/zlib.h
src/gzip/zutil.c
src/gzip/zutil.h
#
src/tools/apinames.c
```

src/tools/ftrandom/ftrandom.c

#

#

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1.108 libpng 1.6.35

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1.109 bcel 6.4.1

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Aleksey Gurtovoy (agurtovoy@meta-comm.com)

Andrei Alexandrescu (andrewalex - at - hotmail.com) (See Boost list message of August 12, 2004 11:06:58 AM EST)

Andrew Lumsdaine ()

Anthony Williams (anthony -at- justsoftwaresolutions.co.uk)

Beman Dawes (bdawes@acm.org)

Brad King (brad.king -at- kitware.com) (See Boost list message of Wed, 21 Jul 2004 11:15:46 -0400)

Brian Osman (osman -at- vvisions.com) (See CVS log)

Bruce Barr (schmoost -at- yahoo.com) (See Boost list of Mon, 16 Aug 2004 15:06:43 -0500)

Bruno da Silva de Oliveira (bruno - at - esss.com.br)

Christain Engstrom (christian.engstrom -at- glindra.org) (See Boost list message of Mon, 30 Aug 2004 14:31:49 +0200)

Cromwell D Enage (sponage -at- yahoo.com) (See Boost list message of August 12, 2004 11:49:13 AM EST)

Dan Gohman (djg

-at- cray.com) (See Boost list message of Sat, 21 Aug 2004 10:54:59 +0100)

Dan Nuffer (dan -at- nuffer.name)

Daniel Frey (d.frey -at- gmx.de, daniel.frey -at- aixigo.de)

Daniel Nuffer (dan -at- nuffer.name)

Darin Adler (darin -at- bentspoon.com) (Email to Andreas Huber, see change log)

Daryle Walker (darylew - at - hotmail.com)

Dave Abrahams (dave@boost-consulting.com)

Dave Moore (dmoore -at- viefinancial.com) (See Boost list message of 18 Dec 2003 15:35:50 -0500)

David Abrahams (dave@boost-consulting.com)

Dietmar Kuehl (dietmar_kuehl -at- yahoo.com) (Email to Andreas Huber, see change log)

Douglas Gregor (gregod -at- cs.rpi.edu, dgregor -at- cs.indiana.edu, doug.gregor -at- gmail.com)

Dr John Maddock (john - at - johnmaddock.co.uk)

Edward D. Brey (brey -at- ductape.net) (Email to Andreas Huber, see change log)

Eric Ford (un5o6n902 -at- sneakemail.com) (See Boost list message of Sun, 15 Aug 2004 10:29:13 +0100)

Eric Friedman (ebf@users.sourceforge.net)

Eric Niebler (eric@boost-consulting.com)

Fernando
Cacciola (fernando_cacciola@ciudad.com.ar)
Fernando Luis Cacciola Carballal (fernando_cacciola@ciudad.com.ar)
Francois Faure (Francois.Faure -at- imag.fr) (See CVS log)
Gary Powell (powellg - at - amazon.com) (See Boost list message of 10 Feb 2004 14:22:46 -0800)
Gennadiy Rozental (rogeeff -at- mail.com) (Email to Andreas Huber, see change log)
Gottfried Ganssaug (Gottfried.Ganssaug -at- HAUFE.DE) (See Boost List message of Mon, 16 Aug 2004 10:09:19 +0200)
Gottfried Ganauge (Gottfried.Ganssaug -at- HAUFE.DE) (Alternative spelling of Gottfried Ganssaug)
Greg Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)
Gregory Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)
Gunter Winkler (gunter.winkler -at- unibw-muenchen.de) (See Boost List message of Mon, 16 Aug 2004 10:24:17 +0200)
Hartmut Kaiser (hartmut.kaiser -at- gmail.com)
Herve Bronnimann (hbr -at- poly.edu)
Herv
Brnnimann (hbr -at- poly.edu)
Housemarque Oy (Ilari Kuittinen ilari.kuittinen -at- housemarque.fi)
Howard Hinnant (hinnant -at- twcnny.rr.com) (See Boost list message of July 25, 2004 3:44:49 PM EST)
Hubert Holin (hubert_holin -at- users.sourceforge.net)
Indiana University ()
Itay Maman (imaman -at- users.sourceforge.net)
Jaakko Jrv (jajarvi -at- osl.iu.edu)
Jaap Suter (j.suter -at- student.utwente.nl) (See Boost list message of Thu, 16 Sep 2004 09:32:43 -0700)
Jeff Garland (jeff - at - crystalclearsoftware.com) (see Boost list post of July 25, 2004 19:31:09 -0700)
Jens Maurer (Jens.Maurer@gmx.net)
Jeremy G Siek (jsiek@osl.iu.edu)
Jeremy Siek (jsiek@osl.iu.edu)
Joel de Guzman (joel -at- boost-consulting.com) (See Boost list message of July 25, 2004 8:32:00 PM EST)
John Bandela (jbandela-at-ufl.edu)
John Maddock (john - at - johnmaddock.co.uk)
John R Bandela (jbandela-at-ufl.edu)
Jonathan Turkanis (turkanis -at- coderage dot com)
Juergen Hunold (hunold -at- ive.uni-hannover.de) (See Boost List Message of Fri, 13 Aug 2004 19:39:55 +0200)
Kevlin Henney (kevin -at- curbralan.com) (See Boost list message of Wed, 15 Sep 2004 18:15:17 +0200)
Kresimir Fresl (fresl -at- master.grad.hr) (See Boost List message of August 16, 2004 8:23:35 AM EST)
Lars Gullik Bjnnes (larsbj -at- lyx.org) (See Boost list message of Tue, 17 Aug 2004 15:49:02 +0100)
Lie-Quan Lee (liequan - at - slac.stanford.edu, llee - at - cs.indiana.edu)
Maarten Keijzer (mkeijzer -at- cs.vu.nl) (See Boost list message of Wed, 18 Aug 2004 21:43:18 +0100)
Mac Murrett (mmurrett -at- mac.com)
Marc Wintermantel (wintermantel -at- imes.mavt.ethz.ch, wintermantel -at- even-ag.ch) (See CVS log)
Michael Glassford (glassfordm - at - hotmail.com)
Michael Stevens (Michael.Stevens - at - epost.de)
Multi Media Ltd. (pdimov@mmltd.net)
Nicolai M Josuttis (solutions -at- josuttis.com) (See Boost list message of Mon, 30 Aug 2004 10:52:00 +0100)
Nikolay Mladenov (nickm -at- sitius.com) (See Boost list message of Tue, 17 Aug 2004

15:45:33 +0100)

Paul Mensonides (pmenso57 -at- comcast.net) (See Boost list message of July 21, 2004 1:12:21 AM EST)

Pavol Droba (droba -at- topmail.sk)

Peter Dimov (pdimov@mmltd.net)

R W Grosse-Kunstleve (RWGrosse-Kunstleve@lbl.gov)

Ralf W. Grosse-Kunstleve (RWGrosse-Kunstleve@lbl.gov)

Rational Discovery LLC (Greg Landrum Landrum -at- RationalDiscovery.com) (See Boost list post of Tue, 17 Aug 2004 10:35:36 +0100)

Rene Rivera (grafik/redshift-software.com, rrivera/acm.org)

Robert Ramey (ramey@www.rrsd.com)

Roland Richter (roland -at- flll.jku.at) (See Boost list post of Mon, 16 Aug 2004 22:16:55 +0200)

Roland Schwarz (roland.schwarz -at- chello.at)

Ronald Garcia (garcia -at- cs.indiana.edu) (Email to Andreas Huber, see change log)

Samuel Krempp (krempp -at- crans.ens-cachan.fr) (See Boost list message of Mon, 27 Sep 2004 13:18:36 +0200)

Stefan Seefeld (seefeld -at- sympatico.ca)

Stephen Cleary (sccleary -at- jerviswebb.com) (See Boost list message of Tue, 28 Sep 2004 13:11:46 +0100)

Steve

Cleary (Variant of Stephen Cleary)

Sylvain Pion (Sylvain.Pion - at - sophia.inria.fr)

The Trustees of Indiana University ()

Thomas Witt (witt - at - ive.uni-hannover.de, witt - at - acm.org, witt - at - styleadvisor.com)

Thorsten Jrgen Ottosen (nesotto - at - cs.auc.dk)

Thorsten Ottosen (nesotto - at - cs.auc.dk)

Toon Knapen (toon dot knapen - at - fft.be)

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Vladimir Prus (ghost@cs.msu.su)

William E. Kempf () (email to Beman Dawes, 9/14/2006 4:18 PM)

Joerg Walter (jhr.walter - at - t-online.de : email to ublas mailing list Mon, 17 Sep 2007 10:17:08 +0200)

Mathias Koch (mkoch - at - idesis.de 7 : email to boost-owner@lists.boost.org Sep 2007 13:20:09 +0200)

--- end ---

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This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser.

The Encore Multimax modifications were supplied by Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM.

Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood.

Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code. Al Dossler (dossler@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code.

Jesper Peterson(jep@mtiame.mtia.oz.au), Michel Schinz, and Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port.

Thomas Funke (thf@zelator.in-berlin.de(?)) and

Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports.

Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code.

Bill Janssen (janssen@parc.xerox.com) supplied

the SunOS dynamic loader
specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and
Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and
Dave Detlefs(detlefs@src.dec.com) also provided several generic bug fixes.
Alistair G. Crooks(agc@uts.amdahl.com) supplied the NetBSD and 386BSD ports.
Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port.
Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to
a Motorola 88K processor running CX/UX (Harris NightHawk).
Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to
nonIBM development environments (a nontrivial task).
Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port.
David Chase, then at Olivetti Research, suggested several improvements.
Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the
code to save and print call stacks for leak detection on a SPARC.
Jesse Hull and John Ellis supplied the C++ interface code.
Zhong Shao performed much of the
experimentation that led to the
current typed allocation facility. (His dynamic type inference code hasn't
made it into the released version of the collector, yet.)

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```
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"http://www.boost.org/tools/boostbook/dtd/boostbook.dtd">
<article id="copyright_test" last-revision="DEBUG MODE Date: 2000/12/20 12:00:00 $"
xmlns:xi="http://www.w3.org/2001/XInclude">
  <title>Copyright Test</title>
  <articleinfo>
    <copyright>
      <year>1963</year> <year>1964</year> <year>1965</year> <holder>Jane Doe</holder>
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      <year>2018</year> <holder>Joe Blow, John Coe</holder>
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      <year>1977</year> <year>1985</year> <holder>Someone else</holder>
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////

```
[#mpl]
# MPL Support, <boost/mp11/mpl.hpp>
:toc:
:toc-title:
:idprefix:
```

The header `<boost/mp11/mpl.hpp>`, when included, defines the necessary support infrastructure for `mp_list` and `std::tuple` to be valid link:../../../../libs/mpl[MPL] sequences.

NOTE: `mpl.hpp` is not included by `<boost/mp11.hpp>`.

1.111 libidn 1.35

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1.113 nspr 4.19

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1.114 curl 7.61.0

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One common dilemma is that [GPL](<https://www.gnu.org/licenses/gpl.html>)

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1.115 udev 3.2.5

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1.116 libpng 1.6.37

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1.117 cyrus-sasl 2.1.20

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the

Library even though the source code is not.

Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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- b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.
- c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may

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6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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Last revised by Reagle \$Date: 2005-06-03 18:49:13 -0400 (Fri, 03 Jun 2005)

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analysis/common/src/java/org/apache/lucene/analysis/de/GermanLightStemmer.java
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Specification: JSR-000154 Java(TM) Servlet 2.5 Specification ("Specification")

Version: 2.5

Status: Maintenance Review

Release: August 11, 2005

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Specification: JSR-000154 Java(TM) Servlet Specification ("Specification")

Version: 2.5

Status: Maintenance Review

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Java(tm) Servlet Specification ("Specification")

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Jackson JSON processor

Jackson is a high-performance, Free/Open Source JSON processing library. It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has been in development since 2007. It is currently developed by a community of developers, as well as supported commercially by FasterXML.com.

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1.125 libcap 2.25

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```

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1.129 shadow 4.6

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abstract: 'Build and install Perl modules'

author:

- 'Ken Williams <kwilliams@cpan.org>'

- "Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-build@perl.org>."

build_requires:

File::Temp: 0.15

Test::Harness: 3.16

Test::More: 0.49

generated_by: 'Module::Build version 0.3608'

license: gpl

meta-spec:

url: <http://module-build.sourceforge.net/META-spec-v1.4.html>

version: 1.4

name: Module-Build

resources:

MailingList: <mailto:module-build@perl.org>

license: <http://dev.perl.org/licenses/>

repository: <http://github.com/dagolden/module-build/>

version: 3

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Julian Seward, jseward@bzip.org

bzip2/libbzip2 version 1.0.6 of 6 September 2010

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```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

```
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any later version.
```

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```

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```
Gnomovision version 69, Copyright (C) 19xx name of author
```

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program `Gnomovision' (a program to direct compilers to make passes
at assemblers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!
#!perl

=head1 NAME

copyright.t

=head1 DESCRIPTION

Tests that the latest copyright years in the top-level README file and the
C<perl -v> output match each other.

If the test fails, update at least one of README and perl.c so that they match
reality.

Optionally you can pass the C<--now> option to check they are at the current
year. This isn't checked by default, so that it doesn't fail for people
working on older releases. It should be run before making a new release.

=cut

```
use strict;  
use Config;
```

```
BEGIN { require './test.pl' }
```

```
if ( $Config{usecrosscompile} ) {
```

```

skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so
# that tests still pass for people intentionally working on older versions:
if ($opt eq '--now')
{
my $current_year = (gmtime)[5] + 1900;
is $v_year, $current_year, 'perl -v copyright includes current
year';
is $readme_year, $current_year, 'README copyright includes current year';
}

# Otherwise simply check that the two copyright dates match each other:
else
{
is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;

sub readme_year
# returns the latest copyright year from the top-level README file
{

open my $readme, '<', './README' or die "Opening README failed: $!";

# The copyright message is the first paragraph:
local $/ = "";
my $copyright_msg = <$readme>;

my ($year) = $copyright_msg =~ /\b(\d{4,})/s
or die "Year not found in README copyright message '$copyright_msg'";

$year;
}

sub v_year
# returns the latest copyright year shown in perl -v
{

```

```
my $output = runperl switches => ['-v'];
my ($year) = $output =~ /copyright 1987.*\b(\d{4,})/i
    or die "Copyright statement not found in perl -v output '$output'";
```

```
$year;
}
```

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```

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```
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program `Gnomovision' (a program to direct compilers to make passes
at assemblers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

That's all there is to it!

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```

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1.133 asm 5.0.3

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```
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXClassAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXCodeAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/ASMContentHandler.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/Processor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/xml/SAXFieldAdapter.java
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*/
```

```

/**
 * Creates a new {@link AnalyzerAdapter}. Subclasses must not use this
 * constructor. Instead, they must use the
 * {@link #AnalyzerAdapter(int, String, int, String, String, MethodVisitor)}
 * version.
 *
 * @param owner
 *
 * the owner's class name.
 * @param access
 * the method's access flags (see {@link Opcodes}).
 * @param name
 * the method's name.
 * @param desc
 * the method's descriptor (see {@link Type Type}).
 * @param mv
 * the method visitor to which this adapter delegates calls. May
 * be null.
 * @throws IllegalStateException
 * If a subclass calls this constructor.
 */

```

Found in path(s):

```

/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/AnalyzerAdapter.java

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*/

/**

* Constructs a new {@link Textifier}. *Subclasses must not use this
* constructor*. Instead, they must use the {@link #Textifier(int)}
* version.

*

* @throws IllegalStateException

* If a subclass calls this constructor.

*/

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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*/

/**

* Constructs a new {@link LocalVariableAnnotationNode}. *<i>Subclasses must
* not use this constructor</i>. Instead, they must use the
* {@link #LocalVariableAnnotationNode(int, TypePath, LabelNode[], LabelNode[], int[], String)}* version.
**

* @param typeRef
* a reference to the annotated type. See {@link TypeReference}.

* @param typePath
* the path to the annotated type argument, wildcard bound, array
* element type, or static inner type within 'typeRef'. May be
* `<tt>null</tt>` if the annotation targets 'typeRef' as a whole.

* @param start
* the first instructions corresponding to the continuous ranges
* that make the scope of this local variable (inclusive).

* @param end
* the last instructions corresponding to the continuous ranges
* that make the scope of this local variable (exclusive). This
* array must have the same size as the 'start' array.

* @param index
* the local variable's index in each range. This array must have
* the same size as the 'start' array.

* @param desc
* the class descriptor of

the annotation class.

*/

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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/TypePath.java

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/TypeReference.java

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*/

/**

* Creates a new JSRInliner. *<i>Subclasses must not use this
* constructor</i>. Instead, they must use the
* { @link #JSRInlinerAdapter(int, MethodVisitor, int, String, String, String, String[])}
* version.
*
* @param mv
*
the `MethodVisitor` to send the resulting inlined
* method code to (use `null` for none).
* @param access
* the method's access flags (see { @link Opcodes}). This
* parameter also indicates if the method is synthetic and/or
* deprecated.
* @param name
* the method's name.
* @param desc
* the method's descriptor (see { @link Type}).
* @param signature
* the method's signature. May be `null`.
* @param exceptions
* the internal names of the method's exception classes (see
* { @link Type#getInternalName() getInternalName}). May be
* `null`.
* @throws IllegalStateException
* If a subclass calls this constructor.
/

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
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- jar/org/objectweb/asm/ClassVisitor.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/MethodWriter.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/tree/TableSwitchInsnNode.java
- *
- /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/util/CheckAnnotationAdapter.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/optimizer/MethodOptimizer.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/Item.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/commons/RemappingSignatureAdapter.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/util/TraceAnnotationVisitor.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/util/CheckFieldAdapter.java
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-
- jar/org/objectweb/asm/Handle.java

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/util/CheckClassAdapter.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/LdcInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/LabelNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/commons/InstructionAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/IntInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/FieldNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/analysis/BasicValue.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/util/CheckMethodAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/ParameterNode.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/MultiANewArrayInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/signature/SignatureWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/commons/TableSwitchGenerator.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/util/Printer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/IncnInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/LineNumberNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/MethodInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/Context.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/commons/AdviceAdapter.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/AnnotationNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/commons/LocalVariablesSorter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/util/TraceSignatureVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/analysis/Interpreter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-

jar/org/objectweb/asm/ByteVector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/TraceFieldVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/ClassConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/TypeInsnNode.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/InsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ClassReader.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/MethodConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/FieldConstantsCollector.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/MethodNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/AnnotationWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/FieldInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingAnnotationAdapter.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Subroutine.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ClassWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/VarInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/ConstantPool.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LookupSwitchInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/AbstractInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingFieldAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/NameMapping.java
*
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/StaticInitMerger.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/JumpInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Frame.java

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/Edge.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/RemappingMethodAdapter.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/optimizer/Constant.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/SimpleRemapper.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/RemappingClassAdapter.java
 *
 /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/optimizer/Shrinker.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/MethodVisitor.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/Label.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/Handler.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/optimizer/JarOptimizer.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/CodeSizeEvaluator.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/util/CheckSignatureAdapter.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/AnalyzerException.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/SerialVersionUIDAdder.java
 *
 /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/util/ASMifier.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/FrameNode.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/TypeAnnotationNode.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/InvokeDynamicInsnNode.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/signature/SignatureVisitor.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/SourceInterpreter.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/optimizer/ClassOptimizer.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/LocalVariableNode.java
 *

/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/BasicVerifier.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/signature/SignatureReader.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/InsnList.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/optimizer/AnnotationConstantsCollector.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/InnerClassNode.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/util/TraceMethodVisitor.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/Method.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/Attribute.java
 *
 /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/Frame.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/TryCatchBlockSorter.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/AnnotationVisitor.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/BasicInterpreter.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/FieldWriter.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/Opcodes.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/SimpleVerifier.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/SourceValue.java
 *
 /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/Value.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/Type.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/SmallSet.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/commons/Remapper.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/util/TraceClassVisitor.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/FieldVisitor.java
 * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
 jar/org/objectweb/asm/tree/analysis/Analyzer.java

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/tree/TryCatchBlockNode.java

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- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-jar/org/objectweb/asm/tree/analysis/package.html
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-jar/org/objectweb/asm/signature/package.html
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-jar/org/objectweb/asm/commons/package.html

*

/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-jar/org/objectweb/asm/package.html

- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-jar/org/objectweb/asm/tree/package.html
- * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/asm-5-0-3-sources-jar/org/objectweb/asm/util/package.html

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jar/org/objectweb/asm/util/ASMifiable.java

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/util/Textifiable.java

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*/

/**

* Creates a new { @link GeneratorAdapter}. *Subclasses must not use this*

* constructor*.* Instead, they must use the

* { @link #GeneratorAdapter(int, MethodVisitor, int, String, String)}

* version.

*

* @param mv

* the

method visitor to which this adapter delegates calls.

* @param access

* the method's access flags (see { @link Opcodes}).

```
* @param name
*     the method's name.
* @param desc
*     the method's descriptor (see { @link Type Type}).
* @throws IllegalStateException
*     If a subclass calls this constructor.
*/
```

Found in path(s):

```
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/GeneratorAdapter.java
```

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```
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/shrink-frames.properties
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/shrink-resize.properties
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/shrink-writer.properties
*
```

```
/opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/shrink-signatures.properties
```

```
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/shrink-annotations.properties
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/shrink.properties
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*/
/**
 * Constructs a new {@link ClassNode}. Subclasses must not use this
 * constructor. Instead, they must use the {@link #ClassNode(int)}
 * version.
 *
 * @throws IllegalStateException
 *         If a subclass calls this constructor.
 */

```

Found in path(s):

```

* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/ClassNode.java

```

1.134 libtirpc 1.0.3

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1.135 log-rotate 3.14.0

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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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- c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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- a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the

Sections above.

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1.137 dosfs-tools 4.1

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The

"System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

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- e) Convey the object code using peer-to-peer transmission, provided you inform other peers where the object code and Corresponding

Source of the work are being offered to the general public at no charge under subsection 6d.

A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

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This is JavaScriptCore's variant of the PCRE library. While this library started out as a copy of PCRE, many of the features of PCRE have been removed.

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1.139 heimdal 1.0.1

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1.140 libunwind 1.2-rc1

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1.141 zopfli 1.0.1

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1.142 xmlsec-java 2.2.0

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1.143 tdb 2007-06-22

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Index: tdbsa/tdb.c

--- tdbsa.orig/tdb.c

+++ tdbsa/tdb.c

@@ -4,11 +4,11 @@ Rev: 23371

Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)

*/

/*

- Unix SMB/CIFS implementation.
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- trivial database library - private includes
-
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1.145 cracklib 2.9.5

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library `Frob' (a library for tweaking knobs) written by James
Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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EFFECTIVE OCT 2008, LICENSE IS BEING CHANGED TO LGPL-2.1 (though not reflected
in released code until Nov 2009 - slow release cycle...)

Discussion thread from mailing list archive, with approval from everyone actively involved or holding original licensing rights included.

[Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:16

Attachments: Message as HTML

looks like 2.8.11 is out and marked as "GPL-2" ... releasing libraries unde=
r=20

GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists
=2Dmike

Re: [Cracklib-devel] cracklib license

From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:18

I understand that, and you're welcome to bring it up with Alec directly and see if he wants to relicense his code as LGPL... but at this point, it was enough to just get it consistent and documented as to what it was released under. This wasn't actually a license change, just a clarification of the licensing that was already in place.=20

-- Nathan

=20

Nathan Neulinger EMail: nneul@um...

University of Missouri - Rolla Phone: (573) 341-6679

UMR Information Technology Fax: (573) 341-4216

> -----Original Message-----

> From: cracklib-devel-bounces@li...

> [mailto:cracklib-devel-bounces@li...] On Behalf Of

> Mike Frysinger

> Sent: Monday, October 01, 2007 8:15 PM

> To: cracklib-devel@li...

> Subject: [Cracklib-devel] cracklib license

>=20

> looks like 2.8.11 is out and marked as "GPL-2" ... releasing

> libraries under

> GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists

> -mike

Re: [Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:33

Attachments: Message as HTML

On Monday 01 October 2007, Neulinger, Nathan

wrote:

> I understand that, and you're welcome to bring it up with Alec directly
> and see if he wants to relicense his code as LGPL... but at this point,
> it was enough to just get it consistent and documented as to what it was
> released under. This wasn't actually a license change, just a
> clarification of the licensing that was already in place.

the original license (before moving to sourceforge -- aka, 2.7) was not=20
GPL-2 ... it was a modified artistic license ... i didnt notice the license=
=20
change until it was mentioned in the latest notes.

unlike the old license, GPL-2 prevents people from using cracklib unless th=
eir=20
applications are also GPL-2 which imo is just wrong. it isnt the place of =
a=20
library to dictact to application writes what license they should be using.=
=20
thus LGPL-2.1 enters to fill this void.
=20Dmike

Re: [Cracklib-devel]

cracklib license

From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:46

Seems like the ideal thing here would be for you and the other distro
maintainers to get together with Alec in a conversation and come to a
decision as to what licensing scheme y'all want. I haven't really done
much other than cleaning up the packaging and patches and a small bit of
additional code, so whatever licensing y'all come up with is fine by me.

-- Nathan
=20

Nathan Neulinger EMail: nneul@um...
University of Missouri - Rolla Phone: (573) 341-6679
UMR Information Technology Fax: (573) 341-4216

> -----Original Message-----
> From: cracklib-devel-bounces@li...
> [mailto:cracklib-devel-bounces@li...] On Behalf Of
> Mike Frysinger
> Sent: Monday, October 01, 2007 8:33 PM
> To: Neulinger, Nathan
> Cc: cracklib-devel@li...; Alec Muffett

> Subject: Re: [Cracklib-devel] cracklib license
>=20
> On Monday 01 October 2007, Neulinger, Nathan wrote:
>> I understand that, and you're welcome to bring it up with Alec
> directly
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>> released under. This wasn't actually a license change, just a
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> place of a
> library
to dictact to application writes what license they should
> be using.
> thus LGPL-2.1 enters to fill this void.
> -mike

Re: [Cracklib-devel] cracklib license

From: Alec Muffett <alecm@cr...> - 2007-10-02 08:57

> Seems like the ideal thing here would be for you and the other distro
> maintainers to get together with Alec in a conversation and come to a
> decision as to what licensing scheme y'all want. I haven't really done
> much other than cleaning up the packaging and patches and a small
> bit of
> additional code, so whatever licensing y'all come up with is fine
> by me.

I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be
linked with any code, not just GPL...

-a

Re: [Cracklib-devel] cracklib license

From: Devin Reade <gdr@gn...> - 2007-10-02 15:04

I would like to see it under LGPL as well. I

think it is in everyone's

best interests to have as secure systems as possible, and I think tainting it via GPL will just make it less likely that the library gets used, and will not usually cause companies/developers to GPL the dependent code (where it is not already GPL).

I like GPL, I use it when I can, but I don't think that it's the correct license in this situation.

Devin

--

If it's sinful, it's more fun.

Re: [Cracklib-devel] cracklib license

From: Nalin Dahyabhai <nalin@re...> - 2008-01-28 16:32

On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:

>> Seems like the ideal thing here would be for you and the other distro
>> maintainers to get together with Alec in a conversation and come to a
>> decision as to what licensing scheme y'all want. I haven't really done
>> much other than cleaning up the packaging and patches and a small
>> bit of
>> additional

code, so whatever licensing y'all come up with is fine

>> by me.

>

> I am sympathetic. Guys, what do you reckon?

>

> What I am hearing so far is that LGPL makes sense, since it can be

> linked with any code, not just GPL...

My apologies for not chiming in in anything resembling a reasonable timeframe.

I'd also suggest the LGPL, for the reason you noted above. Alternately, GPLv2 with the option of using the library under a later version of the GPL would permit applications which were released under version 3 of the GPL to use the library, too, which would be sufficient for the packages which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

Cheers,

Nalin

Re: [Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2008-10-05 21:27

Attachments: Message as HTML

On Monday 28 January 2008, Nalin Dahyabhai wrote:

> On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:

>>> Seems like the ideal thing here would be for you and the other distro
>>> maintainers to get together with Alec in a conversation and come to a
>>> decision as to what licensing scheme y'all want. I haven't really done
>>> much other than cleaning up the packaging and patches and a small
>>> bit of
>>> additional code, so whatever licensing y'all come up with is fine
>>> by me.

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>> linked with any code, not just GPL...

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> timeframe.

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> GPL to use the library, too, which would be sufficient for the packages
> which are included in Fedora. FWIW, I'd personally lean toward LGPL.

>

> In any case, I thank you both for working on sorting this out.

looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
the change now ?

-mike

Re: [Cracklib-devel] cracklib license

From: Alec Muffett <alecm@cr...> - 2008-10-05 23:18

>> In any case, I thank you both for working on sorting this out.

>

> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
> the change now ?

yes. go for it. thanks++

-a

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-25 22:34

Attachments: Message as HTML

On Sunday 05 October

2008, Alec Muffett wrote:

>>> In any case, I thank you both for working on sorting this out.
>>
>> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we
>> make the change now ?
>
> yes. go for it. thanks++

Nathan Neulinger is the only one who can actually make said change ...
-mike

BELOW IS ORIGINAL LICENSING DISCUSSION RE CHANGING TO GPL from Artistic.

CrackLib was originally licensed with a variant of the Artistic license. In the interests of wider acceptance and more modern licensing, it was switched with the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed as of 2007-10-01 with the following email from Alec Muffett.

The below email references nneul@umr.edu address, as that is the address that was used at the time. For any future emails regarding this, please use nneul@neulinger.org.

From
alecm@crypticide.com Mon Oct 1 12:26:03 2007
Received: from umr-exproto2.cc.umr.edu ([131.151.0.192]) by UMR-CMAIL1.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
Mon, 1 Oct 2007 12:26:03 -0500
Received: from scansrv2.srv.mst.edu ([131.151.1.114]) by umr-exproto2.cc.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
Mon, 1 Oct 2007 12:26:02 -0500
Received: (qmail 8022 invoked from network); 1 Oct 2007 16:59:55 -0000

Received: from smtp1.srv.mst.edu (131.151.1.43)
by scanin-ipv6.srv.mst.edu with SMTP; 1 Oct 2007 16:59:55 -0000
Received: from spunkymail-mx8.g.dreamhost.com (mx1.spunky.mail.dreamhost.com [208.97.132.47])
by smtp1.srv.mst.edu (8.13.1/8.13.1) with ESMTP id 191Gxtpr020623
for <nneul@umr.edu>; Mon, 1 Oct 2007 11:59:55 -0500
Received: from rutherford.zen.co.uk (rutherford.zen.co.uk [212.23.3.142])
by spunkymail-mx8.g.dreamhost.com (Postfix) with ESMTP id 2C7734D311
for <nneul@neulinger.org>; Mon, 1 Oct 2007 09:59:50 -0700 (PDT)
Received: from
[82.68.43.14] (helo=[192.168.1.3])
by rutherford.zen.co.uk with esmtp (Exim 4.50)
id 1IcOcX-0004Qt-6L
for nneul@neulinger.org; Mon, 01 Oct 2007 16:59:49 +0000
Mime-Version: 1.0 (Apple Message framework v752.2)
In-Reply-To: <1b1b3fd80710010908k11dac0afp1f2dd471059ff9a4@mail.gmail.com>
References: <1190922867.3457.147.camel@localhost.localdomain>
<EC90713277D2BE41B7110CCD74E235CEF44F38@UMR-CMAIL1.umr.edu>
<1b1b3fd80710010908k11dac0afp1f2dd471059ff9a4@mail.gmail.com>
Content-Type: text/plain; charset=US-ASCII; delsp=yes; format=flowed
Message-Id: <117A1264-F6DC-4E25-B0DD-56FBFE6E9F@crypticide.com>
Content-Transfer-Encoding: 7bit
From: Alec Muffett <alecm@crypticide.com>
Subject: Re: cracklib license
Date: Mon, 1 Oct 2007 17:59:46 +0100
To: Nathan Neulinger <nneul@neulinger.org>
X-Mailer: Apple Mail (2.752.2)
X-Originating-Rutherford-IP: [82.68.43.14]
Return-Path: alecm@crypticide.com
X-OriginalArrivalTime: 01 Oct 2007 17:26:03.0008 (UTC) FILETIME=[2420C000:01C80450]
Status: RO
Content-Length:
585
Lines: 21

>
> ----- Forwarded message -----
> From: Neulinger, Nathan <nneul@umr.edu>
> Date: Sep 27, 2007 2:58 PM
> Subject: RE: cracklib license
> To: alecm@crypto.dircon.co.uk
>
> Any chance you could write me a self-contained email stating clearly
> that the license is being changed to GPL, so I could include that
> email
> in the repository and clean up the repository/tarballs? I have all the
> original discussion, but something succinct and self contained
> would be
> ideal.

The license for my code in the Cracklib distribution is henceforth GPL.

Happy now? :-)

-a

1.146 beecrypt 4.2.1

1.146.1 Available under license :

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Version 2, June 1991

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Any executables

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I would like to thank the following people (in alphabetical order):

- Seth Arnold, for contributing to the documentation.
- Daniel Black, (former) maintainer of the Gentoo GNU/Linux BeeCrypt package.
- Jan-Rudolph Bhrmann, for helping me get started on the 64-bit multi-precision integer library.
- Luca Filipozzi, (former) maintainer/packager of BeeCrypt for Debian GNU/Linux.
- Jeff Johnson, the guy behind RedHat's Package Manager, who has inspired and contributed to many of the changes for version 3.0.0. He also provided the new hash functions for version 4.2.0. 73 de Bob.
- Anibal Monsalve Salazar, (current) maintainer/packager of BeeCrypt for Debian GNU/Linux.
- Jon Sturgeon, bug hunter extraordinaire.

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- ARM Ltd, for donating a copy of "ARM Architecture Reference Manual".
- HP/Compaq, for their testdrive program, which gave me the opportunity to test and BeeCrypt on many new platforms.
- FSF France, for providing me with access to the GCC Compile Farm.

- SourceForge, for their excellent open source development platform.

Last but not least: thanks to everyone who provided bits of information, reported bugs, provided feedback, or works on including BeeCrypt in any other distros.

If I've missed anyone, it's due to oversight. Drop me a line and I'll rectify the situation as quickly as possible.

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- * Merlin Hughes
Original author of sensord.
- * Bob Schlaermann
Dynamic chip feature detection (a.k.a. generic chip support) in
libsensors and sensors.
- * Mark M. Hoffman
Many improvements to the libsensors configuration file scanner.
- * Jean Delvare
New libsensors API, and migration of sensors and sensord thereto.
Many optimizations in libsensors and sensors.

Configuration file converter.
Rewrite of sensors-detect.
Support for multiple configuration files in libsensors.

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@end smallexample
```

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1.152 dhcp 4.4.1

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* `configure.ac`, `Makefile.am`: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

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* `atf-c/ui.c`: The `format_paragraph` and `format_text` functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

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* `atf-c++/detail/io.hpp`, `atf-c++/detail/io.cpp`, `atf-c++/detail/io_test.cpp`: These files were derived from the `file_handle`, `systembuf`, `pipe` and `pistream` classes and tests found in the Boost.Process library.

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* `admin/check-style.sh`,

admin/check-style-common.awk,
admin/check-style-cpp.awk, admin/check-style-shell.awk: These files,
except the first one, were first implemented in the Buildtool project.
They were later adapted to be part of Boost.Process and, during that
process, the shell script was created.

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-->

<!-- \$Id\$ -->

<!-- Generate ISC copyright comments from Docbook copyright metadata. -->

<xsl:stylesheet version="1.0"

xmlns:xsl="http://www.w3.org/1999/XSL/Transform"

xmlns:xi="http://www.w3.org/2001/XInclude"

xmlns:db="http://docbook.org/ns/docbook">

<xsl:template name="isc.copyright.format">

<xsl:param name="text"/>

<xsl:value-of select="\$isc.copyright.leader"/>

<xsl:value-of select="normalize-space(substring-before(\$text, '
'))"/>

<xsl:text>
</xsl:text>

<xsl:variable name="rest" select="substring-after(\$text, '
')"/>

<xsl:if test="translate(\$rest, '	 ', '')">

<xsl:call-template name="isc.copyright.format">

<xsl:with-param

name="text" select="\$rest"/>

</xsl:call-template>

</xsl:if>

</xsl:template>

<xsl:variable name="isc.copyright.text">

<xsl:text>

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</xsl:text>

</xsl:variable>

<xsl:variable name="isc.copyright">

<xsl:call-template name="isc.copyright.format">

<xsl:with-param name="text">

<xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">

<xsl:text>Copyright (C) </xsl:text>

```

<xsl:call-template name="copyright.years">
  <xsl:with-param name="years" select="year"/>
</xsl:call-template>
<xsl:text> </xsl:text>
<xsl:value-of select="holder"/>
  <xsl:value-of select="$isc.copyright.breakline"/>
<xsl:text>&#10;</xsl:text>
</xsl:for-each>
<xsl:value-of select="$isc.copyright.text"/>
  </xsl:with-param>
</xsl:call-template>
</xsl:variable>
</xsl:stylesheet>

<!--
- Local variables:
- mode: sgml
- End:
-->
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```

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- c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
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- e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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<signature of Ty Coon>, 1 April 1990

Ty Coon, President of Vice

That's all there is to it!

1.156 libcroco 0.6.12

1.156.1 Available under license :

```
/* -*- Mode: C; indent-tabs-mode:nil; c-basic-offset: 8; *- */
```

```
/*
```

```
* This file is part of The Croco Library
```

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*
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[This is the first released version of the library GPL. It is
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1.157 rpm 4.14.2

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```
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`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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Any executables

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/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)

* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)

*

* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.

* <http://www.hypermall.com/>

* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY

* interrupts us (except possibly for removal/insertion of the cable?)

* 10/4/97 - began heavy inline documentation of the code. Corrected typos

* and spelling mistakes.

* 10/5/97 - added code to handle PHY interrupts, disable PHY on

* loss of link, and correctly re-enable PHY when link is

* re-established. (put back CFG_PHYIE)

*

* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.

*

* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997

*

* Linux driver for the IDT77201 NICStAR PCI ATM controller.

* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;

* see init_nicstar() for PHY initialization to change this. This driver

* expects the Linux ATM stack to support scatter-gather

lists

* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.

*

* Implementing minimal-copy of received data:

* IDT always receives data into a small buffer, then large buffers

* as needed. This means that data must always be copied to create

* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)

* Fix is simple: make large buffers large enough to hold entire

* SDU, and leave <small_buffer_data> bytes empty at the start. Then

- * copy small buffer contents to head of large buffer.
- * Trick is to avoid fragmenting Linux, due to need for a lot of large buffers. This is done by 2 things:
- * 1) skb->destructor / skb->atm.recycle_buffer combined, allow nicstar_free_rx_skb to be called to recycle large data buffers
- * 2) skb_clone of received buffers
- * See nicstar_free_rx_skb and linearize_buffer for implementation details.

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* M. Welsh, 6 July 1996

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1.162 cryptsetup 2.0.4

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1.163 xfsprogs 4.18.0

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* `configure.ac`, `Makefile.am`: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

Author: Julio Merino <jmmv@users.sourceforge.net>

* `atf-c/ui.c`: The `format_paragraph` and `format_text` functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

* `atf-c++/detail/io.hpp`, `atf-c++/detail/io.cpp`, `atf-c++/detail/io_test.cpp`: These files were derived from the `file_handle`, `systembuf`, `pipe` and `pistream` classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* `admin/check-style.sh`,
`admin/check-style-common.awk`,
`admin/check-style-cpp.awk`, `admin/check-style-shell.awk`: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

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=====
vim: filetype=text:textwidth=75:expandtab:shiftwidth=2:softtabstop=2
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-->

<!-- \$Id\$ -->

<!-- Generate ISC copyright comments from Docbook copyright metadata. -->

<xsl:stylesheet version="1.0"

xmlns:xsl="http://www.w3.org/1999/XSL/Transform"

xmlns:xi="http://www.w3.org/2001/XInclude"

xmlns:db="http://docbook.org/ns/docbook">

<xsl:template name="isc.copyright.format">

```

<xsl:param name="text"/>
<xsl:value-of select="$isc.copyright.leader"/>
<xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
<xsl:text>&#10;</xsl:text>
<xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
<xsl:if test="translate($rest, '&#9;&#32;', '')">
  <xsl:call-template name="isc.copyright.format">
    <xsl:with-param
name="text" select="$rest"/>
  </xsl:call-template>
</xsl:if>
</xsl:template>

```

```

<xsl:variable name="isc.copyright.text">
  <xsl:text>
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    License, v. 2.0. If a copy of the MPL was not distributed with this
    file, You can obtain one at http://mozilla.org/MPL/2.0/.
  </xsl:text>
</xsl:variable>

```

```

<xsl:variable name="isc.copyright">
  <xsl:call-template name="isc.copyright.format">
    <xsl:with-param name="text">
      <xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">
        <xsl:text>Copyright (C) </xsl:text>
        <xsl:call-template name="copyright.years">
          <xsl:with-param name="years" select="year"/>
        </xsl:call-template>
        <xsl:text> </xsl:text>
        <xsl:value-of select="holder"/>
        <xsl:value-of select="$isc.copyright.breakline"/>
        <xsl:text>&#10;</xsl:text>
      </xsl:for-each>
      <xsl:value-of select="$isc.copyright.text"/>
    </xsl:with-param>
  </xsl:call-template>
</xsl:variable>
</xsl:stylesheet>

```

<!--

- Local variables:
- mode: sgml
- End:

-->

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1.165 i2c-tools 4.0

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1.166 openssl 1.0.2s

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include/bsd/timeconv.h
include/bsd/vis.h
man/bitstring.3bsd
man/errc.3bsd
man/explicit_bzero.3bsd
man/fgetln.3bsd
man/fgetwln.3bsd
man/fpurge.3bsd
man/funopen.3bsd
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man/heapsort.3bsd
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src/merge.c
src/nlist.c
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man/fmtcheck.3bsd

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man/stringlist.3bsd

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man/readpassphrase.3bsd

man/strncpy.3bsd

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src/arc4random_linux.h

src/arc4random_openbsd.h

src/arc4random_uniform.c

src/arc4random_unix.h

src/arc4random_win.h

src/closefrom.c

src/getentropy_aix.c

src/getentropy_bsd.c

src/getentropy_hpux.c

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src/getentropy_linux.c

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or

linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a)

Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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1.171 lvm2 2.02.180

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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1.172 libvirt 4.7.0

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This license, the Lesser General Public License, applies to some specially designated software packages--typically libraries--of the Free Software Foundation and other authors who decide to use it. You can use it too, but we suggest you first think carefully about whether this license or the ordinary General Public License is the better strategy to use in any particular case, based on the explanations below.

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free

programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
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- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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a) Accompany

the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

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Version 1.1 - 26 February 2007

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1.173 pango 1.42.4

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Version 2, June 1991

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[This is the first released version of the library GPL. It is
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Preamble

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Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference

between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

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b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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1.174 cairo 1.14.12

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```
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/* cairo - a vector graphics library with display and print output
```

```
*
```

```
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```

```
*
```

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```

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```

```
*
```

```
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```

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*
```

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* Contributor(s):
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```

```
* Carl Worth <cworth@cworth.org>
```

```
*/
```

```
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Version 3, 29 June 2007

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1.175 icu 62

1.175.1 Available under license :

```
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## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation
rules
%. $(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
```

```
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
```

```
## Dependency rules
```

```
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s/^(.*)\.o[ :]*/\1.o $@ : /g'" > $@; \
[ -s $@ ] || rm -f $@'
```

```
%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s/^(.*)\.o[ :]*/\1.o $@ : /g'" > $@; \
[ -s $@ ] || rm -f $@'
```

```
## Versioned libraries rules
```

```
%.${SO}.${SO_TARGET_VERSION_MAJOR}: %.${SO}.${SO_TARGET_VERSION}
$(RM) $@ && ln -s ${<F} $@
%.${SO}: %.${SO}.${SO_TARGET_VERSION_MAJOR}
$(RM) $@ && ln -s ${*F}.${SO}.${SO_TARGET_VERSION} $@
```

```
## Bind internal references
```

```
# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic
```

```
# Dependencies [i.e. map files] for the final library
BIR_DEPS=
```

```
## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =
```

```
## End BSD-specific setup
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1. Source Code.

The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The "System

Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

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```
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Version 3, 29 June 2007

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```
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1.177 e2fsprogs 1.44.3

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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Sources were obtained from <http://sourceforge.net/projects/e2fsprogs>

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#

This is a Makefile stub which handles the creation of BSD shared # libraries.

#

In order to use this stub, the following makefile variables must be defined.

#

BSDLIB_VERSION = 1.0

BSDLIB_IMAGE = libce

BSDLIB_MYDIR = et

```

# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#

all:: image

real-subdirs:: Makefile
@echo " MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image: $(BSD_LIB)

$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LD_FLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) \
`echo $(my_dir) | sed -e 's/lib/;"/$(BSD_LIB) $(BSD_LIB))

install-shlibs install: $(BSD_LIB)
@echo " INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) \
$(DESTDIR)$$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)

install-strip: install

install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall:
$(RM) -f $(DESTDIR)$$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean:
$(RM)
-rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)

```

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This is the Debian GNU/Linux prepackaged version of the translation files of the EXT2 file system utilities. The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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Index: tdbsa/tdb.c

=====

--- tdbsa.orig/tdb.c

+++ tdbsa/tdb.c

@@ -4,11 +4,11 @@ Rev: 23371

Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)

*/

/*

- Unix SMB/CIFS implementation.

+ trivial database library - standalone version

- trivial database library - private includes

-

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Gadi Oxman, August 1995

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```

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```

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Theodore Ts'o
23-June-2007

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This is the Debian GNU/Linux prepackaged version of the EXT2 file system utilities (e2fsck, mke2fs, etc.). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

Sources were obtained from <http://sourceforge.net/projects/e2fsprogs>

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

<http://sourceforge.net/projects/e2fsprogs>

Upstream Author: Theodore Ts'o <tytso@mit.edu>

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1.178 dtc 1.4.7

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=====

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David Gibson <david@gibson.dropbear.id.au>

(principal original author of dtc and libfdt)

2 November 2007

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1.179 websocketpp 0.8.1

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Bundled Libraries:

***** Base 64 Library (base64/base64.hpp) *****

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base64.cpp and base64.h

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Ren Nyffenegger rene.nyffenegger@adp-gmbh.ch

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L. Peter Deutsch
ghost@aladdin.com

***** UTF8 Validation logic (utf8_validation.hpp) *****

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1.180 libseccomp 2.3.3

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1.182 openjsse 1.1.3

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1.185 iputils 20180629

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1.186 rpcbind 0.2.4

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* /opt/ws_local/PERMITS_SQL/1045960566_1592319068.67/0/pld-linux-rpcbind-auto-th-rpcbind-0-2-4-1-0-g743aa28-tar-gz/pld-linux-rpcbind-743aa28/rpcbind.spec

1.187 websocketpp 0.5.1

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base64.cpp and base64.h

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Ren Nyffenegger rene.nyffenegger@adp-gmbh.ch

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L. Peter Deutsch
ghost@aladdin.com

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1.188 libidn 2.0.5

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1.191 kmod 25+git0+aca4eca103

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1.193 mdadm 4.1

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```

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1.194 ncurses 6.1+20180630

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-- vile:txtmode fc=72

-- \$Id: COPYING,v 1.9 2020/02/08 13:34:12 tom Exp \$

Upstream source <https://invisible-island.net/ncurses/ncurses.html>

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Files: *

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Files: alocal.m4 package

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Files: doc/html/NCURSES-Programming-HOWTO.html

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1.195 pigz 2.4

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Jyrki Alakuijala
Frdric Kayser
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1.196 rdma-cm 28.1

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Upstream-Contact: Doug Ledford <dledford@redhat.com>,

Leon Romanovsky <Leon@kernel.org>

Source: <https://github.com/linux-rdma/rdma-core>

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* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/type/univ.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/codec/native/test_encoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/compat/test_integer.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/der/decoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/setup.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/ber/encoder.py
*
/opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/ber/decoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/__main__.py

```

```

* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/type/test_char.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/compat/__main__.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/codec/der/test_encoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/compat/test_octets.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/type/test_univ.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/native/decoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/codec/cer/__main__.py
*
/opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/type/test_opentype.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/type/namedtype.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/compat/integer.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/type/useful.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/native/encoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/error.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/ber/eoo.py

```

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ASN.1 library for Python

=====

```

.. toctree::
   :maxdepth: 1

```

Abstract Syntax Notation One (ASN.1

<http://en.wikipedia.org/wiki/Abstract_Syntax_Notation_1x>_) is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.

The `pyasn1` <<https://pypi.org/project/pyasn1/>>_ library implements ASN.1 support in pure-Python.

What is ASN.1

ASN.1 is a large, arguably over-engineered and extremely old data modelling and serialisation tool. It is probably among the first serialisation protocols in the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first electronic mail (known as X.400). Later on it was split off the e-mail application and became a stand-alone tech still being actively supported by its designers and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the first, unsuccessful, version of the Internet. You can read many interesting discussions <https://news.ycombinator.com/item?id=8871453> on that topic.

In the following years, generations of software engineers tackled the serialisation problem many times. We can see that in Google's `ProtoBuffers` <https://developers.google.com/protocol-buffers/> or `FlatBuffers` <https://google.github.io/flatbuffers/>, for example. Interestingly, many new takes on binary protocol design do not depart far from ASN.1 from technical perspective. It's more of a matter of striking a balance between processing overhead, wire format overhead and human readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

- * Data types: the standard introduces a collection of basic data types (integers, bits, strings, arrays and records) that can be used for describing arbitrarily complex, nested data structures.
- * Serialisation protocols: the above data structures could be converted into a series of octets for storage or transmission over the wire as well as recovered back into their structured form. The system is fully agnostic to hardware architectures differences.
- * Schema language: ASN.1 data structures could be described in terms of a schema language for ASN.1 compiler to turn it into platform-specific implementation.

ASN.1 applications

Being an old and generally successful standard, ASN.1 is widely adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:

- * Signaling standards for the public switched telephone network (SS7 family)
- * Network management standards (SNMP, CMIP)

- * Directory standards (X.500 family, LDAP)
- * Public Key Infrastructure standards (X.509, etc.)
- * PBX control (CSTA)
- * IP-based Videoconferencing (H.323 family)
- * Biometrics (BIP, CBEFF, ACBio)
- * Intelligent transportation (SAE J2735)
- * Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

ASN.1 gotchas

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the ``statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>`_` on discovered security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

Documentation

```
.. toctree::
   :maxdepth: 2

   /pyasn1/contents
```

Use case

```
.. toctree::
   :maxdepth: 2

   /example-use-case
```

Download & Install

```
.. toctree::
   :maxdepth: 2

   /download
```

Changes

All changes and release history is maintained in changelog. There you could also download the latest unreleased pyasn1 tarball containing the latest fixes and improvements.

..

toctree::

:maxdepth: 1

/changelog

License

The PyASN1 software is distributed under 2-clause BSD License.

.. toctree::

:maxdepth: 2

/license

Getting help

Please, file your `issues` <<https://github.com/etingof/pyasn1/issues>>` _

and `PRs` <<https://github.com/etingof/pyasn1/pulls>>` _ at GitHub.

Alternatively, you could ask for help at

`Stack Overflow` <<http://stackoverflow.com/questions/tagged/pyasn1>>` _

or search

`pyasn1-users` <<https://lists.sourceforge.net/lists/listinfo/pyasn1-users>>` _

mailing list archive.

Books on ASN.1

The pyasn1 implementation is largely based on reading up the following awesome books:

* `ASN.1 - Communication between heterogeneous systems` <<http://www.oss.com/asn1/dubuisson.html>>` _ by Olivier Dubuisson

* `ASN.1 Complete` <<http://www.oss.com/asn1/resources/books-whitepapers-pubs/larmouth-asn1-book.pdf>>` _ by Prof John Larmouth

Here you can get the official standards which is hard to read:

* `ITU standards`

<<http://www.itu.int/ITU-T/studygroups/com17/languages/X.680-X.693-0207w.zip>>` _

On the other end of the readability spectrum, here is a quick and sweet write up:

* `A Layman's Guide to a Subset of ASN.1, BER, and DER <ftp://ftp.rsasecurity.com/pub/pkcs/ascii/layman.asc>`
by Burton S. Kaliski

If you are working with ASN.1, we'd highly recommend reading a proper book on the subject.

Found in path(s):

* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/docs/source/contents.rst

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ASN.1 library for Python

[![PyPI](https://img.shields.io/pypi/v/pyasn1.svg?maxAge=2592000)](https://pypi.org/project/pyasn1)
[![Python Versions](https://img.shields.io/pypi/pyversions/pyasn1.svg)](https://pypi.org/project/pyasn1/)
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[![Coverage Status](https://img.shields.io/codecov/c/github/etingof/pyasn1.svg)](https://codecov.io/github/etingof/pyasn1)
[![GitHub license](https://img.shields.io/badge/license-BSD-blue.svg)](https://raw.githubusercontent.com/etingof/pyasn1/master/LICENSE.txt)

This is a free and open source implementation of ASN.1 types and codecs as a Python package. It has been first written to support particular protocol (SNMP) but then generalized to be suitable for a wide range of protocols based on

[ASN.1 specification](https://www.itu.int/rec/dologin_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items).

Features

- *
Generic implementation of ASN.1 types (X.208)
- * Standards compliant BER/CER/DER codecs
- * Dumps/loads ASN.1 structures from Python types
- * 100% Python, works with Python 2.4 up to Python 3.7
- * MT-safe
- * Contributed ASN.1 compiler [Asn1ate](https://github.com/kimgr/asn1ate)

Why using pyasn1

ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing

the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1.

[Communication between heterogeneous systems](<http://www.oss.com/asn1/dubuisson.html>)

by

Olivier Dubuisson is one of those high quality books freely available on the Internet.

The pyasn1 package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

How to use pyasn1

With pyasn1 you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```
```bash
Record ::= SEQUENCE {
 id INTEGER,
 room [0] INTEGER OPTIONAL,
 house [1] INTEGER DEFAULT 0
}
```
```

Could be expressed in pyasn1 like this:

```
```python
class Record(Sequence):
 componentType = NamedTypes(
 NamedType('id', Integer()),
 OptionalNamedType(
 'room', Integer().subtype(
 implicitTag=Tag(tagClassContext, tagFormatSimple, 0)
)
),
 DefaultedNamedType(
 'house',
 Integer(0).subtype(
 implicitTag=Tag(tagClassContext, tagFormatSimple, 1)
)
)
)
```
```

```
)  
...  

```

It is in the spirit of ASN.1 to take abstract data description and turn it into a programming language specific form. Once you have your ASN.1 data structure expressed in Python, you can use it along the lines of similar Python type (e.g. ASN.1 `SET` is similar to Python `dict`, `SET OF` to `list`):

```
```python  
>>> record = Record()
>>> record['id'] = 123
>>> record['room'] = 321
>>> str(record)
Record:
id=123
room=321
>>>
...

```

Part of the power of ASN.1 comes from its serialisation features. You can serialise your data structure and send it over the network.

```
```python  
>>> from pyasn1.codec.der.encoder import encode  
>>> substrate = encode(record)  
>>> hexdump(substrate)  
00000: 30 07 02 01 7B 80 02 01 41  
...  

```

Conversely, you can turn serialised ASN.1 content, as received from network or read from a file, into a Python object which you can introspect, modify, encode and send back.

```
```python  
>>> from pyasn1.codec.der.decoder import decode
>>> received_record, rest_of_substrate = decode(substrate, asn1Spec=Record())
>>>
>>> for field in received_record:
>>> print('{} is {}'.format(field, received_record[field]))
id is 123
room is 321
house is 0
>>>
>>> record == received_record
True
>>> received_record.update(room=123)

```

```
>>> substrate = encode(received_record)
>>> hexdump(substrate)
00000: 30 06 02 01 7B 80 01 7B
...
```

The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, list, dict etc.). But ASN.1 types exhibit more complicated behaviour. To make life easier for a Pythonista, they can turn their pyasn1 classes into Python built-ins:

```
```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)
{'id': 123, 'room': 321, 'house': 0}
...
```
```

Or vice-versa -- you can initialize an ASN.1 structure from a tree of Python objects:

```
```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id':
123, 'room': 321, 'house': 0}, asn1Spec=Record())
>>> str(record)
Record:
id=123
room=321
>>>
...
```
```

With ASN.1 design, serialisation codecs are decoupled from data objects, so you could turn every single ASN.1 object into many different serialised forms. As of this moment, pyasn1 supports BER, DER, CER and Python built-ins codecs. The extremely compact PER encoding is expected to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the [documentation](<http://snmplabs.com/pyasn1/>), compiled ASN.1 modules for different protocols and file formats could be found in the pyasn1-modules [repo](<https://github.com/etingof/pyasn1-modules>).

How to get pyasn1

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The pyasn1 package is distributed under terms and conditions of 2-clause BSD [license](<http://snmplabs.com/pyasn1/license.html>). Source code is freely available as a GitHub [repo](<https://github.com/etingof/pyasn1>).

You could `pip install pyasn1` or download it from [PyPI](https://pypi.org/project/pyasn1).

If

something does not work as expected,  
[open an issue](https://github.com/etingof/pyasn1/issues) at GitHub or  
post your question [on Stack Overflow](https://stackoverflow.com/questions/ask)  
or try browsing pyasn1  
[mailing list archives](https://sourceforge.net/p/pyasn1/mailman/pyasn1-users/).

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\* /opt/ws\_local/PERMITS\_SQL/1063016395\_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/README.md

## 1.205 pycparser 2.18

### 1.205.1 Available under license :

pycparser -- A C parser in Python

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- the files in src/spy/
- the files in UnitaryTests/

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Paul Schmidt  
Tim Wegner

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1. [1]Takao Abe <takao\_abe@xurb.jp> Clock driver for JJY receivers
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  6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
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  9. [10]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support
  10. [11]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features
- in sntp
11. [12]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
  12. [13]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
  13. [14]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
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  17. [18]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
  18. [19]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
  19. [20]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing



20. [21]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite
21. [22]Claas Hilbrecht <neoclock4x@linum.com> NeoClock4X clock driver
22. [23]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
23. [24]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
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43. [47]Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code maintenance
44. [48]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling
45. [49]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
46. [50]Wilfredo Snchez <wsanchez@apple.com> added support for NetInfo
47. [51]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
48. [52]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory

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  51. [55]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
  52. [56]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure  
makeover, various other bits (see the ChangeLog)
  53. [57]Kenneth Stone <ken@sdd.hp.com> HP-UX port
  54. [58]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
  55. [59]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK  
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  57. [61]Loganaden Velvindron <loganaden@gmail.com> Sandboxing  
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## References

1. mailto:%20takao\_abe@xurb.jp
2. mailto:%20mark\_andrews@isc.org
3. mailto:%20altmeier@atsoft.de
4. mailto:%20vbais@mailman1.intel.co
5. mailto:%20kirkwood@striderfm.intel.com
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23. mailto:%20glenn@herald.usask.ca
24. mailto:%20iglesias@uci.edu
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## 1.208 ipmi-tool 1.8.18

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## 1.209 kerberos 1.16.1

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cmd/krb5/ipropl/ipropl_hdr.h
cmd/krb5/kadmin/server/ipropl_svc.c
cmd/krb5/kproplog/kproplog.c
cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
lib/libgss/g_canon_name.c
lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
lib/libgss/g_delete_sec_context.c
lib/libgss/g_dsp_name.c
lib/libgss/g_dsp_status.c
lib/libgss/g_dup_name.c
lib/libgss/g_exp_sec_context.c
lib/libgss/g_export_name.c
lib/libgss/g_glue.c
lib/libgss/g_imp_name.c
lib/libgss/g_imp_sec_context.c
```

lib/libgss/g\_init\_sec\_context.c  
lib/libgss/g\_initialize.c  
lib/libgss/g\_inquire\_context.c  
lib/libgss/g\_inquire\_cred.c  
lib/libgss/g\_inquire\_names.c  
lib/libgss/g\_process\_context.c  
lib/libgss/g\_rel\_buffer.c  
lib/libgss/g\_rel\_cred.c  
lib/libgss/g\_rel\_name.c  
lib/libgss/g\_rel\_oid\_set.c  
lib/libgss/g\_seal.c  
lib/libgss/g\_sign.c  
lib/libgss/g\_store\_cred.c  
lib/libgss/g\_unseal.c  
lib/libgss/g\_userok.c  
lib/libgss/g\_utils.c  
lib/libgss/g\_verify.c  
lib/libgss/gssd\_pname\_to\_uid.c  
uts/common/gssapi/include/gssapi\_err\_generic.h  
uts/common/gssapi/include/mechglueP.h

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/* tests/shlib/t_loader.c */
/*
```

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in "src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi\_err\_generic.et  
lib/gssapi/mechglue/g\_accept\_sec\_context.c  
lib/gssapi/mechglue/g\_acquire\_cred.c  
lib/gssapi/mechglue/g\_canon\_name.c  
lib/gssapi/mechglue/g\_compare\_name.c  
lib/gssapi/mechglue/g\_context\_time.c  
lib/gssapi/mechglue/g\_delete\_sec\_context.c  
lib/gssapi/mechglue/g\_dsp\_name.c  
lib/gssapi/mechglue/g\_dsp\_status.c  
lib/gssapi/mechglue/g\_dup\_name.c  
lib/gssapi/mechglue/g\_exp\_sec\_context.c  
lib/gssapi/mechglue/g\_export\_name.c  
lib/gssapi/mechglue/g\_glue.c  
lib/gssapi/mechglue/g\_imp\_name.c  
lib/gssapi/mechglue/g\_imp\_sec\_context.c  
lib/gssapi/mechglue/g\_init\_sec\_context.c  
lib/gssapi/mechglue/g\_initialize.c

lib/gssapi/mechglue/g\_inquire\_context.c  
lib/gssapi/mechglue/g\_inquire\_cred.c  
lib/gssapi/mechglue/g\_inquire\_names.c  
lib/gssapi/mechglue/g\_process\_context.c  
lib/gssapi/mechglue/g\_rel\_buffer.c  
lib/gssapi/mechglue/g\_rel\_cred.c  
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lib/gssapi/mechglue/g\_unseal.c  
lib/gssapi/mechglue/g\_userok.c  
lib/gssapi/mechglue/g\_utils.c  
lib/gssapi/mechglue/g\_verify.c  
lib/gssapi/mechglue/gssd\_pname\_to\_uid.c  
lib/gssapi/mechglue/mglueP.h  
lib/gssapi/mechglue/oid\_ops.c  
lib/gssapi/spnego/gssapiP\_spnego.h  
lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation,  
including  
the following new or changed files:

include/ipropr\_hdr.h  
kadmin/server/ipropr\_svc.c  
lib/kdb/ipropr.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
lib/kdb/kdb\_log.h  
lib/krb5/error\_tables/kdb5\_err.et  
kprop/kpropd\_rpc.c  
kprop/kproplog.c

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## 1.210 syslog-ng 3.16.1

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# 1.211 traceroute 2.1.0

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```

```
Benoit Sigoure <tsuna@google.com>
```

```
Bogdan Pilocca <boo@google.com>
```

```
Chandler Carruth <chandlerc@google.com>
```

```
Dave MacLachlan <dmaclach@gmail.com>
```

```
David Anderson <danderson@google.com>
```

```
Dean Sturtevant
```

```
Gene Volovich <gv@cite.com>
```

```
Hal Burch <gmock@hburch.com>
```

```
Jeffrey Yasskin <jyasskin@google.com>
```

Jim Keller <jimkeller@google.com>  
Joe Walnes <joe@truemesh.com>  
Jon Wray <jwray@google.com>  
Keir Mierle <mierle@gmail.com>  
Keith Ray <keith.ray@gmail.com>  
Kostya Serebryany <kcc@google.com>  
Lev Makhlis  
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Mario Tanev <radix@google.com>  
Mark Paskin  
Markus Heule <markus.heule@gmail.com>  
Matthew Simmons <simmonmt@acm.org>  
Mike Bland <mbland@google.com>  
Neal Norwitz <nnorwitz@gmail.com>  
Nermin Ozkiranartli <nermin@google.com>  
Owen  
Carlsen <ocarlsen@google.com>  
Paneendra Ba <paneendra@google.com>  
Paul Menage <menage@google.com>  
Piotr Kaminski <piotrk@google.com>  
Russ Rufer <russ@pentad.com>  
Sverre Sundsdal <sundsda@gmail.com>  
Takeshi Yoshino <tyoshino@google.com>  
Vadim Berman <vadimb@google.com>  
Vlad Losev <vladl@google.com>  
Wolfgang Klier <wklier@google.com>  
Zhanyong Wan <wan@google.com>  
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Balzs Dn <balazs.dan@gmail.com>  
Bharat Mediratta <bharat@menalto.com>  
Chandler Carruth <chandlerc@google.com>  
Chris Prince <cprince@google.com>  
Chris Taylor <taylorc@google.com>  
Dan Egnor <egnor@google.com>  
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Rainer Klaffenboeck <rainer.klaffenboeck@dynatrace.com>  
Russ Cox <rsc@google.com>  
Russ Rufer <russ@pentad.com>  
Sean Mcafee <eefacm@gmail.com>  
Sigurur sgeirsson <siggi@google.com>  
Tracy Bialik <tracy@pentad.com>  
Vadim Berman <vadimb@google.com>  
Vlad Losev <vladl@google.com>  
Zhanyong Wan <wan@google.com>  
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|                | GL/gltext.h        | Khronos        |
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Mesa 3-D  
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include/GL/glxext.h
include/GL/wglxext.h :
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## jopt-simple v5.0.4

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## 1.220 traceroute 2.1.0-r0

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## 1.221 linux-kernel 4.18.45

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/\* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)

\* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)

\*

\* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.

\* <http://www.hypermall.com/>

\* 10/1/97 - commented out CFG\_PHYIE bit - we don't care when the PHY

\* interrupts us (except possibly for removal/insertion of the cable?)

\* 10/4/97 - began heavy inline documentation of the code. Corrected typos

\* and spelling mistakes.

\* 10/5/97 - added code to handle PHY interrupts, disable PHY on

\* loss of link, and correctly re-enable PHY when link is

\* re-established. (put back CFG\_PHYIE)

\*

\* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.

\*

\* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997

\*

\* Linux driver for the IDT77201 NICStAR PCI ATM controller.

\* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;

\* see init\_nicstar() for PHY initialization to change this. This driver

\* expects the Linux ATM stack to support scatter-gather lists

\* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.

\*

\* Implementing minimal-copy of received data:

\* IDT always receives data into a small buffer, then large buffers

\* as needed. This means that data must always be copied to create

\* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)

\* Fix is simple: make large buffers large enough to hold entire

\* SDU, and leave <small\_buffer\_data> bytes empty at the start. Then

\* copy small buffer contents to head of large buffer.

\* Trick is to avoid fragmenting Linux, due to need for a lot of large

\* buffers. This is done by 2 things:

\* 1) skb->destructor / skb->atm.recycle\_buffer

\* combined, allow nicstar\_free\_rx\_skb to be called to

\* recycle large data buffers

\* 2) skb\_clone of received buffers

\* See nicstar\_free\_rx\_skb and linearize\_buffer for implementation

\* details.

\*

\*

\*

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\*

\* M. Welsh, 6 July 1996

\*

\*

\*/

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```
@c Local Variables:
```

```
@c ispell-local-pdict: "ispell-dict"
```

```
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```

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```
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## 1.226 newt 1.0.1

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\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-  
jar/org/jaxen/util/DescendantAxisIterator.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-  
jar/org/jaxen/util/PrecedingAxisIterator.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-  
jar/org/jaxen/dom/DocumentNavigator.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-  
jar/org/jaxen/util/DescendantOrSelfAxisIterator.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-  
jar/org/jaxen/util/AnccestorOrSelfAxisIterator.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/Navigator.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-  
jar/org/jaxen/util/FollowingAxisIterator.java

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- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/XPathHandler.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/PredicateSet.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultAdditiveExpr.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/NumberExpr.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultAbsolutePath.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/XPathSyntaxException.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultModExpr.java
- \*
- /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/helpers/DefaultXPathHandler.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/helpers/XPathReaderFactory.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/XPathReader.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/Operator.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultXPathFactory.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/base/XPathLexer.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/AdditiveExpr.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-

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jar/org/jaxen/expr/DefaultMultiplyExpr.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/saxpath/base/XPathReader.java
*
/opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/expr/DefaultMinusExpr.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/expr/DefaultLocationPath.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/saxpath/SAXPathEventSource.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/expr/DefaultPathExpr.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/saxpath/base/Token.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/saxpath/SAXPathException.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/expr/DefaultNumberExpr.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/expr/XPathFactory.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/saxpath/Axis.java
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```

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jar/org/jaxen/saxpath/base/Verifier.java

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- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/EqualityExpr.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/VariableReferenceExpr.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/Predicate.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/TrueFunction.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableDescendantOrSelfAxis.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/PositionFunction.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterablePrecedingSiblingAxis.java
- \*  
 /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultEqualityExpr.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/QualifiedName.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/util/SelfAxisIterator.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/NumberFunction.java

\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/ext/LowerFunction.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/util/FollowingSiblingAxisIterator.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/FalseFunction.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/pattern/NodeTypeTest.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableDescendantAxis.java  
 \*  
 /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/JaxenHandler.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableSelfAxis.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/SumFunction.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/util/LinkedIterator.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/NotFunction.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableParentAxis.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/dom/NamespaceNode.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/function/LocalNameFunction.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/Predicated.java  
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 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultMultiplicativeExpr.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/pattern/AnyChildNodeTest.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultLiteralExpr.java  
 \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/XPathSyntaxException.java  
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* Jaxen Project, please see <http://www.jaxen.org/>.
*
* Id
*/

```

Found in path(s):

```

* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/expr/DefaultFunctionCallExpr.java
* /opt/ws_local/PERMITS_SQL/1093498622_1600942376.18/0/jaxen-62-sources-
jar/org/jaxen/saxpath/base/TokenTypes.java

```

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```

/*

```

\$Id\$

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\*/

Found in path(s):

- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/NamedAccessNavigator.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableAxis.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/Context.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/XPath.java
- \*
- /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableAttributeAxis.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/DefaultNameStep.java
- \* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/ContextSupport.java

\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/expr/iter/IterableChildAxis.java  
\* /opt/ws\_local/PERMITS\_SQL/1093498622\_1600942376.18/0/jaxen-62-sources-jar/org/jaxen/javabean/DocumentNavigator.java

## 1.228 enum34 1.1.6

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## 1.229 idna 2.7



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```
"""
```

A library to support the Internationalised Domain Names in Applications (IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

```
"""
```

```
import io, sys
```

```
from setuptools import setup
```

```
def main():
```

```
 python_version = sys.version_info[:2]
```

```
 if python_version < (2,6):
```

```
 raise SystemExit("Sorry, Python 2.6 or newer required")
```

```
 package_data = { }
```

```
 exec(open('idna/package_data.py').read(), package_data)
```

```
 arguments = {
```

```
 'name': 'idna',
```

```
 'packages': ['idna'],
```

```
 'version': package_data['__version__'],
```

```
 'description': 'Internationalized Domain Names in Applications (IDNA)',
```

```
 'long_description': io.open("README.rst", encoding="UTF-8").read(),
```

```
 'author': 'Kim Davies',
```

```
 'author_email': 'kim@cynosure.com.au',
```

```
 'license': 'BSD-like',
```

```
 'url': 'https://github.com/kjd/idna',
```

```
 'classifiers': [
```

```
 'Development Status :: 5 - Production/Stable',
```

```
 'Intended Audience :: Developers',
```

```
 'Intended Audience :: System Administrators',
```

```
 'License :: OSI Approved :: BSD License',
```

```
 'Operating System :: OS Independent',
```

```
 'Programming Language :: Python',
```

```
 'Programming Language :: Python :: 2.6',
```

```
 'Programming Language :: Python :: 2.7',
```

```
 'Programming Language :: Python :: 3',
```

```
 'Programming Language :: Python :: 3.3',
```

```
 'Programming Language :: Python :: 3.4',
```

```
 'Programming Language :: Python :: 3.5',
```

```
 'Programming Language :: Python :: 3.6',
```

```

 'Topic :: Internet :: Name Service (DNS)',
 'Topic :: Software Development :: Libraries :: Python Modules',
 'Topic :: Utilities',
],
'test_suite':
'tests',
}

setup(**arguments)

if __name__ == '__main__':
 main()

```

Found in path(s):

\* /opt/cola/permits/1110765725\_1606850597.72/0/kjd-idna-v2-7-0-g5d76cb6-1-tar-gz/kjd-idna-77c8bce/setup.py

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-----

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\* /opt/cola/permits/1110765725\_1606850597.72/0/kjd-idna-v2-7-0-g5d76cb6-1-tar-gz/kjd-idna-77c8bce/LICENSE.rst

## 1.230 python3-asn1crypto 0.24.0

### 1.230.1 Available under license :

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## 1.231 ptyprocess 0.6.0

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## 1.232 mini-xml 2.6

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October 18, 2005

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## 1.233 ipmiutil 1.8.2

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Source: <http://ipmiutil.sourceforge.net>

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This is free software, and you are welcome to redistribute
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```

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<signature of Ty Coon>, 1 April 1989
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## 1.241 mod-security 2.9.0

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# 1.242 sysv-init 2.88

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The of the start-stop-daemon

- \* A rewrite
- of the original Debian's start-stop-daemon Perl script
- \* in C (faster - it is executed many times during system startup).
- \*
- \* Written by Marek Michalkiewicz <[marekm@i17linuxb.ists.pwr.wroc.pl](mailto:marekm@i17linuxb.ists.pwr.wroc.pl)>.
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## 1.243 shared-mime-info 1.1

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## 1.244 acl 2.2.52

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# 1.245 mpdecimal 2.4.2

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Version 3, 29 June 2007

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# 1.250 pciutils 3.6.2

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```

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```
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```

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## 1.251 tdb 1.44.3

### 1.251.1 Available under license :

This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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This is the Debian GNU/Linux prepackaged version of the static EXT2 file system consistency checker (e2fsck.static). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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```

This is a Makefile stub which handles the creation of BSD shared
libraries.

In order to use this stub, the following makefile variables must be defined.

BSDLIB_VERSION = 1.0
BSDLIB_IMAGE = libce
BSDLIB_MYDIR = et
BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
```

```
all:: image
```

```
real-subdirs:: Makefile
@echo " MKDIR pic"
@mkdir -p pic
```

```
BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic
```

```
image: $(BSD_LIB)
```

```
$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LD_FLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) \
`echo $(my_dir) | sed -e 's;lib/;;'/'$(BSD_LIB) $(BSD_LIB))
```

```
install-shlibs install:: $(BSD_LIB)
@echo " INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) \
$(DESTDIR)$BSDLIB_INSTALL_DIR/$(BSD_LIB)
@-$(LDCONFIG)
```

install-strip: install

install-shlibs-strip:: install-shlibs

```
uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$BSDLIB_INSTALL_DIR/$(BSD_LIB)
```

```
clean::
$(RM)
-rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)
```

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Index: tdbsa/tdb.c

=====

--- tdbsa.orig/tdb.c

+++ tdbsa/tdb.c

@@ -4,11 +4,11 @@ Rev: 23371

Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)

\*/

/\*

- Unix SMB/CIFS implementation.
- + trivial database library - standalone version
  
- trivial database library - private includes
- 
- Copyright (C) Andrew Tridgell            2005
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Gadi Oxman, August 1995

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before you ship. The release schedules for this package are flexible, if you give me enough lead time.

Theodore Ts'o  
23-June-2007

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

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## 1.254 freeradius 3.0.21

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Ackeret, Matt  
Adler, Mark  
Allbery, Russ  
Anderson, Jamie  
Andrew, Nick  
Andric, Dimitry  
Barron, Danny  
Bates, Tom  
Behan, Zdenk  
Bellis, Ray  
Benali, Elias  
Beverly, Jamie  
Boardman, Spider  
Bos, Sander  
Bostley, P.J.  
Bowes, Keith  
Boyce, Keith Garry  
Brantley, Michael  
Braun, Rob  
Bezina, Pavel  
Brooks, Piete  
Brown, Jerry  
Burr, Michael E  
Burton, Ross  
Bussjaeger, Andreas  
Calvin, Gary  
Campbell, Aaron  
Chazelas, Stephane  
Cheloha, Scott  
ek, Vtzslav  
Coleman, Chris  
Corzine, Deven T.  
Cusack, Frank  
Dai, Wei  
Dill, David  
Earickson, Jeff  
Eckhardt, Drew  
Edgington, Ben  
Esipovich, Marc  
Espie, Marc  
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Gelman, Stephen  
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Knox, Tim  
Komarnitsky, Alek O.  
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Levin, Dmitry V.  
Libby, Kendall  
Lobbes, Phillip E.  
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Makey, Jeff  
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Manner, Rbert  
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Michael, David  
Miller, Todd C.  
Minier, Loc  
Moffat, Darren  
Moldung, Jan Thomas  
Morris, Charles  
Mueller, Andreas  
Mller, Dworkin  
Nieuwsma, Jeff  
Nikitser, Peter A.  
Nussel, Ludwig  
Orbn, Lszl  
Ouellet, Jean-Philippe  
Paquet, Eric  
Paradis, Chantal  
Pasteleurs, Frederic  
Percival, Ted  
Perera, Andres  
Peron, Christian S.J.  
Peschel, Aaron  
Peslyak, Alexander  
Peterson, Toby  
Petten, Diego Elio  
Pickett, Joel  
Plotnick, Alex  
de Raadt, Theo  
Rasch, Gudleik  
Reid, Steve  
Richards, Matt  
Rossum, Guido van  
Rouillard, John P.  
Rowe, William A., Jr.  
Roy, Alain  
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Ryabinkin, Eygene  
Sato, Yuichi  
Snchez, Wilfredo  
Sanders, Miguel  
Sasaki, Kan  
Saucier, Jean-Francois  
Schoenfeld, Patrick  
Schuring, Arno  
Schwarze, Ingo



Scott, Dougal

Sieger, Nick

Simon, Thor Lancelot

Slemko, Marc

Smith, Andy

Sobrado, Igor

Soulen, Steven

Spangler, Aaron

Spradling, Cloyce D.

Spradling, Michael

Stier, Matthew

Stoeckmann, Tobias

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Torek, Chris

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Walsh, Dan

Warburton, John

Webb, Kirk

Wetzel, Timm

Wieringen, Marco van

Wilk, Jakub

Winiger, Gary

Wood, David

Zacarias, Gustavo

Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see <https://translationproject.org> for more details.

Albuquerque, Pedro

Blttermann, Mario

Bogusz, Jakub

Buo-ren, Lin

Casagrande, Milo  
Castro, Felipe  
Cho, Seong-ho  
Chornoivan, Yuri  
Diguez, Francisco  
Fontenelle, Rafael  
Garca-Fontes, Walter  
Gezer, Volkan  
Hamasaki, Takeshi  
Hamming, Peter  
Hansen, Joe  
Hantrais, Frdric  
Hein, Jochen  
Hufthammer, Karl Ove  
Jerovek, Damir  
Karvonen, Jorma  
Kazik, Duan  
Kelemen, Gbor  
Keeci, Mehmet  
Koir, Klemen  
Kozlov, Yuri  
Kramer, Jakob  
Krznar, Tomislav  
Marchal, Frdric  
Margeviius, Algimantas  
Maryanov, Pavel  
Florentina Muat  
Nurmi, Lauri  
Nikoli, Miroslav  
Nylander, Daniel  
Pan, Yi-Jyun  
Psa, Petr  
Puente, Enol  
Putanec, Boidar  
Qun, Trn Ngc  
Rasmussen, Sebastian  
Regueiro, Leandro  
Sarer, zgr  
Sendn, Abel  
erbnescu, Daniel  
Sikrom, ka  
Spingos, Dimitris  
Taniguchi, Yasuaki  
Tomat, Fbio  
r, Balzs  
Uranga, Mikel Olasagasti  
Vorotnikov, Artem  
Wang, Wylmer

Yang, Boyuan

The following people designed the artwork used on the sudo website:

Shield logo: Badger, Trent

Sandwich logo: Stillman, Mark

# 1.256 kerberos 1.16.1

## 1.256.1 Available under license :

```
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 */
```

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cmd/krb5/iprof/iprof_hdr.h
cmd/krb5/kadmin/server/iprofd_svc.c
cmd/krb5/kproplog/kproplog.c
cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
lib/libgss/g_canon_name.c
lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
lib/libgss/g_delete_sec_context.c
lib/libgss/g_dsp_name.c
lib/libgss/g_dsp_status.c
lib/libgss/g_dup_name.c
lib/libgss/g_exp_sec_context.c
lib/libgss/g_export_name.c
lib/libgss/g_glue.c
lib/libgss/g_imp_name.c
lib/libgss/g_imp_sec_context.c
lib/libgss/g_init_sec_context.c
lib/libgss/g_initialize.c
lib/libgss/g_inquire_context.c
lib/libgss/g_inquire_cred.c
lib/libgss/g_inquire_names.c
lib/libgss/g_process_context.c
lib/libgss/g_rel_buffer.c
lib/libgss/g_rel_cred.c
lib/libgss/g_rel_name.c
lib/libgss/g_rel_oid_set.c
lib/libgss/g_seal.c
lib/libgss/g_sign.c
```

lib/libgss/g\_store\_cred.c  
lib/libgss/g\_unseal.c  
lib/libgss/g\_userok.c  
lib/libgss/g\_utils.c  
lib/libgss/g\_verify.c  
lib/libgss/gssd\_pname\_to\_uid.c  
uts/common/gssapi/include/gssapi\_err\_generic.h  
uts/common/gssapi/include/mechglueP.h

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in "src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi\_err\_generic.et  
lib/gssapi/mechglue/g\_accept\_sec\_context.c  
lib/gssapi/mechglue/g\_acquire\_cred.c  
lib/gssapi/mechglue/g\_canon\_name.c  
lib/gssapi/mechglue/g\_compare\_name.c  
lib/gssapi/mechglue/g\_context\_time.c  
lib/gssapi/mechglue/g\_delete\_sec\_context.c

lib/gssapi/mechglue/g\_dsp\_name.c  
lib/gssapi/mechglue/g\_dsp\_status.c  
lib/gssapi/mechglue/g\_dup\_name.c  
lib/gssapi/mechglue/g\_exp\_sec\_context.c  
lib/gssapi/mechglue/g\_export\_name.c  
lib/gssapi/mechglue/g\_glue.c  
lib/gssapi/mechglue/g\_imp\_name.c  
lib/gssapi/mechglue/g\_imp\_sec\_context.c  
lib/gssapi/mechglue/g\_init\_sec\_context.c  
lib/gssapi/mechglue/g\_initialize.c  
lib/gssapi/mechglue/g\_inquire\_context.c  
lib/gssapi/mechglue/g\_inquire\_cred.c  
lib/gssapi/mechglue/g\_inquire\_names.c  
lib/gssapi/mechglue/g\_process\_context.c  
lib/gssapi/mechglue/g\_rel\_buffer.c  
lib/gssapi/mechglue/g\_rel\_cred.c  
lib/gssapi/mechglue/g\_rel\_name.c  
lib/gssapi/mechglue/g\_rel\_oid\_set.c  
lib/gssapi/mechglue/g\_seal.c  
lib/gssapi/mechglue/g\_sign.c  
lib/gssapi/mechglue/g\_store\_cred.c  
lib/gssapi/mechglue/g\_unseal.c  
lib/gssapi/mechglue/g\_userok.c  
lib/gssapi/mechglue/g\_utils.c  
lib/gssapi/mechglue/g\_verify.c  
lib/gssapi/mechglue/gssd\_pname\_to\_uid.c  
lib/gssapi/mechglue/mglueP.h  
lib/gssapi/mechglue/oid\_ops.c  
lib/gssapi/spnego/gssapiP\_spnego.h  
lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation,  
including  
the following new or changed files:

include/iprop\_hdr.h  
kadmin/server/ipropd\_svc.c  
lib/kdb/iprop.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
lib/kdb/kdb\_log.h  
lib/krb5/error\_tables/kdb5\_err.et  
kprop/kpropd\_rpc.c  
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```
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# 1.261 golang 1.12.12

## 1.261.1 Available under license :

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Please keep the list sorted.

Aamir Khan <syst3m.w0rm@gmail.com>
Aaron Cannon <cannona@fireantproductions.com>
Aaron France <aaron.l.france@gmail.com>
Aaron Jacobs <jacobsa@google.com>
Aaron Kemp <kemp.aaron@gmail.com>
Aaron Stein <aaronstein12@gmail.com>
Aaron Torres <tcboox@gmail.com>
Aaron Zinman <aaron@azinman.com>
Aarti Parikh <aarti.parikh@gmail.com>
Abdullah Al Maruf <mymail.maruf@gmail.com>
Abe Haskins <abeisgreat@abeisgreat.com>
Abhinav Gupta <abhinav.g90@gmail.com>
Adam Azarchs <adam.azarchs@10xgenomics.com>
Adam Bender <abender@google.com>
Adam Eijdenberg <adam@continusec.com>
Adam Harvey <aharvey@php.net>
Adam Kisala <adam.kisala@gmail.com>
```

Adam Langley <agl@golang.org>  
Adam Medzinski <adam.medzinski@gmail.com>  
Adam Shannon <adamkshannon@gmail.com>  
Adam Sindelar <adamsh@google.com>  
Adam Thomason <athomason@gmail.com>  
Adam Woodbeck <adam@woodbeck.net>  
Aditya Mukerjee <dev@chimeracoder.net>  
Adrian Hesketh <adrianhesketh@hushmail.com>  
Adrian  
Nos <nos.adrian@gmail.com>  
Adrian O'Grady <elpollouk@gmail.com>  
Adrien Bustany <adrien-xx-google@bustany.org>  
Adrien Petel <peteladrien@gmail.com>  
Acio Jnior <aeciodantasjunior@gmail.com>  
Aeneas Rekkas (arekkas) <aeneas@ory.am>  
Afanasev Stanislav <phpprogger@gmail.com>  
Agis Anastasopoulos <agis.anast@gmail.com>  
Agniva De Sarker <agnivade@yahoo.co.in>  
Ahmed Wahed <oneofone@gmail.com>  
Ahmet Alp Balkan <ahmetb@google.com>  
Ahmet Soormally <ahmet@mangomm.co.uk>  
Ahmy Yulrizka <yulrizka@gmail.com>  
Aiden Scandella <ai@uber.com>  
Ainar Garipov <gugl.zadolbal@gmail.com>  
Aishraj Dahal <aishraj@users.noreply.github.com>  
Akhil Indurti <contact@akhilindurti.com>  
Akihiro Suda <suda.kyoto@gmail.com>  
Akshat Kumar <seed@mail.nanosouffle.net>  
Alan Braithwaite <alan@ipaddr.org>  
Alan Donovan <adonovan@google.com>  
Alan Shreve <alan@inconshreveable.com>  
Albert Nigmatzianov <albertnigma@gmail.com>  
Albert Strasheim <fullung@gmail.com>  
Albert Yu <yukinying@gmail.com>  
Alberto Bertogli <albertito@blitiri.com.ar>  
Alberto  
Donizetti <alb.donizetti@gmail.com>  
Alberto Garca Hierro <alberto@garciahierro.com> <alberto.garcia.hierro@gmail.com>  
Aleksa Sarai <cyphar@cyphar.com>  
Aleksandar Dezelin <dezelin@gmail.com>  
Aleksandr Lukinykh <a.lukinykh@xsolla.com>  
Aleksandr Razumov <ar@cydev.ru>  
Alekseev Artem <a.artem060@gmail.com>  
Alessandro Arzilli <alessandro.arzilli@gmail.com>  
Alessandro Baffa <alessandro.baffa@gmail.com>  
Alex A Skinner <alex@lx.lc>  
Alex Brainman <alex.brainman@gmail.com>  
Alex Bramley <abramley@google.com>

Alex Browne <stephenalexbrowne@gmail.com>  
Alex Carol <alex.carol.c@gmail.com>  
Alex Jin <toalexjin@gmail.com>  
Alex Kohler <alexjohnkohler@gmail.com>  
Alex Myasoedov <msoedov@gmail.com>  
Alex Plugaru <alex@plugaru.org> <alexandru.plugaru@gmail.com>  
Alex Schroeder <alex@gnu.org>  
Alex Sergeev <abc@alexsergeev.com>  
Alex Vaghin <crhyme@google.com>  
Alexander Demakin <alexander.demakin@gmail.com>  
Alexander Dring <email@alex.d.ch>  
Alexander F Rdseth <alexander.rodseth@appeartv.com>  
Alexander  
Guz <kalimatas@gmail.com>  
Alexander Kauer <alexander@affine.space>  
Alexander Kucherenko <alxkchr@gmail.com>  
Alexander Larsson <alexander.larsson@gmail.com>  
Alexander Menzhinsky <amenzhinsky@gmail.com>  
Alexander Morozov <lk4d4math@gmail.com>  
Alexander Neumann <alexander@bumpen.de>  
Alexander Orlov <alexander.orlov@loxal.net>  
Alexander Pantyukhin <apantykhin@gmail.com>  
Alexander Polcyn <apolcyn@google.com>  
Alexander Reece <awreece@gmail.com>  
Alexander Surma <surma@surmair.de>  
Alexander Zhavnerchik <alex.vizor@gmail.com>  
Alexander Zillion <alex@alexzillion.com>  
Alexander Zolotov <goldifit@gmail.com>  
Alexandre Cesaro <alexandre.cesaro@gmail.com>  
Alexandre Fiori <fiorix@gmail.com>  
Alexandre Maari <draeron@gmail.com>  
Alexandre Normand <alexandre.normand@gmail.com>  
Alexandre Parentea <aubonbeurre@gmail.com>  
Alexandre Viau <alexandre@alexandreviau.net>  
Alexandru Mooi <brtznr@gmail.com>  
Alexei Sholik <alcosholik@gmail.com>  
Alexey Alexandrov <aalexand@google.com>  
Alexey Borzenkov  
<snaury@gmail.com>  
Alexey Naidonov <alexey.naidyonov@gmail.com>  
Alexey Neganov <neganovalexey@gmail.com>  
Alexey Palazhchenko <alexey.palazhchenko@gmail.com>  
Alexis Hildebrandt <surryhill@gmail.com>  
Alexis Hunt <lexer@google.com>  
Alexis Imperial-Legrand <ail@google.com>  
Ali Rizvi-Santiago <arizvisa@gmail.com>  
Aliaksandr Valialkin <valyala@gmail.com>  
Alif Rachmawadi <subosito@gmail.com>



Allan Simon <allan.simon@supinfo.com>  
Alok Menghrajani <alok.menghrajani@gmail.com>  
Aman Gupta <aman@tmm1.net>  
Amir Mohammad Saied <amir@gluegadget.com>  
Amr Mohammed <merodiro@gmail.com>  
Amrut Joshi <amrut.joshi@gmail.com>  
Anand K. Mistry <anand@mistry.ninja>  
Anders Pearson <anders@columbia.edu>  
Andr Carvalho <asantostc@gmail.com>  
Andre Nathan <andrenth@gmail.com>  
Andrea Nodari <andrea.nodari91@gmail.com>  
Andrea Spadaccini <spadaccio@google.com>  
Andreas Auernhammer <aead@mail.de>  
Andreas Jellinghaus <andreas@ionisiert.de> <anj@google.com>  
Andreas Litt <andreas.litt@gmail.com>  
Andrei Gherzan <andrei@resin.io>  
Andrei  
Korzhevskii <a.korzhevskiy@gmail.com>  
Andrei Tudor Clin <mail@acln.ro>  
Andrei Vieru <euvieru@gmail.com>  
Andres Erbsen <andreser@google.com>  
Andres Lowrie <andres.lowrie@gmail.com>  
Andrew Austin <andrewacht@gmail.com>  
Andrew Balholm <andybalholm@gmail.com>  
Andrew Benton <andrewmbenton@gmail.com>  
Andrew Bonventre <andybons@chromium.org>  
Andrew Braunstein <awbraunstein@gmail.com>  
Andrew Bursavich <abursavich@gmail.com>  
Andrew Ekstedt <andrew.ekstedt@gmail.com>  
Andrew Etter <andrew.etter@gmail.com>  
Andrew Gerrand <adg@golang.org>  
Andrew Harding <andrew@spacemonkey.com>  
Andrew Jackura <ajackura@google.com>  
Andrew Lutomirski <andy@luto.us>  
Andrew Pilloud <andrewpilloud@igneoussystem.com>  
Andrew Pogrebnoy <absourd.noise@gmail.com>  
Andrew Poydence <apoydence@pivotal.io>  
Andrew Pritchard <awpritchard@gmail.com>  
Andrew Radev <andrey.radev@gmail.com>  
Andrew Skiba <skibaa@gmail.com>  
Andrew Stribblehill <ads@wompom.org>  
Andrew Szeto <andrew@jabagawee.com>  
Andrew Werner <andrew@upthere.com>  
<awerner32@gmail.com>  
Andrew Wilkins <axwalk@gmail.com>  
Andrew Williams <williams.andrew@gmail.com>  
Andrey Mirtchovski <mirtchovski@gmail.com>  
Andrey Petrov <andrey.petrov@shazow.net>

Andrii Soldatenko <andrii.soldatenko@gmail.com>  
Andrii Soluk <isoluchok@gmail.com>  
Andriy Lytvynov <lytvynov.a.v@gmail.com>  
Andrzej eel <andrii.zhezhel@gmail.com>  
Andy Balholm <andy@balholm.com>  
Andy Davis <andy@bigandian.com>  
Andy Finkenstadt <afinkenstadt@zynga.com>  
Andy Lindeman <andy@lindeman.io>  
Andy Maloney <asmaloney@gmail.com>  
Andy Walker <walkeraj@gmail.com>  
Andzej Maciusovic <andzej.maciusovic@gmail.com>  
Anfernee Yongkun Gui <anfernee.gui@gmail.com>  
Angelo Bulfone <mbulfone@gmail.com>  
Anh Hai Trinh <anh.hai.trinh@gmail.com>  
Anit Gandhi <anitgandhi@gmail.com>  
Ankit Goyal <ankit3goyal@gmail.com>  
Anmol Sethi <anmol@aubble.com>  
Anschel Schaffer-Cohen <anschelsc@gmail.com>  
Anthony Alves <cvballa3g0@gmail.com>  
Anthony Canino <anthony.canino1@gmail.com>  
Anthony Eufemio <anthony.eufemio@gmail.com>  
Anthony  
Fok <foka@debian.org>  
Anthony Martin <ality@pbrane.org>  
Anthony Sottile <asottile@umich.edu>  
Anthony Starks <ajstarks@gmail.com>  
Anthony Voutas <voutasaurus@gmail.com>  
Anthony Woods <awoods@raintank.io>  
Antoine GIRARD <sapk@sapk.fr>  
Antoine Martin <antoine97.martin@gmail.com>  
Anton Gyllenberg <anton@iki.fi>  
Antonin Amand <antonin.amand@gmail.com>  
Antonio Antelo <aantelov87@gmail.com>  
Antonio Bibiano <antbbn@gmail.com>  
Antonio Murdaca <runcom@redhat.com>  
Antonio Troina <thoeni@gmail.com>  
Apisak Darakananda <pongad@gmail.com>  
Aram Hvrneanu <aram@mgk.ro>  
Areski Belaid <areski@gmail.com>  
Ariel Mashraki <ariel@mashraki.co.il>  
Arkadi Pyuro <arkadi@google.com>  
Arlo Breault <arlolra@gmail.com>  
Arnaud Ysmal <arnaud.ysmal@gmail.com>  
Arne Hormann <arnehormann@gmail.com>  
Arnout Engelen <arnout@bzst.net>  
Aron Nopanen <aron.nopanen@gmail.com>  
Arthur Fabre <arthur@arthurfabre.com>  
Arthur Khashaev <arthur@khashaev.ru>

Artyom Pervukhin <artyom.pervukhin@gmail.com>  
Arvindh Rajesh Tamilmanni <art@a-30.net>  
Ashish  
Gandhi <ag@ashishgandhi.org>  
Asim Shankar <asimshankar@gmail.com>  
Atin Malaviya <amalaviy@akamai.com>  
Ato Araki <ato.araki@gmail.com>  
Audrey Lim <audreylh@gmail.com>  
Audrius Butkevicius <audrius.butkevicius@gmail.com>  
Augusto Roman <aroman@gmail.com>  
Aulus Egnatius Varialus <varialus@gmail.com>  
Aurlien Rainone <aurelien.rainone@gmail.com>  
Austin Clements <austin@google.com> <aclements@csail.mit.edu>  
Avi Flax <avi@timehop.com>  
awaw fumin <awawfumin@gmail.com>  
Awn Umar <awn@cryptolosophy.io>  
Axel Wagner <axel.wagner.hh@googlemail.com>  
Ayanamist Yang <ayanamist@gmail.com>  
Aymerick Jhanne <aymerick@jehanne.org>  
Azat Kaumov <kaumov.a.r@gmail.com>  
Baiju Muthukadan <baiju.m.mail@gmail.com>  
Balaram Makam <bmakam.qdt@qualcommdatacenter.com>  
Balazs Lecz <leczb@google.com>  
Baokun Lee <nototon@gmail.com>  
Bartosz Grzybowski <melkorm@gmail.com>  
Bartosz Oler <brtsz@google.com>  
Bastian Ike <bastian.ike@gmail.com>  
Ben Burkert <ben@benburkert.com>  
Ben Eitzen <eitzenb@golang.org>  
Ben  
Fried <ben.fried@gmail.com>  
Ben Haines <bhainesva@gmail.com>  
Ben Hoyt <benhoyt@gmail.com>  
Ben Laurie <ben@links.org> <benl@google.com>  
Ben Lubar <ben.lubar@gmail.com>  
Ben Lynn <benlynn@gmail.com>  
Ben Olive <sionide21@gmail.com>  
Ben Schwartz <bemasc@google.com>  
Ben Shi <powerman1st@163.com>  
Benjamin Black <b@b3k.us>  
Benjamin Cable <cable.benjamin@gmail.com>  
Benjamin Hsieh <tanookiben@users.noreply.github.com>  
Benjamin Prosnitz <bprosnitz@google.com>  
Benjamin Wester <bwester@squareup.com>  
Benny Siegert <bsiegert@gmail.com>  
Benoit Sigoure <tsunanet@gmail.com>  
Berengar Lehr <Berengar.Lehr@gmx.de>  
Bill Neubauer <wcn@golang.org> <wcn@google.com> <bill.neubauer@gmail.com>

Bill O'Farrell <billo@ca.ibm.com>  
Bill Prin <waprin@google.com>  
Bill Thiede <couchmoney@gmail.com>  
Bill Zissimopoulos <billziss@navimatics.com>  
Billie Harold Cleek <bhcleek@gmail.com>  
Billy Lynch <wlynch@google.com>  
Bjorn Tillenius <bjorn@tillenius.me>  
Bjorn Tipling <bjorn.tipling@gmail.com>  
Blain Smith <rebelgeek@blainsmith.com>  
Blake  
Gentry <blakesgentry@gmail.com>  
Blake Mesdag <blakemesdag@gmail.com>  
Blake Mizerany <blake.mizerany@gmail.com>  
Blixt <me@blixt.nyc>  
Bob Briski <rbriski@gmail.com>  
Bob Potter <bobby.potter@gmail.com>  
Bobby DeSimone <bobbydesimone@gmail.com>  
Bobby Powers <bobbypowers@gmail.com>  
Boris Nagaev <nagaev@google.com>  
Borja Clemente <borja.clemente@gmail.com>  
Brad Burch <brad.burch@gmail.com>  
Brad Fitzpatrick <bradfitz@golang.org> <bradfitz@gmail.com>  
Brad Garcia <bgarcia@golang.org>  
Brad Jones <rbjones@google.com>  
Brad Morgan <brad@morgabra.com>  
Brad Whitaker <bwhitaker@fastly.com>  
Braden Bassingthwaite <bbassingthwaite@vendasta.com>  
Bradley Falzon <brad@teambrad.net>  
Brady Catherman <brady@gmail.com>  
Brady Sullivan <brady@bsull.com>  
Brandon Bennett <bbennett@fb.com>  
Brandon Gilmore <varz@google.com>  
Brendan Daniel Tracey <tracey.brendan@gmail.com>  
Brendan O'Dea <bod@golang.org>  
Brett Cannon <bcannon@gmail.com>  
Brett Merrill <brett.j.merrill94@gmail.com>  
Brian Dellisanti <briandellisanti@gmail.com>  
Brian  
Downs <brian.downs@gmail.com>  
Brian G. Merrell <bgmerrell@gmail.com>  
Brian Gitonga Marete <marete@toshnix.com> <bgmarete@gmail.com> <bgm@google.com>  
Brian Kennedy <btkenney@gmail.com>  
Brian Kessler <brian.m.kessler@gmail.com>  
Brian Ketelsen <bketelsen@gmail.com>  
Brian Slesinsky <skybrian@google.com>  
Brian Smith <ohohvi@gmail.com>  
Brian Starke <brian.starke@gmail.com>  
Bryan Alexander <Kozical@msn.com>

Bryan C. Mills <bcmills@google.com>  
Bryan Chan <bryan.chan@ca.ibm.com>  
Bryan Ford <brynosaurus@gmail.com>  
Bulat Gaifullin <gaifullinbf@gmail.com>  
Burak Guven <bguven@gmail.com>  
Caine Tighe <arctanofyourface@gmail.com>  
Caio Marcelo de Oliveira Filho <caio.oliveira@intel.com>  
Caleb Martinez <accounts@calebmartinez.com>  
Caleb Spare <cespare@gmail.com>  
Carl Chatfield <carlchatfield@gmail.com>  
Carl Henrik Lunde <chlunde@ifi.uio.no>  
Carl Jackson <carl@stripe.com>  
Carl Johnson <me@carlmjohnson.net>  
Carl Mastrangelo <notcarl@google.com>  
Carl Shapiro <cshapiro@google.com> <cshapiro@golang.org>  
Carlisia  
Campos <carlisia@grokkingtech.io>  
Carlo Alberto Ferraris <cafxx@strayorange.com>  
Carlos Castillo <cookieo9@gmail.com>  
Carlos Cirello <uldericofilho@gmail.com>  
Carlos Eduardo Seo <cseo@linux.vnet.ibm.com>  
Carlos Souza <carloshrsouza@gmail.com>  
Carolyn Van Slyck <me@carolynvanslyck.com>  
Cary Hull <chull@google.com>  
Case Nelson <case.nelson@gmail.com>  
Casey Callendrello <squeed@gmail.com>  
Casey Marshall <casey.marshall@gmail.com>  
Catalin Nicutar <cnicutar@google.com>  
Catalin Patulea <catalinp@google.com>  
Cedric Staub <cs@squareup.com>  
Cezar S Espinola <cezarsa@gmail.com>  
Chad Rosier <mrosier.qdt@qualcommdatacenter.com>  
ChaiShushan <chaishushan@gmail.com>  
Channing Kimble-Brown <channing@golang.org>  
Charles Fenwick Elliott <Charles@FenwickElliott.io>  
Charles Kenney <charlesc.kenney@gmail.com>  
Charles L. Dorian <cldorian@gmail.com>  
Charles Lee <zombie.fml@gmail.com>  
Charles Weill <weill@google.com>  
Cherry Zhang <cherryz@google.com>  
Chew Choon Keat <choonkeat@gmail.com>  
Cholerae  
Hu <choleraehyq@gmail.com>  
Chotepud Teo <AlexRouSg@users.noreply.github.com>  
Chris Ball <chris@printf.net>  
Chris Biscardi <chris@christopherbiscardi.com>  
Chris Broadfoot <cbro@golang.org>  
Chris Dollin <ehog.hedge@gmail.com>

Chris Farmiloe <chrisfarms@gmail.com>  
Chris Hines <chris.cs.guy@gmail.com>  
Chris Howey <howeyc@gmail.com>  
Chris Hundt <hundt@google.com>  
Chris Jones <chris@cjones.org> <chris.jones.yar@gmail.com>  
Chris Kastorff <encryptio@gmail.com>  
Chris Lennert <calennert@gmail.com>  
Chris Liles <caveryliles@gmail.com>  
Chris Manghane <cmang@golang.org>  
Chris McGee <sirnewton\_01@yahoo.ca> <newton688@gmail.com>  
Chris Raynor <raynor@google.com>  
Chris Roche <rodaine@gmail.com>  
Chris Smith <chrsmith@users.noreply.github.com>  
Chris Stockton <chrisstocktonaz@gmail.com>  
Chris Zou <chriszou@ca.ibm.com>  
Christian Alexander <christian@linux.com>  
Christian Couder <chriscool@tuxfamily.org>  
Christian Himpel <chressie@googlemail.com> <chressie@gmail.com>  
Christian Pellegrin <chri@evolware.org>  
Christian  
R. Petrin <christianpetrin@gmail.com>  
Christine Hansmann <chhansmann@gmail.com>  
Christoffer Buchholz <christoffer.buchholz@gmail.com>  
Christoph Blecker <admin@toph.ca>  
Christoph Hack <christoph@tux21b.org>  
Christopher Cahoon <chris.cahoon@gmail.com>  
Christopher Guiney <chris@guiney.net>  
Christopher Henderson <chris@chenderson.org>  
Christopher Koch <chrisko@google.com>  
Christopher Nelson <nadiasvertex@gmail.com>  
Christopher Nielsen <m4dh4tt3r@gmail.com>  
Christopher Redden <christopher.redden@gmail.com>  
Christopher Swenson <cswenson@google.com>  
Christopher Wedgwood <cw@f00f.org>  
Christos Zoulas <christos@zoulas.com> <zoulasc@gmail.com>  
Christy Perez <christy@linux.vnet.ibm.com>  
CL Sung <clsung@gmail.com> <cl\_sung@htc.com>  
Clment Chigot <clement.chigot@atos.net>  
Clement Skau <clementskau@gmail.com>  
Cody Oss <the.cody.oss@gmail.com>  
Colby Ranger <cranger@google.com>  
Colin Cross <ccross@android.com>  
Colin Edwards <colin@recursivepenguin.com>  
Colin Kennedy <moshen.colin@gmail.com>  
Colin  
Nelson <colnnelson@google.com>  
Colin Rice <clr@google.com>  
Conrad Irwin <conrad.irwin@gmail.com>

Conrad Meyer <cemeyer@cs.washington.edu>  
Conrado Gouvea <conradopl@gmail.com>  
Constantin Konstantinidis <constantinkonstantinidis@gmail.com>  
Corey Thomasson <cthom.lists@gmail.com>  
Cosmos Nicolaou <cnicolaou@google.com>  
Costin Chirvasuta <ctin@google.com>  
Craig Citro <craigcitro@google.com>  
Cristian Staretu <unclejacksons@gmail.com>  
Cuihtlauac ALVARADO <cuihtlauac.alvarado@orange.com>  
Cyrill Schumacher <cyrill@schumacher.fm>  
Daisuke Fujita <dtanshi45@gmail.com>  
Daker Fernandes Pinheiro <daker.fernandes.pinheiro@intel.com>  
Damian Gryski <dgryski@gmail.com>  
Damien Lespiau <damien.lespiau@gmail.com> <damien.lespiau@intel.com>  
Damien Mathieu <42@dmathieu.com>  
Damien Neil <dneil@google.com>  
Damien Tournoud <damien@platform.sh>  
Dan Ballard <dan@mindstab.net>  
Dan Caddigan <goldcaddy77@gmail.com>  
Dan Callahan <dan.callahan@gmail.com>  
Dan Harrington <harringtond@google.com>  
Dan  
Jacques <dnj@google.com>  
Dan Johnson <computerdruide@google.com>  
Dan Peterson <dpiddy@gmail.com>  
Dan Pupius <dan@medium.com>  
Dan Sinclair <dan.sinclair@gmail.com>  
Danil de Kok <me@danieldk.eu>  
Daniel Fleischman <danielfleischman@gmail.com>  
Daniel Ingram <ingramds@appstate.edu>  
Daniel Johansson <dajo2002@gmail.com>  
Daniel Kerwin <d.kerwin@gini.net>  
Daniel Krech <eikeon@eikeon.com>  
Daniel Lidn <daniel.liden.87@gmail.com>  
Daniel Mart <mvdan@mvdan.cc>  
Daniel Morsing <daniel.morsing@gmail.com>  
Daniel Nadasi <dnadasi@google.com>  
Daniel Nephin <dnephin@gmail.com>  
Daniel Ortiz Pereira da Silva <daniel.particular@gmail.com>  
Daniel Skinner <daniel@dasa.cc>  
Daniel Speichert <daniel@speichert.pl>  
Daniel Theophanes <kardianos@gmail.com>  
Daniel Upton <daniel@floppy.co>  
Daniela Petruzalek <daniela.petruzalek@gmail.com>  
Danny Rosseau <daniel.rosseau@gmail.com>  
Daria Kolistratova <daria.kolistratova@intel.com>  
Darien Raymond <admin@v2ray.com>  
Darren Elwood <darren@textnode.com>

Darshan Parajuli <parajulidarshan@gmail.com>  
Datong  
Sun <dndx@idndx.com>  
Dave Borowitz <dborowitz@google.com>  
Dave Bort <dbort@golang.org>  
Dave Cheney <dave@cheney.net>  
Dave Day <djd@golang.org>  
Dave Grijalva <dgrijalva@ngmoco.com>  
Dave MacFarlane <driusan@gmail.com>  
Dave Russell <forfuncsake@gmail.com>  
David Anderson <danderson@google.com>  
David Barnett <dbarnett@google.com>  
David Benjamin <davidben@google.com>  
David Brophy <dave@brophy.uk>  
David Brgin <676c7473@gmail.com>  
David Calavera <david.calavera@gmail.com>  
David Carlier <devnexen@gmail.com>  
David Chase <drchase@google.com>  
David Covert <davidhcovert@gmail.com>  
David Crawshaw <david.crawshaw@zensus.com> <crawshaw@google.com> <crawshaw@golang.org>  
David du Colombier <0intro@gmail.com>  
David Forsythe <dforsythe@gmail.com>  
David G. Andersen <dave.andersen@gmail.com>  
David Glasser <glasser@meteor.com>  
David Heuschmann <heuschmann.d@gmail.com>  
David Howden <dhowden@gmail.com>  
David Hubbard <dsp@google.com>  
David Jakob Fritz <david.jakob.fritz@gmail.com>  
David  
Jones <dxjones@gmail.com>  
David Lazar <lazard@golang.org>  
David Leon Gil <coruus@gmail.com>  
David McLeish <davemc@google.com>  
David Ndungu <dnjuguna@gmail.com>  
David NewHamlet <david@newhamlet.com>  
David Presotto <presotto@gmail.com>  
David R. Jenni <david.r.jenni@gmail.com>  
David Sansome <me@davidsansome.com>  
David Stainton <dstainton415@gmail.com>  
David Symonds <dsymonds@golang.org>  
David Thomas <davidthomas426@gmail.com>  
David Timm <dtimm@pivotal.io>  
David Titarenco <david.titarenco@gmail.com>  
David Tolpin <david.tolpin@gmail.com>  
David Url <david@urld.io>  
David Volquartz Lebech <david@lebech.info>  
David Wimmer <davidlwimmer@gmail.com>  
Davies Liu <davies.liu@gmail.com>



Davor Kapsa <davor.kapsa@gmail.com>  
Dean Prichard <dean.prichard@gmail.com>  
Deepak Jois <deepak.jois@gmail.com>  
Denis Bernard <db047h@gmail.com>  
Denis Brandolini <denis.brandolini@gmail.com>  
Denis Nagorny <denis.nagorny@intel.com>  
Dennis Kuhnert <mail.kuhnert@gmail.com>  
Denys Honsiorovskiy <honsiorovskiy@gmail.com>  
Denys  
Smirnov <denis.smirnov.91@gmail.com>  
Derek Buitenhuis <derek.buitenhuis@gmail.com>  
Derek Che <drc@yahoo-inc.com>  
Derek McGowan <derek@mcgstyle.net>  
Derek Parker <parkerderek86@gmail.com>  
Derek Shockey <derek.shockey@gmail.com>  
Dev Ojha <dojha12@gmail.com>  
Dev Zhoujun <dev.zhoujun@gmail.com>  
Devon H. O'Dell <devon.odell@gmail.com>  
Dhaivat Pandit <dhaivatpandit@gmail.com>  
Dhananjay Nakrani <dhananjayn@google.com>  
Dhiru Kholia <dhiru.kholia@gmail.com>  
Dhruvdudd Jadhav <dhruvdudd.jadhav@gmail.com>  
Di Xiao <dixiao@google.com>  
Didier Spezia <didier.06@gmail.com>  
Diego Siqueira <diego9889@gmail.com>  
Dieter Plaetinck <dieter@raintank.io>  
Dimitri Sokolyuk <sokolyuk@gmail.com>  
Dimitri Tcaciuc <dtcaciuc@gmail.com>  
Dina Garmash <dgrmsh@gmail.com>  
Diogo Pinela <diogoid7400@gmail.com>  
Dirk Gadsden <dirk@esherido.com>  
Diwaker Gupta <diwakergupta@gmail.com>  
Dmitri Popov <operator@cv.dp-net.com>  
Dmitri Shuralyov <dmitshur@golang.org> <dmitri@shuralyov.com>  
Dmitriy Cherchenko <dcherchenko@gmail.com>  
Dmitriy  
Dudkin <dudkin.dmitriy@gmail.com>  
Dmitriy Shelenin <deemok@googlemail.com> <deemok@gmail.com>  
Dmitriy Vyukov <dvyukov@google.com>  
Dmitry Chestnykh <dchest@gmail.com>  
Dmitry Doroginin <doroginin@gmail.com>  
Dmitry Neverov <dmitry.neverov@gmail.com>  
Dmitry Savintsev <dsavints@gmail.com>  
Dmitry Yakunin <nonamezeil@gmail.com>  
Domen Ipavec <domen@ipavec.net>  
Dominic Green <dominicgreen1@gmail.com>  
Dominik Honnef <dominik.honnef@gmail.com>  
Dominik Vogt <vogt@linux.vnet.ibm.com>

Don Byington <don@dbyington.com>  
Donald Huang <don.hcd@gmail.com>  
Dong-hee Na <donghee.na92@gmail.com>  
Donovan Hide <donovanhide@gmail.com>  
Doug Anderson <douga@google.com>  
Doug Fawley <dfawley@google.com>  
Douglas Danger Manley <doug.manley@gmail.com>  
Drew Flower <drewvanstone@gmail.com>  
Drew Hintz <adhintz@google.com>  
Duncan Holm <mail@frou.org>  
Dustin Carlino <dcarlino@google.com>  
Dustin Herbison <djherbis@gmail.com>  
Dustin Long <dustmop@gmail.com>  
Dustin Sallings <dsallings@gmail.com>  
Dustin Shields-Cloues  
<dcloues@gmail.com>  
Dvir Volk <dvir@everything.me> <dvirsky@gmail.com>  
Dylan Waits <dylan@waits.io>  
Edan Bedrik <3d4nb3@gmail.com>  
Eden Li <eden.li@gmail.com>  
Eduard Urbach <e.urbach@gmail.com>  
Eduardo Ramalho <eduardo.ramalho@gmail.com>  
Edward Muller <edwardam@interlix.com>  
Egon Elbre <egonelbre@gmail.com>  
Ehren Kret <ehren.kret@gmail.com>  
Eitan Adler <lists@eitanadler.com>  
Eivind Uggedal <eivind@uggedal.com>  
Elbert Fliek <efliek@gmail.com>  
Eldar Rakhimberdin <ibeono@gmail.com>  
Elena Grahovac <elena@grahovac.me>  
Elias Naur <mail@eliasnaur.com> <elias.naur@gmail.com>  
Elliot Morrison-Reed <elliottmr@gmail.com>  
Emerson Lin <linyintor@gmail.com>  
Emil Hessman <emil@hessman.se>  
Emil Mursalimov <mursalimovemeel@gmail.com>  
Emilien Kenler <hello@emilienkenler.com>  
Emmanuel Odeke <emm.odeke@gmail.com> <odeke@ualberta.ca>  
Eno Compton <enocom@google.com>  
Eoghan Sherry <ejsherry@gmail.com>  
Eric Chiang <eric.chiang.m@gmail.com>  
Eric Clark <zerohp@gmail.com>  
Eric Daniels <eric@erdaniels.com>  
Eric  
Engestrom <eric@engestrom.ch>  
Eric Garrido <ekg@google.com>  
Eric Koleda <ekoleda+devrel@google.com>  
Eric Lagergren <ericscottlagergren@gmail.com>  
Eric Milliken <emilliken@gmail.com>

Eric Pauley <eric@pauley.me>  
Eric Ponce <tricokun@gmail.com>  
Eric Rescorla <ekr@rtfm.com>  
Eric Roshan-Eisner <eric.d.eisner@gmail.com>  
Eric Rykwaldler <e.rykwaldler@gmail.com>  
Erik Aigner <aigner.erik@gmail.com>  
Erik Dubbelboer <erik@dubbelboer.com>  
Erik St. Martin <alakriti@gmail.com>  
Erik Staab <estaab@google.com>  
Erik Westrup <erik.westrup@gmail.com>  
Erin Masatsugu <erin.masatsugu@gmail.com>  
Ernest Chiang <ernest\_chiang@htc.com>  
Erwin Oegema <blablaechthema@hotmail.com>  
Esko Luontola <esko.luontola@gmail.com>  
Ethan Burns <eaburns@google.com>  
Ethan Miller <eamiller@us.ibm.com>  
Euan Kemp <euank@euank.com>  
Eugene Kalinin <e.v.kalinin@gmail.com>  
Evan Broder <evan@stripe.com>  
Evan Brown <evanbrown@google.com>  
Evan Hicks <evan.hicks2@gmail.com>  
Evan Jones <ej@evanjones.ca>  
Evan Klitzke <evan@eklitzke.org>  
Evan Kroske  
<evankroske@google.com>  
Evan Martin <evan.martin@gmail.com>  
Evan Phoenix <evan@phx.io>  
Evan Shaw <chickencha@gmail.com>  
Evgeniy Polyakov <zbr@ioremap.net>  
Ewan Chou <coocood@gmail.com>  
Ewan Valentine <ewan.valentine89@gmail.com>  
Eyal Posener <posener@gmail.com>  
Fabian Wickborn <fabian@wickborn.net>  
Fabian Zaremba <fabian@youreemail.eu>  
Fabrizio Milo <mistobaan@gmail.com>  
Faiyaz Ahmed <ahmedf@vmware.com>  
Fan Hongjian <fan.howard@gmail.com>  
Fangming Fang <fangming.fang@arm.com>  
Fannie Zhang <fannie.zhang@arm.com>  
Fatih Arslan <fatih@arslan.io>  
Fazal Majid <majid@apsalar.com>  
Fazlul Shahriar <fshahriar@gmail.com>  
Federico Bond <federicobond@gmail.com>  
Federico Simoncelli <fsimonce@redhat.com>  
Fedor Indutny <fedor@indutny.com>  
Felipe Oliveira <felipeweb.programador@gmail.com>  
Felix Geisendrfer <haimuiba@gmail.com>  
Felix Kollmann <fk@konsorten.de>

Filip Gruszczyski <gruszcz@ gmail.com>  
Filip Haglund <drathier@users.noreply.github.com>  
Filip Stanis <fstanis@google.com>  
Filippo Valsorda  
<filippo@golang.org> <filippo@cloudflare.com> <hi@filippo.io>  
Firmansyah Adiputra <frm.adiputra@gmail.com>  
Florian Forster <octo@google.com>  
Florian Uekermann <florian@uekermann-online.de> <f1@uekermann-online.de>  
Florian Weimer <fw@deneb.enyo.de>  
Florin Patan <florinpatan@gmail.com>  
Folke Behrens <folke@google.com>  
Ford Hurley <ford.hurley@gmail.com>  
Francesc Campoy <campoy@golang.org>  
Francisco Claude <fclaude@recoded.cl>  
Francisco Rojas <francisco.rojas.gallegos@gmail.com>  
Francisco Souza <franciscossouza@gmail.com>  
Frank Schroeder <frank.schroeder@gmail.com>  
Frank Somers <fsomers@arista.com>  
Frederic Guillot <frederic.guillot@gmail.com>  
Frederick Kelly Mayle III <frederickmayle@gmail.com>  
Frederik Ring <frederik.ring@gmail.com>  
Fredrik Enestad <fredrik.enestad@soundtrackyourbrand.com>  
Fredrik Forsmo <fredrik.forsmo@gmail.com>  
Fredrik Wallgren <fredrik.wallgren@gmail.com>  
Frew Schmidt <github@frew.co>  
Frithjof Schulze <schulze@math.uni-hannover.de> <sfrithjof@gmail.com>  
Frits van Bommel  
<fvbommel@gmail.com>  
Fumitoshi Ukai <ukai@google.com>  
G. Hussain Chinoy <ghchinoy@gmail.com>  
Gaal Yahas <gaal@google.com>  
Gabrel Arthr Ptursson <gabriel@system.is>  
Gabriel Aszalos <gabriel.aszalos@gmail.com>  
Gabriel Nicolas Avellaneda <avellaneda.gabriel@gmail.com>  
Gabriel Russell <gabriel.russell@gmail.com>  
Gareth Paul Jones <gpj@foursquare.com>  
Garret Kelly <gdk@google.com>  
Garrick Evans <garrick@google.com>  
Gary Burd <gary@beagledreams.com> <gary.burd@gmail.com>  
Gary Elliott <garyelliott@google.com>  
Gaurish Sharma <contact@aurishsharma.com>  
Gautham Thambidorai <gautham.dorai@gmail.com>  
Gauthier Jolly <gauthier.jolly@gmail.com>  
Geert-Johan Riemer <gjr19912@gmail.com>  
Genevieve Luyt <genevieve.luyt@gmail.com>  
Gengliang Wang <ltnwgl@gmail.com>  
Geoff Berry <gberry.qdt@qualcommdatacenter.com>  
Geoffroy Lorieux <lorieux.g@gmail.com>

Geon Kim <geon0250@gmail.com>  
Georg Reinke <guelfey@gmail.com>  
George Gkirtsou <ggirtsou@gmail.com>  
George Shammass <george@shamm.as> <georgyo@gmail.com>  
Gerasimos  
(Makis) Maropoulos <kataras2006@hotmail.com>  
Gerasimos Dimitriadis <gedimitr@gmail.com>  
Gergely Brautigam <skarlso777@gmail.com>  
Getulio Snchez <valentin2507@gmail.com>  
Gianguido Sora` <g.sora4@gmail.com>  
Gideon Jan-Wessel Redelinghuys <gjredelinghuys@gmail.com>  
Giles Lean <giles.lean@pobox.com>  
Giovanni Bajo <rasky@develer.com>  
GitHub User @ajnrp (1688456) <ajnrp@users.noreply.github.com>  
GitHub User @andrius4669 (4699695) <andrius4669@gmail.com>  
GitHub User @as (8127015) <as.utf8@gmail.com>  
GitHub User @bgadrian (830001) <aditza8@gmail.com>  
GitHub User @bontequero (2674999) <bontequero@gmail.com>  
GitHub User @cch123 (384546) <buaa.cch@gmail.com>  
GitHub User @chanxuehong (3416908) <chanxuehong@gmail.com>  
GitHub User @dupoxy (1143957) <dupoxy@users.noreply.github.com>  
GitHub User @erifan (31343225) <eric.fang@arm.com>  
GitHub User @esell (9735165) <eujon.sellers@gmail.com>  
GitHub User @itchyny (375258) <itchyny@hatena.ne.jp>  
GitHub User @kc1212 (1093806) <kc1212@users.noreply.github.com>  
GitHub  
User @Kropekk (13366453) <kamilkropiewnicki@gmail.com>  
GitHub User @LotusFenn (13775899) <fenn.lotus@gmail.com>  
GitHub User @madiganz (18340029) <zacharywmadigan@gmail.com>  
GitHub User @mkishere (224617) <224617+mkishere@users.noreply.github.com>  
GitHub User @OlgaVIPetrova (44112727) <OVPPetrova@gmail.com>  
GitHub User @pityonline (438222) <pityonline@gmail.com>  
GitHub User @pytimer (17105586) <lixin20101023@gmail.com>  
GitHub User @saitarunreddy (21041941) <saitarunreddypalla@gmail.com>  
GitHub User @shogo-ma (9860598) <Choroma194@gmail.com>  
GitHub User @tell-k (26263) <ffk2005@gmail.com>  
GitHub User @uhei (2116845) <uhei@users.noreply.github.com>  
GitHub User @uropek (39370426) <uropek@gmail.com>  
Giulio Iotti <dullgiulio@gmail.com>  
Giulio Micheloni <giulio.micheloni@gmail.com>  
Giuseppe Valente <gvalente@arista.com>  
Gleb Stepanov <glebstepanov1992@gmail.com>  
Glenn Brown <glennb@google.com>  
Glenn Lewis <gmlewis@google.com>  
Gordon Klaus <gordon.klaus@gmail.com>  
Graham King  
<graham4king@gmail.com>  
Graham Miller <graham.miller@gmail.com>

Grant Griffiths <ggp493@gmail.com>  
Greg Poirier <greg.istehbest@gmail.com>  
Greg Steuck <gnezdo+github@google.com>  
Greg Ward <greg@gerg.ca>  
Grgoire Delattre <gregoire.delattre@gmail.com>  
Gregory Man <man.gregory@gmail.com>  
Guilherme Caruso <gui.martinscaruso@gmail.com>  
Guilherme Garnier <guilherme.garnier@gmail.com>  
Guilherme Goncalves <guilhermeaugustosg@gmail.com>  
Guilherme Rezende <guilhermebr@gmail.com>  
Guillaume J. Charmes <guillaume@charmest.net>  
Guobiao Mei <meiguobiao@gmail.com>  
Guoliang Wang <iamwgliang@gmail.com>  
Gustav Paul <gustav.paul@gmail.com>  
Gustav Westling <gustav@westling.xyz>  
Gustavo Franco <gustavorfranco@gmail.com>  
Gustavo Niemeyer <gustavo@niemeyer.net> <n13m3y3r@gmail.com>  
Gwenael Treguier <gwenn.kahz@gmail.com>  
Gyu-Ho Lee <gyuhox@gmail.com>  
H. brahim Gngr <igungor@gmail.com>  
Hajime Hoshi <hajimehoshi@gmail.com>  
Hallgrimur Gunnarsson <halg@google.com>  
HAMANO Tsukasa <hamano@osstech.co.jp>  
Han-Wen  
Nienhuys <hanwen@google.com>  
Hang Qian <hangqian90@gmail.com>  
Hanjun Kim <hallazzang@gmail.com>  
Harald Nordgren <haraldnordgren@gmail.com>  
Hari haran <hariharan.uno@gmail.com>  
Hariharan Srinath <srinathh@gmail.com>  
Harley Laue <losinggeneration@gmail.com>  
Harry Moreno <morenoh149@gmail.com>  
Harshavardhana <hrshvardhana@gmail.com>  
Hauke Lffler <hloeffler@users.noreply.github.com>  
Hvard Haugen <havard.haugen@gmail.com>  
He Liu <liulonnie@gmail.com>  
Hector Chu <hectorchu@gmail.com>  
Hector Martin Cantero <hector@marcansoft.com>  
Henning Schmiedehausen <henning@schmiedehausen.org>  
Henrik Edwards <henrik.edwards@gmail.com>  
Henrik Hodne <henrik@hodne.io>  
Henry Adi Sumarto <henry.adisumarto@gmail.com>  
Henry Bubert <google@mindeco.de>  
Henry Chang <mr.changyuheng@gmail.com>  
Henry Clifford <h.a.clifford@gmail.com>  
Herbert Georg Fischer <herbert.fischer@gmail.com>  
Herbie Ong <herbie@google.com>  
Heschi Kreinick <heschi@google.com>

Hidetatsu Yaginuma <ygnmhdt@gmail.com>  
Hilko Bengen <bengen@hilluzination.de>  
Hiroaki  
Nakamura <hnakamur@gmail.com>  
Hironao OTSUBO <motemen@gmail.com>  
Hiroshi Ioka <hirochachacha@gmail.com>  
Hitoshi Mitake <mitake.hitoshi@gmail.com>  
Holden Huang <ttyh061@gmail.com>  
Hong Ruiqi <hongruiqi@gmail.com>  
Hongfei Tan <feilengcui008@gmail.com>  
Hossein Sheikh Attar <hattar@google.com>  
Hsin Tsao <tsao@google.com>  
Hsin-Ho Yeh <yhh92u@gmail.com>  
Hu Keping <hukeping@huawei.com>  
Hugues Bruant <hugues.bruant@gmail.com>  
Huy Le <huy.dinh.le.89@gmail.com>  
Hyang-Ah Hana Kim <hakim@google.com> <hyangah@gmail.com>  
Ian Cottrell <iancottrell@google.com>  
Ian Davis <nospam@iandavis.com>  
Ian Gudger <ian@loosescre.ws>  
Ian Haken <ihaken@netflix.com>  
Ian Kent <iankent85@gmail.com>  
Ian Lance Taylor <iant@golang.org>  
Ian Leue <ian@appboy.com>  
Ian Zapolsky <ianzapolsky@gmail.com>  
Ibrahim AshShohail <ibra.sho@gmail.com>  
Icarus Sparry <golang@icarus.freeuk.com>  
Iccha Sethi <icchasethi@gmail.com>  
Idora Shinatose <idora.shinatose@gmail.com>  
Igor Bernstein <igorbernstein@google.com>  
Igor Dolzhevik  
<bluesriverz@gmail.com>  
Igor Vashyst <ivashyst@gmail.com>  
Igor Zhilianin <igor.zhilianin@gmail.com>  
Ilya Tocar <ilya.tocar@intel.com>  
INADA Naoki <songofacandy@gmail.com>  
Inanc Gumus <m@inanc.io>  
Ingo Gottwald <in.gottwald@gmail.com>  
Ingo Krabbe <ikrabbe.ask@gmail.com>  
Ingo Oeser <nightlyone@googlemail.com> <nightlyone@gmail.com>  
Ioannis Georgoulas <geototti21@hotmail.com>  
Irfan Sharif <irfanmahmoudsharif@gmail.com>  
Irieda Noboru <irieda@gmail.com>  
Isaac Ardis <isaac.ardis@gmail.com>  
Isaac Wagner <ibw@isaacwagner.me>  
Iskander Sharipov <iskander.sharipov@intel.com> <quasilyte@gmail.com>  
Issac Trotts <issactrotts@google.com>  
Ivan Babrou <ivan@cloudflare.com>

Ivan Bertona <ivan.bertona@gmail.com>  
Ivan Krasin <krasin@golang.org>  
Ivan Kutuzov <arbrix@gmail.com>  
Ivan Markin <sw@nogoegst.net>  
Ivan Moscoso <moscoso@gmail.com>  
Ivan Sharavuev <shpiwan@gmail.com>  
Ivan Ukhov <ivan.ukhov@gmail.com>  
Ivy Evans <ivy@ivyevans.net>  
Jaana Burcu Dogan <jbd@google.com> <jbd@golang.org> <burcujdogan@gmail.com>  
Jack  
Britton <jackxbritton@gmail.com>  
Jack Lindamood <jlindamo@justin.tv>  
Jacob Baskin <jbaskin@google.com>  
Jacob H. Haven <jacob@cloudflare.com>  
Jacob Hoffman-Andrews <github@hoffman-andrews.com>  
Jae Kwon <jae@tendermint.com>  
Jake B <doogie1012@gmail.com>  
Jakob Borg <jakob@nym.se>  
Jakob Weisblat <jakobw@mit.edu>  
Jakub ajka <jcajka@redhat.com>  
Jakub Ryszard Czarnowicz <j.czarnowicz@gmail.com>  
James Aguilar <jaguilar@google.com>  
James Bardin <j.bardin@gmail.com>  
James Chacon <jchacon@google.com>  
James Clarke <jrtc27@jrtc27.com>  
James Cowgill <James.Cowgill@imgtec.com>  
James Craig Burley <james-github@burleyarch.com>  
James David Chalfant <james.chalfant@gmail.com>  
James Fysh <james.fysh@gmail.com>  
James Gray <james@james4k.com>  
James Hartig <fastest963@gmail.com>  
James Lawrence <jljatone@gmail.com>  
James Meneghello <rawrz0r@gmail.com>  
James Myers <jfmyers9@gmail.com>  
James Neve <jamesoneve@gmail.com>  
James P. Cooper <jamespcooper@gmail.com>  
James Robinson <jamesr@google.com> <jamesr.gatech@gmail.com>  
James  
Schofield <james@shoeboxapp.com>  
James Smith <jrs1995@icloud.com>  
James Sweet <james.sweet88@googlemail.com>  
James Toy <nil@opensesame.st>  
James Treanor <jtreanor3@gmail.com>  
James Tucker <raggi@google.com>  
James Whitehead <jnwhiteh@gmail.com>  
Jamie Beverly <jamie.r.beverly@gmail.com>  
Jamie Gennis <jgennis@google.com> <jgennis@gmail.com>  
Jamie Kerr <jkerr113@googlemail.com>



Jamie Liu <jamieliu@google.com>  
Jamie Stackhouse <contin673@gmail.com>  
Jamie Turner <jamwt@dropbox.com>  
Jamie Wilkinson <jaq@spacepants.org>  
Jamil Djadala <djadala@gmail.com>  
Jan Berktold <jan@berktold.co>  
Jan H. Hosang <jan.hosang@gmail.com>  
Jan Kratochvil <jan.kratochvil@redhat.com>  
Jan Lehnardt <jan@apache.org>  
Jan Mercl <0xjnml@gmail.com> <befelemepeseveze@gmail.com>  
Jan Newmarch <jan.newmarch@gmail.com>  
Jan Pilzer <jan.pilzer@gmx.de>  
Jan Ziak <0xe2.0x9a.0x9b@gmail.com>  
Jani Monoses <jani.monoses@ubuntu.com> <jani.monoses@gmail.com>  
Jared Culp <jculp14@gmail.com>  
Jaroslavas Poepko  
<jp@webmaster.ms>  
Jason A. Donenfeld <Jason@zx2c4.com>  
Jason Barnett <jason.w.barnett@gmail.com>  
Jason Buberel <jbuberel@google.com>  
Jason Chu <jasonchujc@gmail.com>  
Jason Del Ponte <delpontej@gmail.com>  
Jason Hall <jasonhall@google.com>  
Jason Keene <jasonkeene@gmail.com>  
Jason LeBrun <jblebrun@gmail.com>  
Jason Smale <jsmale@zendesk.com>  
Jason Travis <infomaniac7@gmail.com>  
Jason Wangsadinata <jwangsadinata@gmail.com>  
Javier Kohen <jkohen@google.com>  
Javier Segura <javism@gmail.com>  
Jay Conrod <jayconrod@google.com>  
Jay Taylor <outtatime@gmail.com>  
Jay Weisskopf <jay@jayschwa.net>  
Jean de Klerk <deklerk@google.com>  
Jean-Andr Santoni <jean.andre.santoni@gmail.com>  
Jean-Francois Cantin <jfcantin@gmail.com>  
Jean-Marc Eurin <jmeurin@google.com>  
Jean-Nicolas Moal <jn.moal@gmail.com>  
Jed Denlea <jed@fastly.com>  
Jeet Parekh <jeetparekh96@gmail.com>  
Jeevanandam M <jeeva@myjeeva.com>  
Jeff (Zhefu) Jiang <jeffjiang@google.com>  
Jeff Craig <jeffcraig@google.com>  
Jeff Dupont <jeff.dupont@gmail.com>  
Jeff  
Hodges <jeff@somethingsimilar.com>  
Jeff Johnson <jrjohnson@google.com>  
Jeff R. Allen <jra@nella.org> <jeff.allen@gmail.com>

Jeff Sickel <jas@corpus-callosum.com>  
Jeff Wendling <jeff@spacemonkey.com>  
Jeffrey H <jeffreyh192@gmail.com>  
Jelte Fennema <github-tech@jeltef.nl>  
Jens Frederich <jfrederich@gmail.com>  
Jeremiah Harmsen <jeremiah@google.com>  
Jeremy Banks <\_@jeremy.ca>  
Jeremy Canady <jcanady@gmail.com>  
Jeremy Jackins <jeremyjackins@gmail.com>  
Jeremy Schlatter <jeremy.schlatter@gmail.com>  
Jeroen Bobbeldijk <jerbob92@gmail.com>  
Jerrin Shaji George <jerrinsg@gmail.com>  
Jess Frazelle <me@jessfraz.com>  
Jesse Szwedko <jesse.szwedko@gmail.com>  
Jess Espino <jespino@gmail.com>  
Jianing Yu <jnyu@google.com>  
Jianqiao Li <jianqiaoli@google.com>  
Jihyun Yu <yjh0502@gmail.com>  
Jim Cote <jfcote87@gmail.com>  
Jim Kingdon <jim@bolt.me>  
Jim McGrath <jimmc2@gmail.com>  
Jim Minter <jminter@redhat.com>  
Jimmy Franche <soapboxcicero@gmail.com>  
Jimmy Zelinskie <jimmyzelinskie@gmail.com>  
Jin-wook Jeong <jeweljar@hanmail.net>  
Jingcheng  
Zhang <diogin@gmail.com>  
Jingguo Yao <yaojingguo@gmail.com>  
Jiong Du <londdevil@gmail.com>  
Jirka Dank <dnk@mail.muni.cz>  
Jiulong Wang <jjiulongw@gmail.com>  
Joakim Sernbrant <serbaut@gmail.com>  
Joe Bowbeer <joe.bowbeer@gmail.com>  
Joe Cortopassi <joe@joecortopassi.com>  
Joe Farrell <joe2farrell@gmail.com>  
Joe Harrison <joehazzers@gmail.com>  
Joe Henke <joed.henke@gmail.com>  
Joe Kyo <xunianzu@gmail.com>  
Joe Poirier <jdpoirier@gmail.com>  
Joe Richey <joerichey@google.com>  
Joe Shaw <joe@joeshaw.org>  
Joe Sylve <joe.sylve@gmail.com>  
Joe Tsai <joetsai@digital-static.net>  
Joel Sing <joel@sing.id.au> <jsing@google.com>  
Jol Stemmer <jstemmer@google.com>  
Joel Stemmer <stemmertech@gmail.com>  
Joey Geiger <jgeiger@users.noreply.github.com>  
Johan Brandhorst <johan.brandhorst@gmail.com>

Johan Euphrosine <proppy@google.com>  
Johan Sageryd <j@1616.se>  
John Asmuth <jasmuth@gmail.com>  
John Beisley <huin@google.com>  
John C Barstow <jbowtie@amathaine.com>  
John DeNero <denero@google.com>  
John  
Dethridge <jcd@golang.org>  
John Gibb <johngibb@gmail.com>  
John Gilik <john@jgilik.com>  
John Graham-Cumming <jgc@jgc.org> <jgrahamc@gmail.com>  
John Howard Palevich <jack.palevich@gmail.com>  
John Jeffery <jjeffery@sp.com.au>  
John Jenkins <twodopeshaggy@gmail.com>  
John Leidegren <john.leidegren@gmail.com>  
John Newlin <jnewlin@google.com>  
John Potocny <johnp@vividcortex.com>  
John R. Lenton <jlenton@gmail.com>  
John Schnake <schnake.john@gmail.com>  
John Shahid <jvshahid@gmail.com>  
John Tuley <john@tuley.org>  
Johnny Luo <johnnyluo1980@gmail.com>  
Jon Chen <jchen@justin.tv>  
Jonas Bernoulli <jonas@bernoul.li>  
Jonathan Allie <jonallie@google.com>  
Jonathan Amsterdam <jba@google.com>  
Jonathan Boule <jonathanboule@gmail.com>  
Jonathan Chen <dijonkitchen@users.noreply.github.com>  
Jonathan Feinberg <feinberg@google.com>  
Jonathan Gold <jgold.bg@gmail.com>  
Jonathan Hseu <jhseu@google.com>  
Jonathan Mark <jhmark@xenops.com> <jhmark000@gmail.com>  
Jonathan Nieder <jrn@google.com>  
Jonathan Pentecost  
<pentecostjonathan@gmail.com>  
Jonathan Pittman <jmpittman@google.com> <jonathan.mark.pittman@gmail.com>  
Jonathan Rudenberg <jonathan@titanous.com>  
Jonathan Stacks <jonstacks13@gmail.com>  
Jonathan Wills <runningwild@gmail.com>  
Jongmin Kim <atomaths@gmail.com>  
Joonas Kuorilehto <joneskoo@derbian.fi>  
Joop Kiefte <ikojba@gmail.com> <joop@kiefte.net>  
Jordan Krage <jmank88@gmail.com>  
Jordan Lewis <jordanthelewis@gmail.com>  
Jordan Rhee <jordanrh@microsoft.com>  
Jos Visser <josv@google.com>  
Jose Luis Vazquez Gonzalez <josvazg@gmail.com>  
Joseph Bonneau <jcb@google.com>

Joseph Holsten <joseph@josephholsten.com>  
Josh Bleecher Snyder <josharian@gmail.com>  
Josh Chorlton <jchorlton@gmail.com>  
Josh Deprez <josh.deprez@gmail.com>  
Josh Goebel <dreamer3@gmail.com>  
Josh Hoak <jhoak@google.com>  
Josh Holland <jrh@joshh.co.uk>  
Josh Roppo <joshroppo@gmail.com>  
Josh Varga <josh.varga@gmail.com>  
Joshua Boelter <joshua.boelter@intel.com>  
Joshua Chase <jcjosuachase@gmail.com>  
Joshua Rubin <joshua@rubixconsulting.com>  
Josselin  
Costanzi <josselin@costanzi.fr>  
Jostein Stuhaug <js@solidsystem.no>  
JP Sugarbroad <jpsugar@google.com>  
JT Olds <jtolds@xnet5.com>  
Juan Carlos <juanjcsr@gmail.com>  
Juan Pablo Civile <elementohb@gmail.com>  
Jude Pereira <judebpereira@gmail.com>  
Jukka-Pekka Kekkonen <karatepekka@gmail.com>  
Julia Hansbrough <flowerhack@google.com>  
Julian Kornberger <jk+github@digineo.de>  
Julian Pastarmov <pastarmovj@google.com>  
Julian Phillips <julian@quantumfyre.co.uk>  
Julie Qiu <julie@golang.org>  
Julien Kauffmann <julien.kauffmann@freelan.org>  
Julien Salleyron <julien.salleyron@gmail.com>  
Julien Schmidt <google@julienschmidt.com>  
Julio Montes <julio.montes@intel.com>  
Junda Liu <junda@celer.network>  
Jungho Ahn <jhahn@google.com>  
Junya Hayashi <ledmonster@gmail.com>  
Jure Ham <jure.ham@zemanta.com>  
Justin Gracenin <jgracenin@gmail.com>  
Justin Nu <nuss.justin@gmail.com>  
Justyn Temme <justyntemme@gmail.com>  
Kai Backman <kaib@golang.org>  
Kai Trukenmller <ktye78@gmail.com>  
Kale Blankenship <kale@lemnisis.com>  
Kaleb  
Elwert <kelwert@atlassian.com>  
Kamal Aboul-Hosn <aboulhosn@google.com>  
Kamil Chmielewski <kamil.chm@gmail.com>  
Kamil Kisiel <kamil@kamilkisiel.net> <kamil.kisiel@gmail.com>  
Kamil Rytarowski <krytarowski@users.noreply.github.com>  
Kang Hu <hukangustc@gmail.com>  
Karan Dhiman <karandhi@ca.ibm.com>

Karel Pazdera <pazderak@gmail.com>  
Karoly Negyesi <chx1975@gmail.com>  
Karsten Khler <karsten.koehler95@gmail.com>  
Kashav Madan <kshvmdn@gmail.com>  
Kate Manson <kate.manson@izettle.com>  
Katie Hockman <katie@golang.org>  
Kato Kazuyoshi <kato.kazuyoshi@gmail.com>  
Katrina Owen <katrina.owen@gmail.com>  
Kaviraj Kanagaraj <kavirajkanagaraj@gmail.com>  
Kay Zhu <kayzhu@google.com>  
Kazuhiro Sera <seratch@gmail.com>  
KB Sriram <kbsriram@google.com>  
Keegan Carruthers-Smith <keegan.csmith@gmail.com>  
Kei Son <hey.calmdown@gmail.com>  
Keiji Yoshida <keijiyoshida.mail@gmail.com>  
Keith Ball <inflatablewoman@gmail.com>  
Keith Randall <chr@golang.org>  
Keith Rarick <kr@xph.us>  
Kelsey Hightower <kelsey.hightower@gmail.com>  
Kelvin  
Foo Chuan Lyi <vmirage@gmail.com>  
Ken Friedenbach <kenliz@cruzio.com>  
Ken Rockot <ken@oz.gs> <ken.rockot@gmail.com>  
Ken Sedgwick <ken@bonsai.com>  
Ken Thompson <ken@golang.org>  
Kenji Kaneda <kenji.kaneda@gmail.com>  
Kenji Yano <kenji.yano@gmail.com>  
Kenneth Shaw <kenshaw@gmail.com>  
Kenny Grant <kennygrant@gmail.com>  
Kevin Ballard <kevin@sb.org>  
Kevin Burke <kev@inburke.com>  
Kevin Kirsche <kev.kirsche@gmail.com>  
Kevin Klues <klueska@gmail.com> <klueska@google.com>  
Kevin Malachowski <chowski@google.com>  
Kevin Ruffin <kruffin@gmail.com>  
Kevin Vu <kevin.m.vu@gmail.com>  
Kevin Zita <bleedgreenandgold@gmail.com>  
Kieran Colford <kieran@kcolford.com>  
Kim Shrier <kshrier@racktopsystems.com>  
Kim Yongbin <kybinz@gmail.com>  
Kir Kolyshkin <kolyshkin@gmail.com>  
Kirill Smelkov <kirr@nexedi.com>  
Kirk Han <kirk91.han@gmail.com>  
Kirklin McDonald <kirklin.mcdonald@gmail.com>  
Klaus Post <klauspost@gmail.com>  
Kodie Goodwin <kodiegoodwin@gmail.com>  
Koichi Shiraishi <zchee.io@gmail.com>  
Koki

Ide <niconegoto@yahoo.co.jp>  
Komu Wairagu <komuw05@gmail.com>  
Konstantin <konstantin8105@gmail.com>  
Konstantin Shaposhnikov <k.shaposhnikov@gmail.com>  
Kris Kwiatkowski <kris@cloudflare.com>  
Kris Nova <kris@nivenly.com>  
Kris Rousey <krousey@google.com>  
Kristopher Watts <traetox@gmail.com>  
Kun Li <likunarmstrong@gmail.com>  
Kunpei Sakai <namusyaka@gmail.com>  
Kuntal Majumder <hellozee@disroot.org>  
Kyle Consalus <consalus@gmail.com>  
Kyle Isom <kyle@gokyle.net>  
Kyle Jones <kyle@kyledj.com>  
Kyle Lemons <kyle@kylelemons.net> <kevlar@google.com>  
Kyle Shannon <kyle@pobox.com>  
Kyle Spiers <eiais@google.com>  
Kyle Wood <kyle@kylewood.cc>  
Kyohei Kadota <lufia@lufia.org>  
Kirylo Silin <silin@kirylo.org>  
L Campbell <unpantsu@gmail.com>  
Lai Jiangshan <eag0628@gmail.com>  
Lajos Papp <lalyos@yahoo.com>  
Lakshay Garg <lakshay.garg.1996@gmail.com>  
Lann Martin <lanm@google.com>  
Lanre Adelowo <yo@lanre.wtf>  
Larry Clapp <larry@theclapp.org>  
Larry Hosken <lahosken@golang.org>  
Lars Jeppesen <jeppesen.lars@gmail.com>  
Lars  
Lehtonen <lars.lehtonen@gmail.com>  
Lars Wiegman <lars@namsral.com>  
Larz Conwell <larzconwell@gmail.com>  
Laurent Voisin <lpvoisin@gmail.com>  
Laurie Clark-Michalek <laurie@qubit.com>  
LE Manh Cuong <cuong.manhle.vn@gmail.com>  
Lee Hinman <hinman@gmail.com>  
Lee Packham <lpackham@gmail.com>  
Lehner Florian <dev@der-flo.net>  
Leigh McCulloch <leighmcc@gmail.com>  
Leo Antunes <leo@costela.net>  
Leo Rudberg <ljr@google.com>  
Leon Klingele <git@leonklingele.de>  
Leonel Quinteros <leonel.quinteros@gmail.com>  
Lev Shamardin <shamardin@gmail.com>  
Lewin Bormann <lewin.bormann@gmail.com>  
Lion Yang <lion@aosc.xyz>  
Lloyd Dewolf <foolswisdom@gmail.com>

Lorenz Bauer <lmb@cloudflare.com>  
Lorenzo Masini <rugginoso@develer.com>  
Lorenzo Stoakes <lstoakes@gmail.com>  
Louis Kruger <louisk@google.com>  
Luan Santos <cfcluan@gmail.com>  
Lubomir I. Ivanov <neolit123@gmail.com>  
Luca Bruno <luca.bruno@coreos.com>  
Luca Greco <luca.greco@alcacoop.it>  
Lucas Bremgartner <lucas.bremgartner@gmail.com>  
Lucas Clemente  
<lclemente@google.com>  
Lucien Stuker <lucien.stuker@gmail.com>  
Lucio De Re <lucio.dere@gmail.com>  
Ludi Rehak <ludi317@gmail.com>  
Luigi Riefolo <luigi.riefolo@gmail.com>  
Luit van Drongelen <luitvd@gmail.com>  
Luka Zakrajek <tr00.g33k@gmail.com>  
Lukasz Milewski <lmmilewski@gmail.com>  
Luke Curley <qpingu@gmail.com>  
Luke Granger-Brown <git@lukegb.com>  
Luna Duclos <luna.duclos@palmstonegames.com>  
Luuk van Dijk <lvd@golang.org> <lvd@google.com>  
Lyle Franklin <lylejfranklin@gmail.com>  
Lynn Boger <laboger@linux.vnet.ibm.com>  
Ma Peiqi <mapeiqi2017@gmail.com>  
Maarten Bezemer <maarten.bezemer@gmail.com>  
Maciej Dbski <maciejd@google.com>  
Magnus Hiie <magnus.hiie@gmail.com>  
Maicon Costa <maiconscosta@gmail.com>  
Mak Kolybabi <mak@kolybabi.com>  
Maksym Trykur <maksym.trykur@gmail.com>  
Mal Curtis <mal@mal.co.nz>  
Manfred Touron <m@42.am>  
Manigandan Dharmalingam <manigandan.jeff@gmail.com>  
Manish Goregaokar <manishsmail@gmail.com>  
Manoj Dayaram <platform-dev@moovweb.com> <manoj.dayaram@moovweb.com>  
Mansour  
Rahimi <rahimi.mnr@gmail.com>  
Manu Garg <manugarg@google.com>  
Manu S Ajith <neo@codingarena.in>  
Manuel Mendez <mmendez534@gmail.com>  
Marc Weistroff <marc@weistroff.net>  
Marc-Antoine Ruel <maruel@chromium.org>  
Marcel Edmund Franke <marcel.edmund.franke@gmail.com>  
Marcel van Lohuizen <mpvl@golang.org>  
Marcelo Cantos <marcelo.cantos@gmail.com>  
Marcelo E. Magallon <marcelo.magallon@gmail.com>  
Marco Hennings <marco.hennings@freiheit.com>

Marcus Willock <crazcalm@gmail.com>  
Marga Manterola <marga@google.com>  
Marin Bai <marin.basic02@gmail.com>  
Mario Arranz <marioarranzr@gmail.com>  
Marius Nuennerich <mnu@google.com>  
Mark Adams <mark@markadams.me>  
Mark Bucciarelli <mkbucc@gmail.com>  
Mark Harrison <marhar@google.com>  
Mark Percival <m@mdp.im>  
Mark Pulford <mark@kyne.com.au>  
Mark Rushakoff <mark.rushakoff@gmail.com>  
Mark Ryan <mark.d.ryan@intel.com>  
Mark Severson <miquella@gmail.com>  
Mark Theunissen <mark.theunissen@gmail.com>  
Mark Wolfe <mark@wolfe.id.au>  
Mark Zavislak <zavislak@google.com>  
Marko  
Juhani Silokunnas <marko.silokunnas@gmail.com>  
Marko Kevac <marko@kevac.org>  
Marko Mikulicic <mkm@google.com>  
Marko Mudrinic <mudrinic.mare@gmail.com>  
Marko Tiikkaja <marko@joh.to>  
Markus Duft <markus.duft@salomon.at>  
Markus Sonderegger <marraison@gmail.com>  
Markus Zimmermann <zimmski@gmail.com>  
Marten Seemann <martenseemann@gmail.com>  
Martin Bertschler <mbertschler@gmail.com>  
Martin Garton <garton@gmail.com>  
Martin Habbecke <marhab@google.com>  
Martin Hamrle <martin.hamrle@gmail.com>  
Martin Hoefling <martin.hoefling@gmx.de>  
Martin Kreichgauer <martinkr@google.com>  
Martin Kunc <martinkunc@users.noreply.github.com>  
Martin Lindhe <martin.j.lindhe@gmail.com>  
Martin Mohrmann <moehrmann@google.com> <martisch@uos.de>  
Martin Neubauer <m.ne@gmx.net>  
Martin Olsen <github.com@martinolsen.net>  
Martin Olsson <martin@minimum.se>  
Martin Probst <martin@probst.io>  
Martin Sucha <anty.sk+git@gmail.com>  
Martin Tournoij <martin@arp242.net>  
Martins Sipenko <martins.sipenko@gmail.com>  
Martynas Budrinas  
<mabu@google.com>  
Marvin Stenger <marvin.stenger94@gmail.com>  
Marwan Sulaiman <marwan.sulaiman@work.co>  
Maryan Hratson <gmarik@gmail.com>  
Masahiro Furudate <masahiro.furudate@gmail.com>



Masahiro Wakame <vvakame@gmail.com>  
Masaki Yoshida <yoshida.masaki@gmail.com>  
Mat Byczkowski <mbyczkowski@gmail.com>  
Mat Ryer <thatmatryer@gmail.com>  
Mt Gulys <mgulyas86@gmail.com>  
Matej Bao <matejbaco@gmail.com>  
Mateus Amin <mateus.amin@gmail.com>  
Mateusz Czapliski <czapkofan@gmail.com>  
Mathias Beke <git@denbeke.be>  
Mathias Hall-Andersen <mathias@hall-andersen.dk>  
Mathias Leppich <mleppich@muhqu.de>  
Mathieu Lonjaret <mathieu.lonjaret@gmail.com>  
Mats Lidell <mats.lidell@cag.se> <mats.lidell@gmail.com>  
Matt Aimonetti <mattaimonetti@gmail.com>  
Matt Blair <me@matthewblair.net>  
Matt Bostock <matt@mattbostock.com>  
Matt Brown <mbrown@google.com>  
Matt Dee <mdee@hioscar.com>  
Matt Drollette <matt@drollette.com>  
Matt Harden <matt.harden@gmail.com>  
Matt Jibson <matt.jibson@gmail.com>  
Matt Joiner <anacrolix@gmail.com>  
Matt  
Jones <mrjones@google.com>  
Matt Juran <thepciet@gmail.com>  
Matt Layher <mdlayher@gmail.com>  
Matt Reiferson <mreiferson@gmail.com>  
Matt Robenolt <matt@ydekproductions.com>  
Matt Strong <mstrong1341@gmail.com>  
Matt T. Proud <matt.proud@gmail.com>  
Matt Williams <gh@mattyw.net> <mattyjwilliams@gmail.com>  
Matthew Brennan <matty.brennan@gmail.com>  
Matthew Broberg <matthewbbroberg@gmail.com>  
Matthew Cottingham <mattcottingham@gmail.com>  
Matthew Dempsky <mdempsky@google.com>  
Matthew Denton <mdenton@skyportsystems.com>  
Matthew Holt <Matthew.Holt+git@gmail.com>  
Matthew Horsnell <matthew.horsnell@gmail.com>  
Matthew Waters <mwwaters@gmail.com>  
Matthieu Hauglustaine <matt.hauglustaine@gmail.com>  
Matthieu Olivier <olivier.matthieu@gmail.com>  
Matthijs Kooijman <matthijs@stdin.nl>  
Max Riveiro <kavu13@gmail.com>  
Max Schmitt <max@schmitt.mx>  
Max Ushakov <ushmax@gmail.com>  
Maxim Khitrov <max@mxcrypt.com>  
Maxim Pimenov <mpimenov@google.com>  
Maxim Ushakov <ushakov@google.com>

Maxime de Roucy <maxime.deroucy@gmail.com>  
Mximo  
Cuadros Ortiz <mcuadros@gmail.com>  
Maxwell Krohn <themax@gmail.com>  
Maya Rashish <maya@NetBSD.org>  
Mayank Kumar <krmayankk@gmail.com>  
Meir Fischer <meirfischer@gmail.com>  
Meng Zhuo <mengzhuo1203@gmail.com>  
Mhd Sulhan <m.shulhan@gmail.com>  
Micah Stetson <micah.stetson@gmail.com>  
Michael Anthony Knyszek <mknyszek@google.com>  
Michael Brandenburg <mbrandenburg@bolste.com>  
Michael Chaten <mchaten@gmail.com>  
Michael Darakananda <pongad@google.com>  
Michael Dorner <mail@michaeldorf.de>  
Michael Edwards <medwards@walledcity.ca>  
Michael Elkins <michael.elkins@gmail.com>  
Michael Ellis <micellis@justin.tv>  
Michael Fraenkel <michael.fraenkel@gmail.com>  
Michael Fromberger <michael.j.fromberger@gmail.com>  
Michael Gehring <mg@ebfe.org> <gnirheg.leahcim@gmail.com>  
Michael Henderson <mdhender@users.noreply.github.com>  
Michael Hendricks <michael@ndrix.org>  
Michael Hoisie <hoisie@gmail.com>  
Michael Hudson-Doyle <michael.hudson@linaro.org>  
Michael Kasch <michael.kasch@gmail.com>  
Michael  
Kufel <golang@c.michael-kaeufl.de>  
Michael Kelly <mjk@google.com>  
Michael Lewis <mikelikespie@gmail.com>  
Michael MacInnis <Michael.P.MacInnis@gmail.com>  
Michael Marineau <michael.marineau@coreos.com>  
Michael Matloob <matloob@google.com>  
Michael McConville <momconville@gmail.com>  
Michael McGreevy <mcgreevy@golang.org>  
Michael McLoughlin <mmcloughlin@gmail.com>  
Michael Munday <mike.munday@ibm.com>  
Michael Pearson <mipearson@gmail.com>  
Michael Piatek <piatek@google.com>  
Michael Pratt <mpratt@google.com>  
Michael Schaller <michael@5challer.de>  
Michael Schurter <michael.schurter@gmail.com>  
Michael Shields <mshields@google.com>  
Michael Stapelberg <michael@stapelberg.de> <mstplbrg@googlemail.com>  
Michael Steinert <mike.steinert@gmail.com>  
Michael T. Jones <mtj@google.com> <michael.jones@gmail.com>  
Michael Teichgraber <mteichgraber@gmx.de> <mt4swm@googlemail.com>  
Michael Traver <mtraver@google.com>

Michael Vetter <g.bluehut@gmail.com>  
Michal Bohuslvek <mbohuslvek@gmail.com>  
Michal  
Cierniak <cierniak@google.com>  
Micha Derkacz <ziutek@inet.pl>  
Michal Franc <lam.michal.franc@gmail.com>  
Michal Pristas <michal.pristas@gmail.com>  
Michal Rostecki <mrostecki@suse.de>  
Michalis Kargakis <michaliskargakis@gmail.com>  
Michel Lespinasse <>walken@google.com>  
Miek Gieben <miek@miek.nl> <remigijs.gieben@gmail.com>  
Miguel Mendez <stxmendez@gmail.com>  
Miguel Molina <hi@mvader.me>  
Mihai Borobocea <MihaiBorobocea@gmail.com>  
Mihai Todor <todormihai@gmail.com>  
Mihail Minaev <minaev.mike@gmail.com>  
Mikael Tillenius <mikti42@gmail.com>  
Mike Andrews <mra@xoba.com>  
Mike Appleby <mike@app.leby.org>  
Mike Danese <mikedanese@google.com>  
Mike Houston <mike@kothar.net>  
Mike Kabischev <kabischev@gmail.com>  
Mike Rosset <mike.rosset@gmail.com>  
Mike Samuel <mikesamuel@gmail.com>  
Mike Solomon <msolo@gmail.com>  
Mike Strosaker <strosake@us.ibm.com>  
Mike Tsao <mike@sowbug.com>  
Mike Wiacek <mjwiacek@google.com>  
Mikhail Gusarov <dottedmag@dottedmag.net>  
Mikhail Panchenko <m@mihasya.com>  
Miki Tebeka <miki.tebeka@gmail.com>  
Mikio  
Hara <mikioh.mikioh@gmail.com>  
Mikkel Krautz <mikkel@krautz.dk> <krautz@gmail.com>  
Mikoaj Baranowski <mikolajb@gmail.com>  
Milan Knezevic <milan.knezevic@mips.com>  
Milutin Jovanovic <jovanovic.milutin@gmail.com>  
MinJae Kwon <mingrammer@gmail.com>  
Miquel Sabat Sol <mikisabate@gmail.com>  
Miroslav Genov <mgenov@gmail.com>  
Misty De Meo <mistydemeo@gmail.com>  
Mohit Agarwal <mohit@sdf.org>  
Mohit kumar Bajoria <mohitbajo36@gmail.com>  
Momchil Velikov <momchil.velikov@gmail.com>  
Monis Khan <mkhan@redhat.com>  
Monty Taylor <mordred@inaugust.com>  
Moriyoshi Koizumi <mozo@mozo.jp>  
Morten Siebuhr <sbhr@sbhr.dk>

Mshe van der Sterre <moshevds@gmail.com>  
Mostyn Bramley-Moore <mostyn@antipode.se>  
Mrunal Patel <mrunalp@gmail.com>  
Muhammad Falak R Wani <falakreyaz@gmail.com>  
Muhammed Uluyol <uluyol0@gmail.com>  
Mura Li <mura\_li@castech.com.tw>  
Nan Deng <monnand@gmail.com>  
Naoki Kanatani <k12naoki@gmail.com>  
Nate Wilkinson <nathanwilk7@gmail.com>  
Nathan Cantelmo <n.cantelmo@gmail.com>  
Nathan  
Caza <mastercactapus@gmail.com>  
Nathan Humphreys <nkhumphreys@gmail.com>  
Nathan John Youngman <nj@nathany.com>  
Nathan Otterness <otterness@cs.unc.edu>  
Nathan P Finch <nate.finch@gmail.com>  
Nathan VanBenschoten <nvanbenschoten@gmail.com>  
Nathan Youngman <git@nathany.com>  
Nathan(yinian) Hu <nathanhu@google.com>  
Nathaniel Cook <nvcook42@gmail.com>  
Naveen Kumar Sangi <naveenkumarsangi@protonmail.com>  
Neelesh Chandola <neeeesh.c98@gmail.com>  
Neil Lyons <nwjlyons@googlemail.com>  
Neuman Vong <neuman.vong@gmail.com>  
Neven Sajko <nsajko@gmail.com>  
Nevins Bartolomeo <nevins.bartolomeo@gmail.com>  
Niall Sheridan <nsheridan@gmail.com>  
Nic Day <nic.day@me.com>  
Nicholas Katsaros <nick@nickkatsaros.com>  
Nicholas Maniscalco <nicholas@maniscalco.com>  
Nicholas Ng <nickng@nickng.io>  
Nicholas Presta <nick@nickpresta.ca> <nick1presta@gmail.com>  
Nicholas Sullivan <nicholas.sullivan@gmail.com>  
Nicholas Waples <nwaples@gmail.com>  
Nick Cooper <nmvc@google.com>  
Nick Craig-Wood <nick@craig-wood.com> <nickcw@gmail.com>  
Nick  
Harper <nharper@google.com>  
Nick Kubala <nkubala@google.com>  
Nick Leli <nicholasleli@gmail.com>  
Nick Miyake <nmiyake@users.noreply.github.com>  
Nick Patavalis <nick.patavalis@gmail.com>  
Nick Petroni <npetroni@cs.umd.edu>  
Nick Robinson <nrobinson13@gmail.com>  
Nick Smolin <nick27surgut@gmail.com>  
Nicolas BRULEZ <n.brulez@gmail.com>  
Nicolas Kaiser <nikai@nikai.net>  
Nicolas Owens <mischief@offblast.org>

Nicolas S. Dade <nic.dade@gmail.com>  
Niek Sanders <niek.sanders@gmail.com>  
Niels Widger <niels.widger@gmail.com>  
Nigel Kerr <nigel.kerr@gmail.com>  
Nigel Tao <nigeltao@golang.org>  
Nik Nyby <nnyby@columbia.edu>  
Nikhil Benesch <nikhil.benesch@gmail.com>  
Niklas Schnelle <niklas.schnelle@gmail.com>  
Niko Dziemba <niko@dziemba.com>  
Nikolay Turpitko <nikolay@turpitko.com>  
Nils Larsgrd <nilsmagnus@gmail.com>  
Niranjan Godbole <niranjan8192@gmail.com>  
Nishanth Shanmugham <nishanth.gerrard@gmail.com>  
Noah Campbell <noahcampbell@gmail.com>  
Noble Johnson <noblepoly@gmail.com>  
Nodir Turakulov  
<nodir@google.com>  
Noel Georgi <git@frezbo.com>  
Norberto Lopes <nlopes.ml@gmail.com>  
Odin Ugedal <odin@ugedal.com>  
Oleg Bulatov <dmage@yandex-team.ru>  
Oleg Vakheta <helginet@gmail.com>  
Oleku Konko <oleku.konko@gmail.com>  
Oling Cat <olingcat@gmail.com>  
Oliver Hookins <ohookins@gmail.com>  
Oliver Stenbom <ostenbom@pivotal.io>  
Oliver Tonnhofer <olt@bogosoftware.com>  
Olivier Antoine <olivier.antoine@gmail.com>  
Olivier Duperray <duperray.olivier@gmail.com>  
Olivier Poitrey <rs@dailymotion.com>  
Olivier Saingre <osaingre@gmail.com>  
Omar Jarjur <ojarjur@google.com>  
Oryan Moshe <iamoryanmoshe@gmail.com>  
Osamu TONOMORI <osamingo@gmail.com>  
zgr Kesim <oec-go@kesim.org>  
Pablo Lalloni <plalloni@gmail.com>  
Pablo Rozas Larraondo <pablo.larraondo@anu.edu.au>  
Pablo Santiago Blum de Aguiar <scorphus@gmail.com>  
Padraig Kitterick <padraigkitterick@gmail.com>  
Pallat Anchaleechamaikorn <yod.pallat@gmail.com>  
Paolo Giarrusso <p.giarrusso@gmail.com>  
Paolo Martini <mrtnpaolo@gmail.com>  
Parker Moore <parkrmoore@gmail.com>  
Parminder  
Singh <parmsingh101@gmail.com>  
Pascal S. de Kloe <pascal@quies.net>  
Pat Moroney <pat@pat.email>  
Patrick Crosby <patrick@stathat.com>

Patrick Gavlin <pgavlin@gmail.com>  
Patrick Higgins <patrick.allen.higgins@gmail.com>  
Patrick Lee <pattyshack101@gmail.com>  
Patrick Mzard <patrick@mezard.eu>  
Patrick Mylund Nielsen <patrick@patrickmn.com>  
Patrick Pelletier <pp.pelletier@gmail.com>  
Patrick Riley <pfr@google.com>  
Patrick Smith <pat42smith@gmail.com>  
Paul A Querna <paul.querna@gmail.com>  
Paul Borman <borman@google.com>  
Paul Boyd <boyd.paul2@gmail.com>  
Paul Chang <paulchang@google.com>  
Paul Hammond <paul@paulhammond.org>  
Paul Hankin <paulhankin@google.com>  
Paul Jolly <paul@myitcv.org.uk>  
Paul Lalonde <paul.a.lalonde@gmail.com>  
Paul M Furley <paul@paulfurley.com>  
Paul Marks <pmarks@google.com>  
Paul Meyer <paul.meyer@microsoft.com>  
Paul Nasrat <pnasrat@google.com>  
Paul PISCUC <paul.piscuc@gmail.com>  
Paul Querna <pquerna@apache.org>  
Paul Rosania <paul.rosania@gmail.com>  
Paul Ruest  
<pruest@gmail.com>  
Paul Sbarra <Sbarra.Paul@gmail.com>  
Paul Smith <paulsmith@pobox.com> <paulsmith@gmail.com>  
Paul Tyng <paul@paultyng.net>  
Paul van Brouwershaven <paul@vanbrouwershaven.com>  
Paul Wankadia <junyer@google.com>  
Paulo Casaretto <pcasaretto@gmail.com>  
Paulo Flabiano Smorigo <pfsmorigo@linux.vnet.ibm.com>  
Pavel Paulau <pavel.paulau@gmail.com>  
Pavel Zinovkin <pavel.zinovkin@gmail.com>  
Pavlo Sumkin <ymkins@gmail.com>  
Pawel Knap <pawelknap88@gmail.com>  
Pawel Szczur <filemon@google.com>  
Percy Wegmann <ox.to.a.cart@gmail.com>  
Perry Abbott <perry.j.abbott@gmail.com>  
Petar Maymoukov <petarm@gmail.com>  
Peter Armitage <peter.armitage@gmail.com>  
Peter Bourgon <peter@bourgon.org>  
Peter Collingbourne <pcc@google.com>  
Peter Conerly <pconerly@gmail.com>  
Peter Dotchev <dotchev@gmail.com>  
Peter Froehlich <peter.hans.froehlich@gmail.com>  
Peter Gonda <pgonda@google.com>  
Peter Hoyes <pahoyes@gmail.com>

Peter Kleiweg <pkleiweg@xs4all.nl>  
Peter McKenzie <petermck@google.com>  
Peter Moody <pmoody@uber.com>  
Peter  
Morjan <pmorjan@gmail.com>  
Peter Mundy <go.peter.90@gmail.com>  
Peter Nguyen <peter@mictis.com>  
Pter Surnyi <speter.go1@gmail.com>  
Pter Szab <pts@google.com>  
Pter Szilgyi <peterke@gmail.com>  
Peter Teichman <pteichman@fastly.com>  
Peter Tseng <ptseng@squareup.com>  
Peter Waldschmidt <peter@waldschmidt.com>  
Peter Waller <peter.waller@gmail.com>  
Peter Weinberger <pjw@golang.org>  
Peter Williams <pwil3058@gmail.com>  
Peter Wu <pwu@cloudflare.com>  
Peter Zhang <i@ddatsh.com>  
Petrica Voicu <pvoicu@paypal.com>  
Phil Pearl <philip.j.r.pearl@gmail.com>  
Phil Pennock <pdp@golang.org>  
Philip Brgesen <philip.borgesen@gmail.com>  
Philip Brown <phil@bolthole.com>  
Philip Hofer <phofer@umich.edu>  
Philip K. Warren <pkwarren@gmail.com>  
Philip Nelson <me@pnelson.ca>  
Philipp Stephani <phst@google.com>  
Pierre Durand <pierredurand@gmail.com>  
Pierre Prinetti <pierreprinetti@gmail.com>  
Pierre Roullon <pierre.roullon@gmail.com>  
Piers <google@hellopiers.pro>  
Pieter Droogendijk <pieter@binky.org.uk>  
Pietro  
Gagliardi <pietro10@mac.com>  
Piyush Mishra <piyush@codeitout.com>  
Plekhanov Maxim <kishtatix@gmail.com>  
Pontus Leitzler <leitzler@gmail.com>  
Prasanna Swaminathan <prasanna@mediamath.com>  
Prashant Varanasi <prashant@prashantv.com>  
Pravendra Singh <hackpravj@gmail.com>  
Preetam Jinka <pj@preet.am>  
Qais Patankar <qaisjp@gmail.com>  
Qiuxuan Zhu <ilsh1022@gmail.com>  
Quan Tran <qeed.quan@gmail.com>  
Quan Yong Zhai <qyzhai@gmail.com>  
Quentin Perez <qperez@ocs.online.net>  
Quentin Renard <contact@asticode.com>  
Quentin Smith <quentin@golang.org>

Quinn Slack <sq@sourcegraph.com>  
Quinten Yearsley <qyearsley@chromium.org>  
Quoc-Viet Nguyen <afelion@gmail.com>  
Radek Sohlich <sohlich@gmail.com>  
Radu Berinde <radu@cockroachlabs.com>  
Rafal Jeczalik <rjeczalik@gmail.com>  
Raghavendra Nagaraj <jamdagni86@gmail.com>  
Rahul Chaudhry <rahulchaudhry@chromium.org>  
Raif S. Naffah <go@naffah-raif.name>  
Rajat Goel <rajat.goel2010@gmail.com>  
Rajath Agasthya <rajathagasthya@gmail.com>  
Rajender Reddy Kompally <rajenderreddykompally@gmail.com>  
Ralph  
Corderoy <ralph@inputplus.co.uk>  
Ramazan AYYILDIZ <rayyildiz@gmail.com>  
Ramesh Dharan <dharan@google.com>  
Raph Levien <raph@google.com>  
Raphael Geronimi <raphael.geronimi@gmail.com>  
Raul Silvera <rsilvera@google.com>  
Ravil Bikbulatov <weeellz12@gmail.com>  
RaviTeja Pothana <ravi.tezu@gmail.com>  
Ray Tung <rtung@thoughtworks.com>  
Raymond Kazlauskas <raima220@gmail.com>  
Rebecca Stambler <rstambler@golang.org>  
Reilly Watson <reillywatson@gmail.com>  
Reinaldo de Souza Jr <juniorz@gmail.com>  
Remi Gillig <remigillig@gmail.com>  
Rmy Oudompheng <oudomphe@phare.normalesup.org> <remyoudompheng@gmail.com>  
Rens Rikkerink <Ikkerens@users.noreply.github.com>  
Rhys Hiltner <rhys@justin.tv>  
Ricardo Padilha <ricardospadilha@gmail.com>  
Richard Barnes <rlb@ipv.sx>  
Richard Crowley <r@rcrowley.org>  
Richard Dingwall <rdingwall@gmail.com>  
Richard Eric Gavaletz <gavaletz@gmail.com>  
Richard Gibson <richard.gibson@gmail.com>  
Richard Miller <miller.research@gmail.com>  
Richard Musiol  
<mail@richard-musiol.de> <neelance@gmail.com>  
Rick Arnold <rickarnoldjr@gmail.com>  
Rick Hudson <rlh@golang.org>  
Rick Sayre <whorfin@gmail.com>  
Rijnard van Tonder <rvantonder@gmail.com>  
Riku Voipio <riku.voipio@linaro.org>  
Risto Jaakko Saarelm <rsaarelm@gmail.com>  
Rob Earhart <earhart@google.com>  
Rob Norman <rob.norman@infinitycloud.com>  
Rob Phoenix <rob@robphoenix.com>



Rob Pike <r@golang.org>  
Robert Daniel Kortschak <dan.kortschak@adelaide.edu.au> <dan@kortschak.io>  
Robert Dinu <r@varp.se>  
Robert Figueiredo <robfig@gmail.com>  
Robert Griesemer <gri@golang.org>  
Robert Hencke <robert.hencke@gmail.com>  
Robert Iannucci <iannucci@google.com>  
Robert Obryk <robryk@gmail.com>  
Robert Seseek <rseseek@google.com>  
Robert Snedegar <roberts@google.com>  
Robert Stepanek <robert.stepanek@gmail.com>  
Robert-Andr Mauchin <zebob.m@gmail.com>  
Roberto Clapis <robclap8@gmail.com>  
Roberto Selbach <roberto@selbach.ca>  
Robin Eklind <r.eklind.87@gmail.com>  
Rodolfo Carvalho <rhcarvalho@gmail.com>  
Rodolfo Rodriguez  
<rodolfobgibson@gmail.com>  
Rodrigo Moraes de Oliveira <rodrigo.moraes@gmail.com>  
Rodrigo Rafael Monti Kochenburger <divoxx@gmail.com>  
Roger Pau Monn <royger@gmail.com>  
Roger Peppe <rogpeppe@gmail.com>  
Roland Illig <roland.illig@gmx.de>  
Roland Shoemaker <rolandshoemaker@gmail.com>  
Roman Budnikov <romanyx90@yandex.ru>  
Roman Shchekin <mrqtros@gmail.com>  
Ron Hashimoto <mail@h2so5.net>  
Ron Minnich <rminnich@gmail.com>  
Ross Chater <rdchater@gmail.com>  
Ross Light <light@google.com> <rlight2@gmail.com>  
Ross Smith II <ross@smithii.com>  
Rowan Marshall <rowanajmarshall@gmail.com>  
Rowan Worth <sqweek@gmail.com>  
Rudi Kramer <rudi.kramer@gmail.com>  
Rui Ueyama <ruiui@google.com>  
Ruslan Nigmatullin <elessar@dropbox.com>  
Russ Cox <rsc@golang.org>  
Russell Haering <russellhaering@gmail.com>  
Ryan Bagwell <ryanbagwell@outlook.com>  
Ryan Barrett <ryanb@google.com>  
Ryan Boehning <ryan.boehning@apcera.com>  
Ryan Brown <ribrd@b@gmail.com>  
Ryan Canty <jrcanty@gmail.com>  
Ryan Dahl <ry@tinyclouds.org>  
Ryan Hitchman  
<hitchmanr@gmail.com>  
Ryan Lower <rpjlower@gmail.com>  
Ryan Roden-Corrent <ryan@rcorre.net>

Ryan Seys <ryan@ryanseys.com>  
Ryan Slade <ryanslade@gmail.com>  
Ryan Zhang <ryan.zhang@docker.com>  
Ryoichi KATO <ryo1kato@gmail.com>  
Ryuji Iwata <qt.luigi@gmail.com>  
Ryuma Yoshida <ryuma.y1117@gmail.com>  
Ryuzo Yamamoto <ryuzo.yamamoto@gmail.com>  
S.alar Onur <caglar@10ur.org>  
Sabin Mihai Rapan <sabin.rapan@gmail.com>  
Sai Cheemalapati <saicheems@google.com>  
Sakeven Jiang <jc5930@sina.cn>  
Salmn Aljammz <s@0x65.net>  
Sam Boyer <tech@samboyer.org>  
Sam Ding <samding@ca.ibm.com>  
Sam Hug <samuel.b.hug@gmail.com>  
Sam Thorogood <thorogood@google.com> <sam.thorogood@gmail.com>  
Sam Whited <sam@samwhited.com>  
Sameer Ajmani <sameer@golang.org> <ajmani@gmail.com>  
Sami Commerot <samic@google.com>  
Sami Pnknen <sami.ponkanen@gmail.com>  
Samuel Kelemen <SCKelemen@users.noreply.github.com>  
Samuel Tan <samueltan@google.com>  
Samuele Pedroni <pedronis@lucediaruna.net>  
Sanjay Menakuru <balasanjay@gmail.com>  
Santhosh Kumar  
Tekuri <santhosh.tekuri@gmail.com>  
Sarah Adams <shadams@google.com>  
Sascha Brawer <sascha@brawer.ch>  
Sasha Lionheart <lionhearts@google.com>  
Sasha Sobol <sasha@scaledinference.com>  
Scott Barron <scott.barron@github.com>  
Scott Bell <scott@sctsm.com>  
Scott Crunkleton <crunk1@gmail.com>  
Scott Ferguson <scottwferg@gmail.com>  
Scott Lawrence <bytbody@gmail.com>  
Scott Mansfield <smansfield@netflix.com>  
Scott Schwartz <scotts@golang.org>  
Scott Van Woudenberg <scottvw@google.com>  
Sean Burford <sburford@google.com>  
Sean Chen <oohcode@gmail.com>  
Sean Chittenden <seanc@joyent.com>  
Sean Christopherson <sean.j.christopherson@intel.com>  
Sean Dolphin <Sean.Dolphin@kpcompass.com>  
Sean Harger <sharger@google.com>  
Sean Rees <sean@erifax.org>  
Sebastiaan van Stijn <github@gone.nl>  
Sebastian Schmidt <yath@google.com>  
Sebastien Binet <seb.binet@gmail.com>

Sbastien Paolacci <sebastien.paolacci@gmail.com>  
Sebastien Williams-Wynn <sebastien@cytora.com>  
Seiji Takahashi <timaki.st@gmail.com>  
Sergei Skorobogatov  
<skorobo@rambler.ru>  
Sergey 'Snake' Gromov <snake.scaly@gmail.com>  
Sergey Arseev <sergey.arseev@intel.com>  
Sergey Frolov <sfrolov@google.com>  
Sergey Lukjanov <me@slukjanov.name>  
Sergey Mishin <sergeymishine@gmail.com>  
Sergey Mudrik <sergey.mudrik@gmail.com>  
Sergey Semin <gray12511@gmail.com>  
Sergio Luis O. B. Correia <sergio@correia.cc>  
Sergiusz Bazanski <bazanski@gmail.com>  
Serhii Aheienko <serhii.aheienko@gmail.com>  
Seth Hoenig <seth.a.hoenig@gmail.com>  
Seth Vargo <sethvargo@gmail.com>  
Shahar Kohanim <skohanim@gmail.com>  
Shamil Garatuev <garatuev@gmail.com>  
Shane Hansen <shanemhansen@gmail.com>  
Shaozhen Ding <dsz0111@gmail.com>  
Shaun Dunning <shaun.dunning@uservoice.com>  
Shawn Ledbetter <sledbetter@google.com>  
Shawn Smith <shawn.p.smith@gmail.com>  
Shawn Walker-Salas <shawn.walker@oracle.com>  
Shenghou Ma <minux@golang.org> <minux.ma@gmail.com>  
Shengyu Zhang <shengyu.zhang@chaitin.com>  
Shi Han Ng <shihanng@gmail.com>  
Shijie Hao <haormj@gmail.com>  
Shinji Tanaka <shinji.tanaka@gmail.com>  
Shintaro  
Kaneko <kaneshin0120@gmail.com>  
Shivakumar GN <shivakumar.gn@gmail.com>  
Shivansh Rai <shivansh@freebsd.org>  
Shun Fan <sfan@google.com>  
Silvan Jegen <s.jegen@gmail.com>  
Simon Jefford <simon.jefford@gmail.com>  
Simon Rawet <simon@rawet.se>  
Simon Thulbourn <simon+github@thulbourn.com>  
Simon Whitehead <chemnova@gmail.com>  
Sina Siadat <siadat@gmail.com>  
Sokolov Yura <funny.falcon@gmail.com>  
Song Gao <song@gao.io>  
Spencer Nelson <s@spenczar.com>  
Spencer Tung <spencertung@google.com>  
Spring Mc <heresy.mc@gmail.com>  
Srdjan Petrovic <spetrovic@google.com>  
Sridhar Venkatakrishnan <sridhar@laddoo.net>

StalkR <stalkr@stalkr.net>  
Stan Schwertly <stan@schwertly.com>  
Stanislav Afanasev <php.progger@gmail.com>  
Steeve Morin <steeve.morin@gmail.com>  
Stefan Nilsson <snilsson@nada.kth.se> <trolleriprofessorn@gmail.com>  
Stepan Shabalin <neverliberty@gmail.com>  
Stephan Renatus <srenatus@chef.io>  
Stphane Travostino <stephane.travostino@gmail.com>  
Stephen Lewis <stephen@sock.org.uk>  
Stephen Lu <steuhs@users.noreply.github.com>  
Stephen  
Ma <stephenm@golang.org>  
Stephen McQuay <stephen@mcquay.me>  
Stephen Searles <stephens2424@gmail.com>  
Stephen Weinberg <stephen@q5comm.com>  
Steve Francia <spf@golang.org>  
Steve Gilbert <stevegilbert23@gmail.com>  
Steve LoFurno <slofurno@gmail.com>  
Steve McCoy <mccoyst@gmail.com>  
Steve Newman <snewman@google.com>  
Steve Phillips <elimisteve@gmail.com>  
Steve Streeting <steve@stevestreeting.com>  
Steven Buss <sbuss@google.com>  
Steven Elliot Harris <seharris@gmail.com>  
Steven Erenst <stevenerenst@gmail.com>  
Steven Hartland <steven.hartland@multiplay.co.uk>  
Steven Littiebrant <imgroxx@gmail.com>  
Steven Wilkin <stevenwilkin@gmail.com>  
Stuart Jansen <sjansen@buscaluz.org>  
Sue Spence <virtuallysue@gmail.com>  
Sugu Sougoumarane <ssougou@gmail.com>  
Suharsh Sivakumar <suharshs@google.com>  
Sukrit Handa <sukrit.handa@utoronto.ca>  
Sunny <me@darkowlzz.space>  
Suriyaa Sundararuban <suriyaasundararuban@gmail.com>  
Suyash <dextrous93@gmail.com>  
Suzy Mueller <suzmue@golang.org>  
Sven  
Almgren <sven@tras.se>  
Sven Blumenstein <svbl@google.com>  
Sylvain Zimmer <sylvain@sylvainzimmer.com>  
Syohei YOSHIDA <syohex@gmail.com>  
Szabolcs Nagy <nsz@port70.net>  
Taavi Kivisik <taavi.kivisik@gmail.com>  
Tad Fisher <tadfisher@gmail.com>  
Tad Glines <tad.glines@gmail.com>  
Tadas Valiukas <tadovas@gmail.com>  
Taesu Pyo <pyotaesu@gmail.com>

Taj Khattrra <taj.khattrra@gmail.com>  
Takashi Matsuo <tmatsuo@google.com>  
Takayoshi Nishida <takayoshi.nishida@gmail.com>  
Takeshi YAMANASHI <9.nashi@gmail.com>  
Takuto Ikuta <tikuta@google.com>  
Takuya Ueda <uedatakuya@gmail.com>  
Tal Shprecher <tshprecher@gmail.com>  
Tamir Duberstein <tamird@gmail.com>  
Tao Shen <shentaoskyking@gmail.com>  
Tao Wang <twang2218@gmail.com>  
Tarmigan Casebolt <tarmigan@gmail.com>  
Taro Aoki <aizu.s1230022@gmail.com>  
Taru Karttunen <taruti@taruti.net>  
Tatsuhiko Tsujikawa <tatsuhiko.t@gmail.com>  
Tatsuya Kaneko <m.ddotx.f@gmail.com>  
Taufiq Rahman <taufiqrx8@gmail.com>  
Teague Cole <tnc1443@gmail.com>  
Ted Kornish <golang@tedkornish.com>  
Tejasvi  
Nareddy <tejunareddy@gmail.com>  
Terin Stock <terinjokes@gmail.com>  
Terrel Shumway <gopher@shumway.us>  
Tetsuo Kiso <tetsuokiso9@gmail.com>  
Than McIntosh <thanm@google.com>  
Thanabodee Charoenpiriyakij <wingyminus@gmail.com>  
Thanatat Tamtan <acoshift@gmail.com>  
Thiago Avelino <t@avelino.xxx>  
Thiago Fransosi Farina <thiago.farina@gmail.com> <tfarina@chromium.org>  
Thomas Alan Copeland <talan.copeland@gmail.com>  
Thomas Bonfort <thomas.bonfort@gmail.com>  
Thomas Bouldin <inlined@google.com>  
Thomas Bruyelle <thomas.bruyelle@gmail.com>  
Thomas Bushnell, BSG <tbushnell@google.com>  
Thomas de Zeeuw <thomasdezeeuw@gmail.com>  
Thomas Desrosiers <thomasdesr@gmail.com>  
Thomas Habets <habets@google.com>  
Thomas Kappler <tkappler@gmail.com>  
Thomas Meson <zllak@hycik.org>  
Thomas Wanielista <tomwans@gmail.com>  
Thorben Krueger <thorben.krueger@gmail.com>  
Thordur Bjornsson <thorduri@secnorth.net>  
Tiago Queiroz <contato@tiago.eti.br>  
Tilman Dilo <tilman.dilo@gmail.com>  
Tim Cooijmans <timcooijmans@gmail.com>  
Tim  
Cooper <tim.cooper@layeh.com>  
Tim Ebringer <tim.ebringer@gmail.com>  
Tim Heckman <t@heckman.io>

Tim Henderson <tim.tadh@gmail.com>  
Tim Hockin <thockin@google.com>  
Tim Swast <swast@google.com>  
Tim Wright <tenortim@gmail.com>  
Tim Xu <xiaoxubeii@gmail.com>  
Timo Savola <timo.savola@gmail.com>  
Timo Truys <alkaloid.btx@gmail.com>  
Timothy Studd <tim@timestudd.com>  
Tipp Moseley <tipp@google.com>  
Tobias Assarsson <tobias.assarsson@gmail.com>  
Tobias Columbus <tobias.columbus@gmail.com> <tobias.columbus@googlemail.com>  
Tobias Klauser <tklauser@distanz.ch>  
Toby Burrell <kurin@google.com>  
Todd Neal <todd@tneal.org>  
Todd Wang <toddwang@gmail.com>  
Tom Bergan <tombergan@google.com>  
Tom Heng <zhm20070928@gmail.com>  
Tom Lanyon <tomlanyon@google.com>  
Tom Levy <tomlevy93@gmail.com>  
Tom Limoncelli <tal@whatexit.org>  
Tom Linfoord <tomlinfoord@gmail.com>  
Tom Payne <twpayne@gmail.com>  
Tom Szymanski <tgs@google.com>  
Tom Thorogood <me+google@tomthorogood.co.uk>  
Tom Wilkie <tom@weave.works>  
Tommy Schaefer  
<tommy.schaefer@teecom.com>  
Tomoya Ishizaki <zaq1tomo@gmail.com>  
Tonis Tiigi <tonistiigi@gmail.com>  
Tony Reix <tony.reix@bull.net>  
Tony Walker <walkert.uk@gmail.com>  
Tor Andersson <tor.andersson@gmail.com>  
Tormod Erevik Lea <tormodlea@gmail.com>  
Toshiki Shima <hayabusa1419@gmail.com>  
Totoro W <tw19881113@gmail.com>  
Travis Bischel <travis.bischel@gmail.com>  
Travis Cline <travis.cline@gmail.com>  
Trevor Strohman <trevor.strohman@gmail.com>  
Trey Lawrence <lawrence.trey@gmail.com>  
Trey Roessig <trey.roessig@gmail.com>  
Trey Tacon <ttacon@gmail.com>  
Tristan Amini <tamini01@ca.ibm.com>  
Tristan Colgate <tcolgate@gmail.com>  
Tristan Oohry <oohry@gmail.com>  
Tristan Rice <rice@fn.lc>  
Troels Thomsen <troels@thomsen.io>  
Trung Nguyen <trung.n.k@gmail.com>  
Tudor Golubenco <tudor.g@gmail.com>

Tugdual Saunier <tugdual.saunier@gmail.com>  
Tuo Shan <sturbo89@gmail.com> <shantuo@google.com>  
Tyler Bui-Palsulich <tpalsulich@google.com>  
Tyler Bunnell <tylerbunnell@gmail.com>  
Tyler Treat <ttreat31@gmail.com>  
Tzu-Jung  
Lee <roylee17@currant.com>  
Ugorji Nwoke <ugorji@gmail.com>  
Ulf Holm Nielsen <doktor@dyregod.dk>  
Ulrich Kunitz <uli.kunitz@gmail.com>  
Umang Parmar <umangjparmar@gmail.com>  
Uriel Mangado <uriel@berlinblue.org>  
Urvil Patel <patelurvil38@gmail.com>  
Uttam C Pawar <uttam.c.pawar@intel.com>  
Vadim Grek <vadimprog@gmail.com>  
Vadim Vygonets <unixdj@gmail.com>  
Val Polouchkine <vpolouch@justin.tv>  
Vega Garcia Luis Alfonso <vegacom@gmail.com>  
Venil Noronha <veniln@vmware.com>  
Veselkov Konstantin <kostozyb@gmail.com>  
Viacheslav Poturaev <vearutop@gmail.com>  
Victor Chudnovsky <vchudnov@google.com>  
Victor Vrantchan <vrancean+github@gmail.com>  
Vignesh Ramachandra <vickyramachandra@gmail.com>  
Vikas Kedia <vikask@google.com>  
Vincent Ambo <tazjin@googlemail.com>  
Vincent Batts <vbatts@hashbangbash.com> <vbatts@gmail.com>  
Vincent Vanackere <vincent.vanackere@gmail.com>  
Vinu Rajashekhar <vinutheraj@gmail.com>  
Vish Subramanian <vish@google.com>  
Vishvananda Ishaya <vishvananda@gmail.com>  
Visweswara  
R <r.visweswara@gmail.com>  
Vitor De Mario <vitordemario@gmail.com>  
Vlad Krasnov <vlad@cloudflare.com>  
Vladimir Kovpak <cn007b@gmail.com>  
Vladimir Kuzmin <vkuzmin@uber.com>  
Vladimir Mihailenco <vladimir.webdev@gmail.com>  
Vladimir Nikishenko <vova616@gmail.com>  
Vladimir Stefanovic <vladimir.stefanovic@imgtec.com>  
Vladimir Varankin <nek.narqo@gmail.com>  
Volker Dobler <dr.volker.dobler@gmail.com>  
Volodymyr Paprotski <vpaprots@ca.ibm.com>  
W. Trevor King <wking@tremily.us>  
Wade Simmons <wade@wades.im>  
Walter Poupore <wpoupore@google.com>  
Wander Lairson Costa <wcosta@mozilla.com>  
Warren Fernandes <warren.f.fernandes@gmail.com>

Wayne Ashley Berry <wayneashleyberry@gmail.com>  
Wedson Almeida Filho <wedsonaf@google.com>  
Weerasak Chongnguluam <singpor@gmail.com>  
Wi Cngru <crvv.mail@gmail.com>  
Wei Fu <fhfuwei@163.com>  
Wei Guangjing <vcc.163@gmail.com>  
Wei Xiao <wei.xiao@arm.com>  
Weichao Tang <tevic.tt@gmail.com>  
Wembley G. Leach, Jr <wembley.gl@gmail.com>  
Wil Selwood <wselwood@gmail.com>  
Wilfried  
Teiken <wteiken@google.com>  
Will Beason <willbeason@gmail.com>  
Will Chan <willchan@google.com>  
Will Faught <will.faught@gmail.com>  
Will Morrow <wmorrow.qdt@qualcommdatacenter.com>  
Will Norris <willnorris@google.com>  
Will Storey <will@summercat.com>  
Willem van der Schyff <willemvds@gmail.com>  
William Chan <willchan@chromium.org>  
William Chang <mr.williamchang@gmail.com>  
William Josephson <wjosephson@gmail.com>  
William Orr <will@worrbase.com> <ay1244@gmail.com>  
Wisdom Omuya <deafgoat@gmail.com>  
Wu Yunzhou <yunzhouwu@gmail.com>  
Xi Ruoyao <xry23333@gmail.com>  
Xia Bin <snyh@snyh.org>  
Xing Xing <mikespook@gmail.com>  
Xu Fei <badgangkiller@gmail.com>  
Xudong Zhang <felixmelon@gmail.com>  
Xudong Zheng <7pkvm5aw@slicealias.com>  
Xuyang Kang <xuyangkang@gmail.com>  
Yamagishi Kazutoshi <ykzts@desire.sh>  
Yan Zou <yzou@google.com>  
Yann Hodique <yhodique@google.com>  
Yann Kerherv <yann.kerherve@gmail.com>  
Yann Salan <yannsalaun1@gmail.com>  
Yao Zhang <lunaria21@gmail.com>  
Yaron de Leeuw <jarondl@google.com>  
Yasha  
Bubnov <girokompass@gmail.com>  
Yasuharu Goto <matope.ono@gmail.com>  
Yasuhiro Matsumoto <mattn.jp@gmail.com>  
Yasuyuki Oka <yasuyk@gmail.com>  
Yazen Shunnar <yazen.shunnar@gmail.com>  
Yestin Sun <ylh@pdx.edu>  
Yesudeep Mangalapilly <yesudeep@google.com>  
Yissakhar Z. Beck <yissakhar.beck@gmail.com>



Yo-An Lin <yoanlin93@gmail.com>  
Yohei Takeda <yo.tak0812@gmail.com>  
Yongjian Xu <i3dmaster@gmail.com>  
Yorman Arias <cixtors@gmail.com>  
Yoshiyuki Kanno <nekotaroh@gmail.com> <yoshiyuki.kanno@stoic.co.jp>  
Yoshiyuki Mineo <yoshiyuki.mineo@gmail.com>  
Yosuke Akatsuka <yosuke.akatsuka@gmail.com>  
Yu Heng Zhang <annita.zhang@cn.ibm.com>  
Yu Xuan Zhang <zyxsh@cn.ibm.com>  
Yuji Yaginuma <yuuji.yaginuma@gmail.com>  
Yuki OKUSHI <huyuumi.dev@gmail.com>  
Yuki Yugui Sonoda <yugui@google.com>  
Yukihiro Nishinaka <6elpinal@gmail.com>  
Yury Smolsky <yury@smolsky.by>  
Yusuke Kagiwada <block.rxckin.beats@gmail.com>  
Yuusei Kuwana <kuwana@kumama.org>  
Yuval Pavel Zholkover <paulzhol@gmail.com>  
Yves Junqueira <yvesj@google.com>  
<yves.junqueira@gmail.com>  
Zac Bergquist <zbergquist99@gmail.com>  
Zach Bintliff <zbintliff@gmail.com>  
Zach Gershman <zachgersh@gmail.com>  
Zachary Amsden <zach@thundertoken.com>  
Zachary Gershman <zgershman@pivotal.io>  
Zak <zrjknill@gmail.com>  
Zakatell Kanda <hi@zkanda.io>  
Zellyn Hunter <zellyn@squareup.com> <zellyn@gmail.com>  
Zev Goldstein <zev.goldstein@gmail.com>  
Zheng Dayu <davidzheng23@gmail.com>  
Zheng Xu <zheng.xu@arm.com>  
Zhengyu He <hzy@google.com>  
Zhongpeng Lin <zplin@uber.com>  
Zhongtao Chen <chenzhongtao@126.com>  
Zhongwei Yao <zhongwei.yao@arm.com>  
Zhou Peng <p@ctruple.cn>  
Ziad Hatahet <hatahet@gmail.com>  
Zorion Arrizabalaga <zorionk@gmail.com>  
<max.faceless.frei@gmail.com>  
<hagen1778@gmail.com>  
<tbunyk@gmail.com>  
<faxriddinjon@gmail.com>  
<zs349596@gmail.com>  
<bronze1man@gmail.com>

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## 1.262 docker

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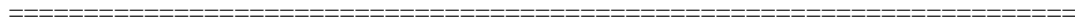
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package licensing

```
import (
 "context"
 "crypto/hmac"
 "crypto/sha256"
 "encoding/base64"
 "encoding/json"
 "fmt"
 "time"

 "github.com/docker/libtrust"
 "github.com/docker/licensing/lib/errors"
 "github.com/docker/licensing/lib/go-clientlib"
 "github.com/docker/licensing/model"
)
```

```

func (c *client) getLicenseFile(ctx context.Context, subID string) (*model.IssuedLicense, error) {
 url := c.baseURI
 url.Path += fmt.Sprintf("/api/billing/v4/subscriptions/%s/license-file", subID)

 license := new(model.IssuedLicense)
 if _, _, err := c.doReq(ctx, "GET", &url, clientlib.RecvJSON(license)); err != nil {
 return nil, err
 }

 return license, nil
}

// Check verifies that the license identified by the given key id is valid. Note that it does not
// interrogate the contents of the license.
func (c *client) check(ctx context.Context, license model.IssuedLicense) (*model.CheckResponse, error) {
 keyID := license.KeyID
 privateKey := license.PrivateKey

 authorization, err := c.getAuthorization(ctx,
 license)
 if err != nil {
 return nil, err
 }

 // TODO: Mason - replace this parseJWS with a non libtrust lib
 signature, err := libtrust.ParseJWS(authorization)
 if err != nil {
 return nil, errors.Wrapf(err, errors.Fields{
 "key_id": keyID,
 }, "license parse JWS failed")
 }

 keys, err := signature.Verify()
 if err != nil {
 return nil, errors.Wrapf(err, errors.Fields{
 "key_id": keyID,
 }, "license signature verification failed")
 }

 keyCnt := len(keys)
 if keyCnt != 1 {
 err = fmt.Errorf("unexpected number of signing keys (%d)", keyCnt)
 return nil, errors.WithStack(err).With(errors.Fields{
 "key_id": keyID,
 })
 }
}

```

```

key := keys[0]

if !c.recognizedSigningKey(key) {
 return nil, errors.New("unrecognized signing key")
}

payload, err := signature.Payload()
if err != nil {
 return nil, errors.Wrapf(err, errors.Fields{
 "key_id": keyID,
 }, "malformed signature payload")
}

checkRes := new(model.CheckResponse)

err = json.Unmarshal(payload, &checkRes)
if
err != nil {
 return nil, errors.Wrapf(err, errors.Fields{
 "key_id": keyID,
 }, "license payload unmarshal failed")
}

msg := checkRes.Expiration.Format(time.RFC3339)
if err := checkToken(msg, checkRes.Token, privateKey); err != nil {
 return nil, errors.Wrap(err, errors.Fields{
 "key_id": keyID,
 })
}

return checkRes, nil
}

// recognizedSigningKey returns true if the given key is signed with a recognized signing key, false otherwise
func (c *client) recognizedSigningKey(key libtrust.PublicKey) bool {
 for _, publicKey := range c.publicKeys {
 if key.KeyID() == publicKey.KeyID() {
 return true
 }
 }
 return false
}

// getAuthorization returns the decoded license authorization
func (c *client) getAuthorization(ctx context.Context, license model.IssuedLicense) ([]byte, error) {
 decoded, err := base64.StdEncoding.DecodeString(license.Authorization)
 if err != nil {
 return nil, errors.Wrapf(err, errors.Fields{

```

```

 "key_id": license.KeyID,
 }, "decoding license authorization
failed")
}
return decoded, nil
}

// All of the functions in this file assume that they are receiving a properly
// formatted private key.

// checkToken performs a MAC algorithm (where token is generated by hashing the
// message with the privateKey via GenerateToken) with the purpose of authenticating
// the validity of both the message and the private key of the person who generated
// the token.
func checkToken(message, token, privateKey string) error {
 tokenBytes, err := base64.URLEncoding.DecodeString(token)
 if err != nil {
 return errors.Wrap(err, errors.Fields{"token": token})
 }

 generatedToken, err := generateToken(message, privateKey)
 if err != nil {
 return errors.Wrap(err, errors.Fields{"token": token})
 }

 generatedBytes, err := base64.URLEncoding.DecodeString(generatedToken)
 if err != nil {
 return errors.Wrap(err, errors.Fields{"token": token})
 }

 if !hmac.Equal(tokenBytes, generatedBytes) {
 return errors.Forbidden(errors.Fields{"token": token},
"invalid token")
 }

 return nil
}

// generateToken generates a hash of the message with the privateKey via the
// sha256 algorithm.
func generateToken(message, privateKey string) (string, error) {
 key, err := base64.URLEncoding.DecodeString(privateKey)
 if err != nil {
 return "", errors.Wrap(err, errors.Fields{"msg": message})
 }

 h := hmac.New(sha256.New, key)
 h.Write([]byte(message))

```

```
return base64.URLEncoding.EncodeToString(h.Sum(nil)), nil
}
```

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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package model

```
import "time"
```

```
// A CheckResponse is the internal content of the PublicCheckResponse signed
```

```
// json blob.
```

```
type CheckResponse struct {
```

```
 Expiration time.Time `json:"expiration"`
```

```
 Token string `json:"token"`
```

```
 MaxEngines int `json:"maxEngines"`
```

```
 ScanningEnabled bool `json:"scanningEnabled"`
```

```
 Type string `json:"licenseType"`
```



```

Tier string `json:"tier"`
}

// IssuedLicense represents an issued license
type IssuedLicense struct {
 KeyID string `json:"key_id"`
 PrivateKey string `json:"private_key"`
 Authorization string `json:"authorization"`
}

// Valid returns true if the License is syntactically valid, false otherwise
func (l *IssuedLicense) Valid() (bool, string) {
 if l.KeyID == "" {
 return false, "empty key_id"
 }

 if l.PrivateKey == "" {
 return false, "empty private_key"
 }

 if l.Authorization == "" {
 return false, "empty authorization"
 }

 return true, ""
}

```

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Common libraries shared by Prometheus Go components.

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package formatter

```
import (
 "bytes"
 "encoding/json"
 "strings"
 "testing"
 "time"

 "github.com/docker/cli/internal/licenseutils"
 "github.com/docker/licensing/model"
 "gotest.tools/assert"
 is "gotest.tools/assert/cmp"
)

func TestSubscriptionContextWrite(t *testing.T) {
 cases := []struct {
 context Context
 expected string
 }{
 // Errors
 {
 Context{Format: "{{InvalidFunction}}"},
 `Template parsing error: template: :1: function "InvalidFunction" not defined`
 },
 {
 Context{Format: "{{nil}}"},
 `Template parsing error: template: :1:2: executing "" at <nil>: nil is not a command`
 },
 // Table format
 {
 Context{Format: NewSubscriptionsFormat("table", false)},
 `NUM OWNER PRODUCT ID EXPIRES PRICING COMPONENTS`
 }
 }

 for _, c := range cases {
 t.Run(c.expected, func(t *testing.T) {
 // ...
 })
 }
}
```

```

2 owner2 productid2 2020-01-01 10:00:00 +0000
UTC compstring
`,
},
{
 Context{Format: NewSubscriptionsFormat("table", true)},
 `1:License Name: name1 Quantity: 10 nodes Expiration date: 2020-01-01
2:License Name: name2 Quantity: 20 nodes Expiration date: 2020-01-01
`,
},
{
 Context{Format: NewSubscriptionsFormat("table {{.Owner}}", false)},
 `OWNER
owner1
owner2
`,
},
{
 Context{Format: NewSubscriptionsFormat("table {{.Owner}}", true)},
 `OWNER
owner1
owner2
`,
},
// Raw Format
{
 Context{Format: NewSubscriptionsFormat("raw", false)},
 `license: id1
name: name1
owner: owner1
components: compstring

license: id2
name: name2
owner: owner2
components: compstring

`,
},
{
 Context{Format: NewSubscriptionsFormat("raw", true)},
 `license: id1
license: id2
`,
},
// Custom Format
{
 Context{Format: NewSubscriptionsFormat("{{.Owner}}", false)},

```

```

 `owner1
owner2
`,
},
}

expiration, _ := time.Parse(time.RFC822, "01 Jan 20 10:00 UTC")

for _, testcase := range cases {
 subscriptions
:= []licenseutils.LicenseDisplay{
 {
 Num: 1,
 Owner: "owner1",
 Subscription: model.Subscription{
 ID: "id1",
 Name: "name1",
 ProductID: "productid1",
 Expires: &expiration,
 PricingComponents: model.PricingComponents{
 &model.SubscriptionPricingComponent{
 Name: "nodes",
 Value: 10,
 },
 },
 },
 ComponentsString: "compstring",
 },
 {
 Num: 2,
 Owner: "owner2",
 Subscription: model.Subscription{
 ID: "id2",
 Name: "name2",
 ProductID: "productid2",
 Expires: &expiration,
 PricingComponents: model.PricingComponents{
 &model.SubscriptionPricingComponent{
 Name: "nodes",
 Value: 20,
 },
 },
 },
 ComponentsString: "compstring",
 },
}
 out := &bytes.Buffer{}
 testcase.context.Output = out
}

```



```

err := SubscriptionsWrite(testcase.context, subscriptions)
if err != nil {
 assert.Error(t, err, testcase.expected)
} else {
 assert.Check(t,
is.Equal(testcase.expected, out.String()))
}
}
}

func TestSubscriptionContextWriteJSON(t *testing.T) {
 expiration, _ := time.Parse(time.RFC822, "01 Jan 20 10:00 UTC")
 subscriptions := []licenseutils.LicenseDisplay{
 {
 Num: 1,
 Owner: "owner1",
 Subscription: model.Subscription{
 ID: "id1",
 Name: "name1",
 ProductID: "productid1",
 Expires: &expiration,
 PricingComponents: model.PricingComponents{
 &model.SubscriptionPricingComponent{
 Name: "nodes",
 Value: 10,
 },
 },
 ComponentsString: "compstring",
 },
 },
 {
 Num: 2,
 Owner: "owner2",
 Subscription: model.Subscription{
 ID: "id2",
 Name: "name2",
 ProductID: "productid2",
 Expires: &expiration,
 PricingComponents: model.PricingComponents{
 &model.SubscriptionPricingComponent{
 Name: "nodes",
 Value: 20,
 },
 },
 ComponentsString: "compstring",
 },
 },
 }
}

```

```

expectedJSONs := []map[string]interface{}{
{
 "Owner":
 "owner1",
 "ComponentsString": "compstring",
 "Expires": "2020-01-01T10:00:00Z",
 "DockerID": "",
 "Eusa": nil,
 "ID": "id1",
 "Start": nil,
 "Name": "name1",
 "Num": float64(1),
 "PricingComponents": []interface{}{
 map[string]interface{}{
 "name": "nodes",
 "value": float64(10),
 },
 },
 "ProductID": "productid1",
 "ProductRatePlan": "",
 "ProductRatePlanID": "",
 "State": "",
 "Summary": "License Name: name1\tQuantity: 10 nodes\tExpiration date: 2020-01-01",
},
{
 "Owner": "owner2",
 "ComponentsString": "compstring",
 "Expires": "2020-01-01T10:00:00Z",
 "DockerID": "",
 "Eusa": nil,
 "ID": "id2",
 "Start": nil,
 "Name": "name2",
 "Num": float64(2),
 "PricingComponents": []interface{}{
 map[string]interface{}{
 "name":
"nodes",
 "value": float64(20),
 },
 },
 "ProductID": "productid2",
 "ProductRatePlan": "",
 "ProductRatePlanID": "",
 "State": "",
 "Summary": "License Name: name2\tQuantity: 20 nodes\tExpiration date: 2020-01-01",
},
}

```

```

out := &bytes.Buffer{ }
err := SubscriptionsWrite(Context{Format: "{{json .}}", Output: out}, subscriptions)
if err != nil {
 t.Fatal(err)
}
for i, line := range strings.Split(strings.TrimSpace(out.String()), "\n") {
 var m map[string]interface{ }
 if err := json.Unmarshal([]byte(line), &m); err != nil {
 t.Fatal(err)
 }
 assert.Check(t, is.DeepEqual(expectedJSONs[i], m))
}
}

func TestSubscriptionContextWriteJSONField(t *testing.T) {
 subscriptions := []licenseutils.LicenseDisplay{
 {Num: 1, Owner: "owner1"},
 {Num: 2, Owner: "owner2"},
 }
 out := &bytes.Buffer{ }
 err := SubscriptionsWrite(Context{Format: "{{json .Owner}}", Output: out}, subscriptions)
 if
 err != nil {
 t.Fatal(err)
 }
 for i, line := range strings.Split(strings.TrimSpace(out.String()), "\n") {
 var s string
 if err := json.Unmarshal([]byte(line), &s); err != nil {
 t.Fatal(err)
 }
 assert.Check(t, is.Equal(subscriptions[i].Owner, s))
 }
}

```

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Gocheck - A rich testing framework for Go

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package formatter

```
import (
 "time"

 "github.com/docker/cli/internal/licenseutils"
 "github.com/docker/licensing/model"
)

const (
 defaultSubscriptionsTableFormat = "table
{ {.Num} }\t{ {.Owner} }\t{ {.ProductID} }\t{ {.Expires} }\t{ {.ComponentsString} }"
 defaultSubscriptionsQuietFormat = "{ {.Num} }:{ {.Summary} }"

 numHeader = "NUM"
 ownerHeader = "OWNER"
 licenseNameHeader = "NAME"
 idHeader = "ID"
 dockerIDHeader = "DOCKER ID"
 productIDHeader = "PRODUCT ID"
 productRatePlanHeader = "PRODUCT RATE PLAN"
 productRatePlanIDHeader = "PRODUCT RATE PLAN ID"
 startHeader = "START"
 expiresHeader = "EXPIRES"
 stateHeader = "STATE"
 eusaHeader = "EUSA"
 pricingComponentsHeader = "PRICING COMPONENTS"
)
```

```

// NewSubscriptionsFormat returns a Format for rendering using a license Context
func NewSubscriptionsFormat(source string, quiet bool) Format {
switch source {
case TableFormatKey:
if quiet
{
return defaultSubscriptionsQuietFormat
}
return defaultSubscriptionsTableFormat
case RawFormatKey:
if quiet {
return `license: {{.ID}}`
}
return `license: {{.ID}}\nname: {{.Name}}\nowner: {{.Owner}}\ncomponents: {{.ComponentsString}}\n`
}
return Format(source)
}

```

```

// SubscriptionsWrite writes the context
func SubscriptionsWrite(ctx Context, subs []licenseutils.LicenseDisplay) error {
render := func(format func(subContext subContext) error) error {
for _, sub := range subs {
licenseCtx := &licenseContext{trunc: ctx.Trunc, l: sub}
if err := format(licenseCtx); err != nil {
return err
}
}
return nil
}
licenseCtx := licenseContext{ }
licenseCtx.header = map[string]string{
"Num": numHeader,
"Owner": ownerHeader,
"Name": licenseNameHeader,
"ID": idHeader,
"DockerID": dockerIDHeader,
"ProductID": productIDHeader,
"ProductRatePlan": productRatePlanHeader,
"ProductRatePlanID":
productRatePlanIDHeader,
"Start": startHeader,
"Expires": expiresHeader,
"State": stateHeader,
"Eusa": eusaHeader,
"ComponentsString": pricingComponentsHeader,
}
return ctx.Write(&licenseCtx, render)
}

```

```

}

type licenseContext struct {
 HeaderContext
 trunc bool
 l licenseutils.LicenseDisplay
}

func (c *licenseContext) MarshalJSON() ([]byte, error) {
 return marshalJSON(c)
}

func (c *licenseContext) Num() int {
 return c.l.Num
}

func (c *licenseContext) Owner() string {
 return c.l.Owner
}

func (c *licenseContext) ComponentsString() string {
 return c.l.ComponentsString
}

func (c *licenseContext) Summary() string {
 return c.l.String()
}

func (c *licenseContext) Name() string {
 return c.l.Name
}

func (c *licenseContext) ID() string {
 return c.l.ID
}

func (c *licenseContext) DockerID() string {
 return c.l.DockerID
}

func (c *licenseContext) ProductID() string {
 return c.l.ProductID
}

func (c *licenseContext)
 ProductRatePlan() string {
 return c.l.ProductRatePlan
}

```

```

func (c *licenseContext) ProductRatePlanID() string {
 return c.l.ProductRatePlanID
}

func (c *licenseContext) Start() *time.Time {
 return c.l.Start
}

func (c *licenseContext) Expires() *time.Time {
 return c.l.Expires
}

func (c *licenseContext) State() string {
 return c.l.State
}

func (c *licenseContext) Eusa() *model.EusaState {
 return c.l.Eusa
}

func (c *licenseContext) PricingComponents() []model.SubscriptionPricingComponent {
 // Dereference the pricing component pointers in the pricing components
 // so it can be rendered properly with the template formatter

 var ret []model.SubscriptionPricingComponent
 for _, spc := range c.l.PricingComponents {
 if spc == nil {
 continue
 }
 ret = append(ret, *spc)
 }
 return ret
}

```

CoreOS Project

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Gocheck - A rich testing framework for Go

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Go support for Protocol Buffers - Google's data interchange format

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## 1.264 six 1.11.0-3.ph3

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The primary author and maintainer of six is Benjamin Peterson. He would like to acknowledge the following people who submitted bug reports, pull requests, and otherwise worked to improve six:

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# 1.267 ply 3.11

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```
PLY (Python Lex-Yacc) Version 3.11
```

```
[![Build Status](https://travis-ci.org/dabeaz/ply.svg?branch=master)](https://travis-ci.org/dabeaz/ply)
```

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Introduction

=====

PLY is a 100% Python implementation of the common parsing tools lex and yacc. Here are a few highlights:

- PLY is very closely modeled after traditional lex/yacc.  
If you know how to use these tools in C, you will find PLY to be similar.
- PLY provides *very* extensive error reporting and diagnostic information to assist in parser construction. The original implementation was developed for instructional purposes. As a result, the system tries to identify the most common types of errors made by novice users.
- PLY provides full support for empty productions, error recovery, precedence specifiers, and moderately ambiguous grammars.
- Parsing is based on LR-parsing which is fast, memory efficient, better suited to large grammars, and which has a number of nice properties when dealing with syntax errors and other parsing problems. Currently, PLY builds its parsing tables using the LALR(1) algorithm used in yacc.
- PLY uses Python introspection features to build lexers and parsers. This greatly simplifies the task of parser construction since it reduces the number of files and eliminates the need to run a separate lex/yacc tool before running your program.

- PLY can be used to build parsers for "real" programming languages. Although it is not ultra-fast due to its Python implementation, PLY can be used to parse grammars consisting of several hundred rules (as might be found for a language like C). The lexer and LR parser are also reasonably efficient when parsing typically sized programs. People have used PLY to build parsers for C, C++, ADA, and other real programming languages.

#### How to Use

=====

PLY consists of two files : lex.py and yacc.py. These are contained within the 'ply' directory which may also be used as a Python package. To use PLY, simply copy the 'ply' directory to your project and import lex and yacc from the associated 'ply' package. For example:

```
import ply.lex as lex
import ply.yacc as yacc
```

Alternatively, you can copy just the files lex.py and yacc.py individually and use them as modules. For example:

```
import lex
import yacc
```

The file setup.py can be used to install ply using distutils.

The file doc/ply.html contains complete documentation on how to use the system.

The example directory contains several different examples including a PLY specification for ANSI C as given in K&R 2nd Ed.

A simple example is found at the end of this document

#### Requirements

=====

PLY requires the use of Python 2.6 or greater. However, you should use the latest Python release if possible. It should work on just about any platform. PLY has been tested with both CPython and Jython. It also seems to work with IronPython.

#### Resources

=====

More information about PLY can be obtained on the PLY webpage at:



<http://www.dabeaz.com/ply>

For a detailed overview of parsing theory, consult the excellent book "Compilers : Principles, Techniques, and Tools" by Aho, Sethi, and Ullman. The topics found in "Lex & Yacc" by Levine, Mason, and Brown may also be useful.

The GitHub page for PLY can be found at:

<https://github.com/dabeaz/ply>

An old and relatively inactive discussion group for PLY is found at:

<http://groups.google.com/group/ply-hack>

#### Acknowledgments

=====

A special thanks is in order for all of the students in CS326 who suffered through about 25 different versions of these tools :-).

The CHANGES file acknowledges those who have contributed patches.

Elias Ioup did the first implementation of LALR(1) parsing in PLY-1.x. Andrew Waters and Markus Schoepflin were instrumental in reporting bugs and testing a revised LALR(1) implementation for PLY-2.0.

#### Special Note for PLY-3.0

=====

PLY-3.0 the first PLY release to support Python 3. However, backwards compatibility with Python 2.6 is still preserved. PLY provides dual Python 2/3 compatibility by restricting its implementation to a common subset of basic language features. You should not convert PLY using 2to3--it is not necessary and may in fact break the implementation.

#### Example

=====

Here is a simple example showing a PLY implementation of a calculator with variables.

```

calc.py

A simple calculator with variables.

```

```

tokens = (
 'NAME','NUMBER',
 'PLUS','MINUS','TIMES','DIVIDE','EQUALS',
 'LPAREN','RPAREN',
)

```

```
Tokens
```

```

t_PLUS = r\+'
t_MINUS = r\-'
t_TIMES = r*'
t_DIVIDE = r\/'
t_EQUALS = r\='
t_LPAREN = r\('
t_RPAREN = r\) '
t_NAME = r'[a-zA-Z][a-zA-Z0-9_]*'

```

```

def t_NUMBER(t):
 r\d+'
 t.value = int(t.value)
 return t

```

```

Ignored characters
t_ignore = " \t"

```

```

def t_newline(t):
 r\n+'
 t.lexer.lineno += t.value.count("\n")

```

```

def t_error(t):
 print("Illegal character '%s'" % t.value[0])
 t.lexer.skip(1)

```

```

Build the lexer
import ply.lex as lex
lex.lex()

```

```

Precedence
rules for the arithmetic operators
precedence = (
 ('left','PLUS','MINUS'),
 ('left','TIMES','DIVIDE'),
 ('right','UMINUS'),
)

```

```

dictionary of names (for storing variables)
names = { }

```

```

def p_statement_assign(p):
 'statement : NAME EQUALS expression'
 names[p[1]] = p[3]

def p_statement_expr(p):
 'statement : expression'
 print(p[1])

def p_expression_binop(p):
 "expression : expression PLUS expression
 | expression MINUS expression
 | expression TIMES expression
 | expression DIVIDE expression"
 if p[2] == '+': p[0] = p[1] + p[3]
 elif p[2] == '-': p[0] = p[1] - p[3]
 elif p[2] == '*': p[0] = p[1] * p[3]
 elif p[2] == '/': p[0] = p[1] / p[3]

def p_expression_uminus(p):
 'expression : MINUS expression %prec UMINUS'
 p[0] = -p[2]

def p_expression_group(p):
 'expression : LPAREN
expression RPAREN'
 p[0] = p[2]

def p_expression_number(p):
 'expression : NUMBER'
 p[0] = p[1]

def p_expression_name(p):
 'expression : NAME'
 try:
 p[0] = names[p[1]]
 except LookupError:
 print("Undefined name '%s'" % p[1])
 p[0] = 0

def p_error(p):
 print("Syntax error at '%s'" % p.value)

import ply.yacc as yacc
yacc.yacc()

while True:
 try:
 s = raw_input('calc > ') # use input() on Python 3

```

```
except EOFError:
```

```
 break
```

```
yacc.parse(s)
```

## Bug Reports and Patches

=====

My goal with PLY is to simply have a decent lex/yacc implementation for Python. As a general rule, I don't spend huge amounts of time working on it unless I receive very specific bug reports and/or patches to fix problems. I also try to incorporate submitted feature requests and enhancements into each new version. Please visit the PLY github page

at <https://github.com/dabeaz/ply> to submit issues and pull requests. To contact me about bugs and/or new features, please send email to [dave@dabeaz.com](mailto:dave@dabeaz.com).

-- Dave

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```
* /opt/cola/permits/1160874329_1620411264.5/0/dabeaz-ply-3-11-0-g0f398b7-1-tar-gz/dabeaz-ply-0f398b7/README.md
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```

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# 1.268 urllib3 1.23

## 1.268.1 Available under license :

# Contributions to the urllib3 project

## Creator & Maintainer

\* Andrey Petrov <andrey.petrov@shazow.net>

## Contributors

In chronological order:

\* victor.vde <<http://code.google.com/u/victor.vde/>>

\* HTTPS patch (which inspired HTTPSConnectionPool)

\* erikcederstrand <<http://code.google.com/u/erikcederstrand/>>

\* NTLM-authenticated HTTPSConnectionPool

\* Basic-authenticated HTTPSConnectionPool (merged into make\_headers)

\* niphlod <niphlod@gmail.com>

\* Client-verified SSL certificates for HTTPSConnectionPool

\* Response gzip and deflate encoding support

\* Better unicode support for filepost using StringIO buffers

\* btoconnor <brian@btoconnor.net>

\* Non-multipart encoding for POST requests

\* p.dobrogost <<http://code.google.com/u/@WBRSRIBZDhBFXQB6/>>

\* Code review, PEP8 compliance, benchmark fix

\* kennethreitz <me@kennethreitz.com>

\* Bugfixes, suggestions, Requests integration

\* georgemarshall <<http://github.com/georgemarshall>>

\* Bugfixes, Improvements and Test coverage

\*

Thomas Kluyver <thomas@kluyver.me.uk>

\* Python 3 support

- \* brandon-rhodes <<http://rhodesmill.org/brandon>>
  - \* Design review, bugfixes, test coverage.
  
- \* studer <[theo.studer@gmail.com](mailto:theo.studer@gmail.com)>
  - \* IPv6 url support and test coverage
  
- \* Shivaram Lingamneni <[slingamn@cs.stanford.edu](mailto:slingamn@cs.stanford.edu)>
  - \* Support for explicitly closing pooled connections
  
- \* hartator <[hartator@gmail.com](mailto:hartator@gmail.com)>
  - \* Corrected multipart behavior for params
  
- \* Thomas Weischuh <[thomas@t-8ch.de](mailto:thomas@t-8ch.de)>
  - \* Support for TLS SNI
  - \* API unification of ssl\_version/cert\_reqs
  - \* SSL fingerprint and alternative hostname verification
  - \* Bugfixes in testsuite
  
- \* Sune Kirkeby <[mig@ibofobi.dk](mailto:mig@ibofobi.dk)>
  - \* Optional SNI-support for Python 2 via PyOpenSSL.
  
- \* Marc Schlaich <[marc.schlaich@gmail.com](mailto:marc.schlaich@gmail.com)>
  - \* Various bugfixes and test improvements.
  
- \* Bryce Boe <[bbzbryce@gmail.com](mailto:bbzbryce@gmail.com)>
  - \* Correct six.moves conflict
  - \* Fixed pickle support of some exceptions
  
- \* Boris Figovsky <[boris.figovsky@ravellosystems.com](mailto:boris.figovsky@ravellosystems.com)>
  - \* Allowed to skip SSL hostname verification
  
- \* Cory Benfield  
<<http://lukasa.co.uk/about/>>
  - \* Stream method for Response objects.
  - \* Return native strings in header values.
  - \* Generate 'Host' header when using proxies.
  
- \* Jason Robinson <[jaywink@basshero.org](mailto:jaywink@basshero.org)>
  - \* Add missing WrappedSocket.fileno method in PyOpenSSL
  
- \* Audrius Butkevicius <[audrius.butkevicius@elastichosts.com](mailto:audrius.butkevicius@elastichosts.com)>
  - \* Fixed a race condition
  
- \* Stanislav Vitkovskiy <[stas.vitkovsky@gmail.com](mailto:stas.vitkovsky@gmail.com)>
  - \* Added HTTPS (CONNECT) proxy support
  
- \* Stephen Holsapple <[sholsapp@gmail.com](mailto:sholsapp@gmail.com)>

- \* Added abstraction for granular control of request fields
  
- \* Martin von Gagern <Martin.vGagern@gmx.net>
- \* Support for non-ASCII header parameters
  
- \* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
- \* Support for separate connect and request timeouts
  
- \* Peter Waller <p@pwaller.net>
- \* HTTPResponse.tell() for determining amount received over the wire
  
- \* Nipunn Koorapati <nipunn1313@gmail.com>
- \* Ignore default ports when comparing hosts for equality
  
- \* Danilo @dbrgn <http://dbrgn.ch/>
- \* Disabled TLS compression by default on Python 3.2+
- \* Disabled TLS compression in pyopenssl contrib module
- \* Configurable cipher suites in pyopenssl contrib module
  
- \* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- \* Account retries on proxy errors
  
- \* Nicolas Delaby <nicolas.delaby@ezeep.com>
- \* Use the platform-specific CA certificate locations
  
- \* Josh Schneier <https://github.com/jschneier>
- \* HTTPHeaderDict and associated tests and docs
- \* Bugfixes, docs, test coverage
  
- \* Tahia Khan <http://tahia.tk/>
- \* Added Timeout examples in docs
  
- \* Arthur Grunseid <http://grunseid.com>
- \* source\_address support and tests (with https://github.com/bui)
  
- \* Ian Cordasco <graffatcolmingov@gmail.com>
- \* PEP8 Compliance and Linting
- \* Add ability to pass socket options to an HTTP Connection
  
- \* Erik Tollerud <erik.tollerud@gmail.com>
- \* Support for standard library io module.
  
- \* Krishna Prasad <kprasad.iitd@gmail.com>
- \* Google App Engine documentation
  
- \* Aaron Meurer <asmeurer@gmail.com>
- \* Added Url.url, which unparses a Url

- \* Evgeny Kapun <abacabadabacaba@gmail.com>
- \* Bugfixes
  
- \* Benjamin Meyer <bm\_witness@yahoo.com>
- \* Security Warning Documentation update for proper capture
  
- \* Shivan Sornarajah <github@sornars.com>
- \* Support for using ConnectionPool and PoolManager as context managers.
  
- \* Alex Gaynor <alex.gaynor@gmail.com>
- \* Updates to the default SSL configuration
  
- \* Tomas Tomecek <ttomecek@redhat.com>
- \* Implemented generator for getting chunks from chunked responses.
  
- \* tlynn <https://github.com/tlynn>
- \* Respect the warning preferences at import.
  
- \* David D. Riddle <ddriddle@illinois.edu>
- \* IPv6 bugfixes in testsuite
  
- \* Jon Wayne Parrott <jonwayne@google.com>
- \* App Engine environment tests.
  
- \* John Krauss <https://github.com/talos>
- \* Clues to debugging problems with `cryptography` dependency in docs
  
- \* Disassem <https://github.com/Disassem>
- \* Fix pool-default headers not applying for url-encoded requests like GET.
  
- \* James  
Atherfold <jlatherfold@hotmail.com>
- \* Bugfixes relating to cleanup of connections during errors.
  
- \* Christian Pedersen <https://github.com/chripede>
- \* IPv6 HTTPS proxy bugfix
  
- \* Jordan Moldow <https://github.com/jmoldow>
- \* Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- \* Bugfix for ``ConnectionPool.urlopen(release\_conn=False)``.
- \* Creation of ``HTTPConnectionPool.ResponseCls``.
  
- \* Predrag Gruevski <https://github.com/obi1kenobi>
- \* Made cert digest comparison use a constant-time algorithm.
  
- \* Adam Talsma <https://github.com/a-tal>
- \* Bugfix to ca\_cert file paths.



- \* Evan Meagher <<https://evanmeagher.net>>
- \* Bugfix related to `memoryview` usage in PyOpenSSL adapter
  
- \* John Vandenberg <[jayvdb@gmail.com](mailto:jayvdb@gmail.com)>
- \* Python 2.6 fixes; pyflakes and pep8 compliance
  
- \* Andy Caldwell <[andy.m.caldwell@gmail.com](mailto:andy.m.caldwell@gmail.com)>
- \* Bugfix related to reusing connections in indeterminate states.
  
- \* Ville Skytt <[ville.skytta@iki.fi](mailto:ville.skytta@iki.fi)>
- \* Logging efficiency improvements, spelling fixes, Travis config.
  
- \* Shige Takeda <[smtakeda@gmail.com](mailto:smtakeda@gmail.com)>
- \* Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response
  
- \* Jesse Shapiro <[jesse@jesseshapiro.net](mailto:jesse@jesseshapiro.net)>
- \* Various character-encoding fixes/tweaks
- \* Disabling IPv6 DNS when IPv6 connections not supported
  
- \* David Foster <<http://dafoster.net/>>
- \* Ensure order of request and response headers are preserved.
  
- \* Jeremy Cline <[jeremy@jcline.org](mailto:jeremy@jcline.org)>
- \* Added connection pool keys by scheme
  
- \* Aviv Palivoda <[palaviv@gmail.com](mailto:palaviv@gmail.com)>
- \* History list to Retry object.
- \* HTTPResponse contains the last Retry object.
  
- \* Nate Prewitt <[nate.prewitt@gmail.com](mailto:nate.prewitt@gmail.com)>
- \* Ensure timeouts are not booleans and greater than zero.
- \* Fixed infinite loop in ``stream`` when amt=None.
- \* Added length\_remaining to determine remaining data to be read.
- \* Added enforce\_content\_length to raise exception when incorrect content-length received.
  
- \* Seth Michael Larson <[sethmichaellarson@protonmail.com](mailto:sethmichaellarson@protonmail.com)>
- \* Created selectors backport that supports PEP 475.
  
- \* Alexandre Dias <[alex.dias@smarkets.com](mailto:alex.dias@smarkets.com)>
- \* Don't retry on timeout if method not in whitelist
  
- \* Moinuddin Quadri <[moin18@gmail.com](mailto:moin18@gmail.com)>
- \* Lazily load idna package
  
- \* Tom White <[s6yg1ez3@mail2tor.com](mailto:s6yg1ez3@mail2tor.com)>
- \* Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

- \* Tim Burke <tim.burke@gmail.com>
  - \* Stop buffering entire deflate-encoded responses.
  
  - \* Tuukka Mustonen <tuukka.mustonen@gmail.com>
  - \* Add counter for status\_forcelist retries.
  
  - \* Erik Rose <erik@mozilla.com>
  - \* Bugfix to pyopenssl vendoring
  
  - \* Wolfgang Richter <wolfgang.richter@gmail.com>
  - \* Bugfix related to loading full certificate chains with PyOpenSSL backend.
  
  - \* Mike Miller <github@mikeage.net>
  - \* Logging improvements to include the HTTP(S) port when opening a new connection
  
  - \* Ioannis Tziakos <mail@itziakos.gr>
  - \* Fix ``util.selectors.\_fileobj\_to\_fd`` to accept ``long``.
  - \* Update appveyor tox setup to use the 64bit python.
  
  - \* Akamai (through Jesse Shapiro) <jshapiro@akamai.com>
  - \* Ongoing maintenance
  
  - \* Dominique Leuenberger <dimstar@opensuse.org>
  - \* Minor fixes in the test suite
  
  - \* Will Bond <will@wbond.net>
  - \* Add Python 2.6 support to ``contrib.securetransport``
  
  - \* Aleksei Alekseev <alekseev.yeskela@gmail.com>
  - \* using auth info for socks proxy
  
  - \* Chris Wilcox <git@crwilcox.com>
  - \* Improve contribution guide
  - \* Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior
  
  - \* [Your name or handle] <[email or website]>
  - \* [Brief summary of your changes]
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## 1.269 python-requests 2.19.1

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## 1.271 python-pip 18.0

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## History

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Before (and including) argparse 1.1, the argparse package was licensed under Apache License v2.0.

After argparse 1.1, all project files from the argparse project were deleted due to license compatibility issues between Apache License 2.0 and GNU GPL v2.

The project repository then had a clean start with some files taken from Python 2.7.1, so definitely all files are under Python License now.

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- \* Arfrever Frehtes Taifersar Arahesis
- \* Christophe Combelles
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=====

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- \* Christophe Combelles
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- Chris McDonough, 2011/02/16
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#### A. HISTORY OF THE SOFTWARE

=====

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member

of  
the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL- compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3				
2.1.2	2002	PSF	yes	
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
2.7	2.6	2010	PSF	yes

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## 1.272 pyopenssl 18.0.0

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# 1.273 sysv-init 2.86-1

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```

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```

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Send patches to `sysvinit-devel@nongnu.org`

The of the start-stop-daemon

- \* A rewrite
- of the original Debian's start-stop-daemon Perl script
- \* in C (faster - it is executed many times during system startup).
- \*
- \* Written by Marek Michalkiewicz <marekm@i17linuxb.ists.pwr.wroc.pl>,
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## 1.274 hd-parm 9.56

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Version 2, June 1991

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## 1.276 pexpect 4.6.0

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# 1.278 procps 3.3.15

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## 1.279 shadow 4.6

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List of Contributors

=====

Microsoft Corporation

Brian Wengert (bwengert79)

Leslie Brody (Les1966)

Michael M (M1xa)

Matt Peterson (MattPeterson1)

Dmitry Kolomiets (kolomiets)

rdeterre

DeCarabas

luisfeliu

intercommiura

halex2005

simonlep

jracle

gandziej

adish

LeonidCSIT

kreuzerkrieg

evanc

Abinsula s.r.l.

Gianfranco Costamagna (LocutusOfBorg)

AutoDesk Inc.

Cyrille Fauvel (cyrillef)

Illumina Inc.

Gery Vessere (gery@vessere.com)

Cisco Systems

Gergely Lukacsy (glukacsy)

Chris Deering (deeringc)

Ocedo GmbH

Henning Pfeiffer (megaposer)

thomasschaub

Trimble

Tim Boundy (gigaplex)

Rami Abughazaleh (icnocop)  
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Ren Nyffenegger [rene.nyffenegger@adp-gmbh.ch](mailto:rene.nyffenegger@adp-gmbh.ch)



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L. Peter Deutsch  
ghost@aladdin.com

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Ren Nyffenegger rene.nyffenegger@adp-gmbh.ch

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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# 1.286 lvm2 2.02.180

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## 1.287 safelib 4.1.3

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## 1.290 python-certifi 2018.08.13

### 1.290.1 Available under license :

This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#  
This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:  
<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>  
It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication.  
Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$

\$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

# 1.291 protobuf-c 1.3.1+git0+269771b4b4

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## 1.292 zlib 1.2.11

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/\* zlib.h -- interface of the 'zlib' general purpose compression library  
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jloup@gzip.org      madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

\*/

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Mon, 15 Nov 2004 12:20:42 +0100.

The debian package files have been derived from the original  
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Upstream Author(s): Lars Fenneberg <lf@elemental.net>

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;*!;._>KfBqxY(F|0.Ve%S61opMG`YfRXvY[R+t[BS=)d ;*!;._>KfBqxY(F|0.Ve%Q?f_{+T@Uo)2fgYZ
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;*!;_>KfBqxY(F|0.Ve%S61opMG`YfRXvY[R+t[BS=)d
;*!;_>KfBqxYm+sS|11np#_(~*uM%S61opMG`YfRXvY[R+t[BS=)d
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*sS|11np#_(~*u7gi}[IgYZ
```

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## 1.302 octeon-sdk 3.3.8

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```
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```

```
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```

```
CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h | 16 ++++++++
../modules/driver/src/common/cn73xx_pf_regs.h | 6 +---
../modules/driver/src/common/cn73xx_vf_regs.h | 6 +---
../driver/src/firmware/apps/common/cvmcs-common.c | 8 +---
../src/firmware/apps/common/cvmcs-common.mk | 2 +-
../driver/src/firmware/apps/ioq/cvmcs-ioq-main.c | 41 ++++++++-----
../modules/driver/src/firmware/core/core-driv.mk | 2 +-
../driver/src/firmware/core/cvm-driver-defs.h | 13 +++++-
../modules/driver/src/firmware/core/cvm-driv.c | 4 +--
../modules/driver/src/firmware/core/cvm-pci-pko.c | 6 ++++
../modules/driver/src/firmware/core/cvm-pci-pko.h | 3 +-
../driver/src/host/linux/user/drv/octeon_ioif.c | 2 +-
12 files changed, 77 insertions(+), 32 deletions(-)
```

```
diff --git a/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
```

```
b/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
```

```
index 975002f..cb3da3f 100644
```

```
--- a/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
```

```
+++ b/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
```

```
@@ -198,11 +198,10 @@
```

```
#error Undefined __CAVIUM_BYTE_ORDER
```

```
#endif /*#if __CAVIUM_BYTE_ORDER == __CAVIUM_BIG_ENDIAN */
```

```
-
```

```
/** Swap 8B blocks */
```

```
#ifdef OCTEON_SWAP_DATA_BYTES
```

```
static __inline void
```

```
octeon_swap_8B_data(uint64_t *data, uint32_t blocks)
```

```
{
```

```
@@ -217,10 +216,25 @@ octeon_swap_8B_data(uint64_t *data UNUSED, uint32_t blocks UNUSED)
```

```
{
```

```
return;
```

```
}
```

```

#endif

+static __inline void
+octeon_swap_info(uint64_t *data, uint32_t blocks)
+{
+ /*In OCTEON to host, Data + Response HDR is correct but resp_hdr->len seems
+ * to be swapped*/
+#ifdef OCTEON_LE
+ while(blocks) {
+ *data = ENDIAN_SWAP_8_BYTE(*data);
+ blocks--; data++;
+ }
+#else
+ octeon_swap_8B_data(data, blocks);
+#endif
+}
+

#ifdef OCTEON_SWAP_DATA_BYTES
/** Swap 4B blocks */
static
__inline void
octeon_swap_4B_data(uint32_t *data, uint32_t blocks)
diff --git a/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_pf_regs.h b/CNNIC-SDK-
SRC/modules/driver/src/common/cn73xx_pf_regs.h
index 84d6a17..b833dc0 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_pf_regs.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_pf_regs.h
@@ -50,18 +50,14 @@
#define CN73XX_CONFIG_PCIE_FLTMSK 0x720

-
-#ifndef OCTEON_LE
-#define CN73XX_ENDIAN_SWAP 0
-#else
+#ifndef CN73XX_ENDIAN_SWAP
#define CN73XX_ENDIAN_SWAP 1
#endif

-
-/*
-##### BAR0 Registers #####
-*/

#define CN73XX_SLI_CTL_PORT_START 0x286E0

```





```

+#endif

int
is_control_core(uint32_t core_id)
{
 return (core_id == control_core);
@@ -509,11 +509,11 @@ cvmcs_common_hostfw_handshake(uint32_t app_mode, uint32_t hs_support)
 uint32_t num_vfs = 0, rpvf = 0, num_pfs = 0, pvf_val
 = 0;

 uint64_t num_if = 0ULL;
 uint64_t phys_addr = 0ULL, scratch_val= 0ULL;
 uint8_t *block_ptr = NULL, counter = NB_ALLOC_MAX_TRIES;
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
 int node = cvmx_get_node_num();
 if(OCTEON_IS_MODEL(OCTEON_CN56XX))
 scratch_reg_addr = CVMX_PEXP_NPEI_SCRATCH_1;

 CFG_GET_APP_MODE(&oct_cfg) = app_mode;
@@ -641,11 +641,11 @@ cvmcs_common_hostfw_handshake(uint32_t app_mode, uint32_t hs_support)

int cvmcs_common_send_active_indication()
{
 int node = cvmx_get_node_num();
 int64_t scratch_val = 0LL, rst_boot =0;
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
 uint64_t coproc_clk =0, core_clk =0;

 /* Get pkind for DPI and send it to host */
 if(OCTEON_IS_MODEL(OCTEON_CN78XX) || OCTEON_IS_MODEL(OCTEON_CN73XX))
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
index b798e26..3ad71c1 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
@@ -38,11 +38,11 @@ d := $(dir)
LIBRARY := $(OBJ_DIR)/libcvmcscommon.a

OBJS_$(d) := $(OBJ_DIR)/cvmcs-common.o
OBJS_$(d) += $(OBJ_DIR)/cvm-app.o

-$(OBJS_$(d)): CFLAGS_LOCAL := -I$(d) -I$(d)/../core/ -I$(CNNIC_ROOT)/modules/driver/src/common -O2 -
-g -Werror
+$(OBJS_$(d)): CFLAGS_LOCAL := -I$(d) -I$(d)/../core/ -I$(CNNIC_ROOT)/modules/driver/src/common -
I$(OCTEON_ROOT)/executive -O2 -g -Werror

```

```
standard component Makefile rules
```

```
DEPS_$(d) := $(OBJS_$(d):.o=.d)
```

```
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
```

```
index a98b19e..7fb8234 100644
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
```

```
+++
```

```
b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
```

```
@@ -96,11 +96,11 @@ cvmcs_ioq_hostfw_handshake()
```

```
 uint64_t scratch_reg_addr;
```

```
 if(OCTEON_IS_MODEL(OCTEON_CN56XX))
```

```
 scratch_reg_addr = CVMX_PEXP_NPEI_SCRATCH_1;
```

```
 else
```

```
- scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
```

```
+ scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
```

```
/* 1: Send Indication to host about Host Firmware support. */
```

```
scratch_val = HOSTFW_HS_SUPPORT_INDICATION;
```

```
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);
```

```
@@ -347,11 +347,15 @@ cvmcs_ioq_send_to_pko3(cvmx_wqe_t *wqe, int dir, int port, int queue, int offl
```

```
 cvmcs_print("%d:%s", __LINE__, __FUNCTION__);
```

```
 return -1;
```

```
 }
```

```
hdr_s = desc.hdr_s;
```

```
+#if 0
```

```
hdr_s->s.df = 0;
```

```
+#else
```

```
+ hdr_s->s.df = 1; //for LINA
```

```
+#endif
```

```
hdr_s->s.ii = 1;
```

```
if(offload) {
```

```
 uint8_t ip0, l4_proto = 0, ipoff = offload
```

```
 - 1;
```

```
@@ -379,10 +383,19 @@ cvmcs_ioq_send_to_pko3(cvmx_wqe_t *wqe, int dir, int port, int queue, int offl
```

```
 else if (l4_proto == 132)
```

```
 hdr_s->s.ckl4 = /* SCTP */ 3;
```

```
 else
```

```
 hdr_s->s.ckl4 = /* Unknown */ 0;
```

```
 }
```

```
+#if 1 //for LINA
```

```

+ /* Free buffer when finished with the packet */
+ cvmx_pko_send_free_t *send_free;
+ send_free = (void *) &desc.word[desc.num_words++];
+
+ send_free->u64 = 0;
+ send_free->s.subdc4 = CVMX_PKO_SENDSUBDC_FREE;
+ send_free->s.addr = (uint64_t)CVM_DRV_GET_PHYS(wqe);
+ #endif

return cvmx_pko3_pdesc_transmit(&desc, queue, NULL);

}

@@ -478,11 +491,11 @@ cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
cvmx_buf_ptr_t *lptr;
cvmx_resp_hdr_t *resp_ptr;
cvmx_buf_ptr_pki_t *pki_lptr;
int port, queue;

-
+ #if 0 //for LINA: as WQE and data share the same buffer.
+ cvmx_wqe_set_bufs(wqe, cvmx_wqe_get_bufs(wqe) + 1);
+ cvmx_wqe_set_len(wqe,
+ (cvmx_wqe_get_len(wqe) + CVMX_RESP_HDR_SIZE));

/* We use the space in WQE starting at byte offset 40 for the response
header information. This allows the WQE to be used for data and let
@@ -490,15 +503,26 @@ cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
lptr = (cvmx_buf_ptr_t *)&wqe->packet_data[48];
lptr->u64 = wqe->packet_ptr.u64;

resp_ptr = (cvmx_resp_hdr_t *)&wqe->packet_data[56];
+ #else
+ cvmx_wqe_set_len(wqe, (cvmx_wqe_get_len(wqe) + CVMX_RESP_HDR_SIZE));
+ resp_ptr = (cvmx_resp_hdr_t *) cvmx_phys_to_ptr(cvmx_wqe_get_pki_pkt_ptr(wqe).addr - 8);
+ #endif
resp_ptr->u64 = 0;
resp_ptr->s.opcode = CORE_NW_DATA_OP;
resp_ptr->s.destqport = rxq;
resp_ptr->s.csum_verified = 1; /* checksum notification to Host */

+ #if 1 //for LINA
+ pki_lptr = (cvmx_buf_ptr_pki_t *)&wqe->packet_ptr;
+ pki_lptr->addr = CVM_DRV_GET_PHYS(resp_ptr);
+ pki_lptr->size += 8;
+
+

```

```

+else
if(OCTEON_IS_MODEL(OCTEON_CN78XX)
|| OCTEON_IS_MODEL(OCTEON_CN73XX)) {
/* lptr is reused */
pki_lptr = (cvmx_buf_ptr_pki_t *)&wqe->packet_ptr;
pki_lptr->u64 = 0;
pki_lptr->addr = CVM_DRV_GET_PHYS(resp_ptr);
@@ -510,11 +534,11 @@ cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
lptr->u64 = 0;
lptr->s.addr = CVM_DRV_GET_PHYS(resp_ptr);
lptr->s.size = 8;
lptr->s.pool = CVMX_FPA_WQE_POOL;
}
-
+endif
DBG("wqe @ %p wqe bufs: %d len: %d pkt_ptr @ %lx\n", wqe,
 cvmx_wqe_get_bufs(wqe), cvmx_wqe_get_len(wqe), (unsigned long)(cvmx_phys_to_ptr(wqe-
>packet_ptr.s.addr)));

port = cvm_pci_get_oq_pkoport(rxq);
queue = cvm_pci_get_oq_pkoqueue(rxq);
@@ -693,11 +717,11 @@ int cvmcs_ioq_init_pci_drv()
* Share the PKI config number with host. This number will be updated
* in the instruction header of every frame sent by host to OCTEON.
*/
void cvmcs_ioq_send_pkind_to_host(void)
{
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+
uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
uint64_t scratch_val = 0ULL;
int node = -1;

if(!OCTEON_IS_MODEL(OCTEON_CN78XX))
return;
@@ -816,11 +840,11 @@ void cvmcs_ioq_print_stats(void)
* Update the PKO ready state to host. This allows host to
* configure Input and Output queues for data transfer.
*/
void cvmcs_ioq_send_pko_ready(void)
{
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
uint64_t scratch_val = 0ULL;
uint32_t indication = 0;
int node = -1;

/* wait for ACK from HOST */

```

```

@@ -887,12 +911,15 @@ cvmcs_ioq_init_global()
cvmx_helper_pki_get_dflt_style(0, &style_cfg);

style_cfg.parm_cfg.first_skip = CVMX_HELPER_FIRST_MBUFF_SKIP;
style_cfg.parm_cfg.later_skip = CVMX_HELPER_NOT_FIRST_MBUFF_SKIP;
style_cfg.parm_cfg.dis_wq_dat = 1; /* use distinct buffers for WQE and packet data */
-
- cvmx_helper_pki_set_dflt_style(0, &style_cfg);
+ style_cfg.parm_cfg.fcs_strip
= 0;
+#ifdef OCTEON_LE
+ style_cfg.parm_cfg.pkt_lend = 1;
+#endif
+ cvmx_helper_pki_set_dflt_style(0, &style_cfg);

}
#endif
if (cvmx_helper_initialize_packet_io_global() == -1) {
 printf("# cvmcs: Failed to initialize input ports\n");
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/core-drv.mk b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/core-drv.mk
index 6c14b39..fdda72d 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/core-drv.mk
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/core-drv.mk
@@ -52,11 +52,11 @@ OBJ_${d} += $(OBJ_DIR)/cn56xx_ep_comm.o
endif

-$(OBJ_${d}): CFLAGS_LOCAL := -I$(d) -I$(d)/../common -O2 -g -Werror
+$(OBJ_${d}): CFLAGS_LOCAL := -I$(d) -I$(d)/../common -I$(OCTEON_ROOT)/executive -O2 -g -Werror

standard component Makefile rules

DEPS_${d} := $(OBJ_${d}).o=.d

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-driver-defs.h b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-driver-defs.h
index
6dc0d08..98dec30 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-driver-defs.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-driver-defs.h
@@ -46,24 +46,29 @@
#include "cvmx-bootmem.h"
#include "cvmx-spinlock.h"
#include "cvmx-version.h"
#include "cvmx-helper.h"

```

```

#include "cvmx-helper-util.h"
#include "cvm-common-wqe.h"
#include "cvm-common-defs.h"
#include "cvm-common-misc.h"
#include "cvm-common-fpa.h"
+//include "cvm-common-wqe.h"
+//include "cvm-common-defs.h"
+//include "cvm-common-misc.h"
+//include "cvm-common-fpa.h"
#include "cvm-drv-debug.h"

#ifndef __CAVIUM_LITTLE_ENDIAN
#define __CAVIUM_LITTLE_ENDIAN 1234
#endif

#ifndef __CAVIUM_BIG_ENDIAN
#define __CAVIUM_BIG_ENDIAN 4321
#endif

+#ifdef CVMX_FPA_WQE_POOL
+#undef CVMX_FPA_WQE_POOL
+#endif
+#define CVMX_FPA_WQE_POOL CVMX_FPA_PACKET_POOL
+
#define MAX_WQE_SIZE 128
/* OCTEON - III Supports Max of 48 cores */
#define MAX_DRV_CORES 48

/* In order to use PCIe port #1, set this macro to 1 */
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-drv.c
index 58e6998..2878be4 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
@@ -66,21 +66,21 @@ cvm_drv_wait(int time_in_us)

void
cvm_drv_local_init()
{
+#if 0 //no used in LINA
cvmx_scratch_write64(CVM_DRV_SCR_PACKET_BUF_PTR, CVM_COMMON_ALLOC_INIT);
CVMX_SYNCW;
cvmx_fpa_async_alloc(CVM_DRV_SCR_PACKET_BUF_PTR, CVMX_FPA_PACKET_POOL);

cvmx_scratch_write64(CVM_DRV_SCR_WQE_BUF_PTR, CVM_COMMON_ALLOC_INIT);
CVMX_SYNCW;
cvmx_fpa_async_alloc(CVM_DRV_SCR_WQE_BUF_PTR, CVMX_FPA_WQE_POOL);

```

```

 cvm_drv_core_id = cvmx_get_core_num();
-
-
+ #endif
}

void
setup_pci_input_ports(void)
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
index
12ec09c..dc25848 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
@@ -219,10 +219,14 @@ __cvm_post_pko3_command(cvmx_buf_ptr_t lptr,

hdr_s->u64 = 0;
 hdr_s->s.df = 0; // H/W will free the buffers by default
 hdr_s->s.ii = 0;

+ #ifdef OCTEON_LE
+ hdr_s->s.le = 1;
+ #endif
+
if(pko_ptr_type == PKO_GATHER_DATA) {
 /* Process legacy gather list */
 uint32_t i;
 cvmx_buf_ptr_t *pPtr;
 cvmx_buf_ptr_t blk;
@@ -464,10 +468,11 @@ cvm_pko_send_data(cvmx_resp_hdr_t *user_resp_hdr,
 cvm_ptr_type_t ptr_type,
 uint32_t total_bytes,
 uint32_t segs,
 uint32_t port)
{
+ #if 0 //not used in LINA, commented out to avoid dependence on cvm-drv-resources.h
 cvmx_buf_ptr_t lptr;
 cvmx_resp_hdr_t *resp_hdr;
 uint16_t
 gcount=0;
 cvmx_buf_ptr_t *glist,*send_buf;

@@ -539,10 +544,11 @@ cvm_pko_send_data(cvmx_resp_hdr_t *user_resp_hdr,
 lptr.s.pool = CVM_PCI_PKO_FPA_POOL;
 lptr.s.i = 1; /* PKO SHOULD FREE THIS BUFFER */
 CVMX_SYNCWS;

```



```

 cvm_pko_send_direct(lptr, PKO_GATHER_DATA, gcount, total_bytes, port);
}
#endif
return 0;
}

```

```

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.h b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-pko.h

```

```

index 8914fbc..67bf122 100644

```

```

--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.h
@@ -41,13 +41,14 @@

```

```

#define CVM_USE_HW_CKSUM_OFFLOAD 1

```

```

#define CVM_PCI_PKO_FPA_POOL CVMX_FPA_PACKET_POOL

```

```

#define CVM_PCI_PKO_SCRATCHPAD CVM_DRV_SCR_PACKET_BUF_PTR

```

```

-

```

```

+#if 0 //not used in LINA, commented out to avoid dependence on cvm-drv-resources.h

```

```

#define CVM_PCI_PKO_ALLOC_BUFFER(ptr) \

```

```

 ptr = cvm_drv_fpa_alloc_async(CVM_PCI_PKO_SCRATCHPAD, CVM_PCI_PKO_FPA_POOL)

```

```

+#endif

```

```

#define CVM_PCI_PKO_FREE_BUFFER(ptr) \

```

```

 cvm_drv_fpa_free(ptr, CVM_PCI_PKO_FPA_POOL, 0)

```

```

#define CVM_PCI_PKO_SET_FPA_POOL(lptr) \

```

```

diff --git a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c b/CNNIC-SDK-
SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c

```

```

index af1299a..4a17d2d 100644

```

```

--- a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c

```

```

+++ b/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c

```

```

@@ -310,11 +310,11 @@ Block *cniq_get_quick(void *rxq, octeon_rx_info_t *rx_info)

```

```

 return NULL;

```

```

}

```

```

}

```

```

info = &(droq->info_list[droq->host_read_index]);

```

```

- octeon_swap_8B_data((uint64_t *)info, 2);

```

```

+ octeon_swap_info((uint64_t *)info, 2);

```

```

if(!info->length)

```

```

{

```

```

 cavium_error("OCTEON:DROQ[%d] idx: %d len:0\n", droq->q_no, droq->host_read_index);

```

```

 return NULL;

```

```

}

```

Found in

path(s):

\* /opt/cola/permits/1588488807\_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-rsync-from-istanbul.patch

No license file was found, but licenses were detected in source scan.

# Copyright (c) 2016 by Cisco Systems, Inc.

# All rights reserved.

OCTEON-SDK/host/coredump-util/Makefile | 9 +++++-----

OCTEON-SDK/host/debug-agent/Makefile | 9 +++++-----

OCTEON-SDK/host/remote-lib/Makefile | 7 +++++---

OCTEON-SDK/host/remote-utils/Makefile | 9 +++++-----

4 files changed, 19 insertions(+), 15 deletions(-)

diff --git a/OCTEON-SDK/host/coredump-util/Makefile b/OCTEON-SDK/host/coredump-util/Makefile

index f386adc..e965900 100644

--- a/OCTEON-SDK/host/coredump-util/Makefile

+++ b/OCTEON-SDK/host/coredump-util/Makefile

@@ -22,14 +22,15 @@ CFLAGS+= -static

LIBS\_FLAGS += -lc -lnss\_files -lnss\_dns -lresolv

LIBS\_CXX += -Wl,-lstdc++,-lc,-lnss\_files,-lnss\_dns,-lresolv

endif

endif

-CROSS=

-CC=\$(CROSS)gcc

-CXX=\$(CROSS)g++

-STRIP=\$(CROSS)strip

+#CISCO\_MODIFIED - changed from = to ?=

+CROSS?=

+CC?=\$(CROSS)gcc

+CXX?=\$(CROSS)g++

+STRIP?=\$(CROSS)strip

BINS=\$(DIR)/oct-remote-coredump

all: \$(BINS) \$(LIBS)

- \$(STRIP) \$(BINS)

diff --git a/OCTEON-SDK/host/debug-agent/Makefile b/OCTEON-SDK/host/debug-agent/Makefile

index 63dad9a..c1d157e 100644

--- a/OCTEON-SDK/host/debug-agent/Makefile

+++ b/OCTEON-SDK/host/debug-agent/Makefile

@@ -19,14 +19,15 @@ CFLAGS+= -static

LIBS\_FLAGS += -lc -lnss\_files -lnss\_dns -lresolv

LIBS\_CXX += -Wl,-lstdc++,-lc,-lnss\_files,-lnss\_dns,-lresolv

endif

endif

```
-CROSS=
-CC=$(CROSS)gcc
-CXX=$(CROSS)g++
-STRIP=$(CROSS)strip
+#CISCO_MODIFIED - changed from = to ?=
+CROSS?=
+CC?=$(CROSS)gcc
+CXX?=$(CROSS)g++
+STRIP?=$(CROSS)strip
```

```
BINS=$(DIR)/oct-debug-agent
```

```
all: $(BINS) $(LIBS)
- $(STRIP) $(BINS)
diff --git a/OCTEON-SDK/host/remote-lib/Makefile b/OCTEON-SDK/host/remote-lib/Makefile
index 25ca60c..3a83c67 100644
--- a/OCTEON-SDK/host/remote-lib/Makefile
+++ b/OCTEON-SDK/host/remote-lib/Makefile
@@ -5,13 +5,14 @@ BOOTLOADER_DEFINES =
CFLAGS = -g -O3 -W -Wall -D_GNU_SOURCE -DUSE_RUNTIME_MODEL_CHECKS=1 -
DCVMX_BUILD_FOR_LINUX_HOST=1 \
-I
$(OCTEON_ROOT)/target/include \
$(BOOTLOADER_HEADERS) $(EXTRA_CFLAGS) $(BOOTLOADER_DEFINES)
```

```
DIR=
-CROSS=
-CC=$(CROSS)gcc
-AR=$(CROSS)ar
+#CISCO_MODIFIED - changed from = to ?=
+CROSS?=
+CC?=$(CROSS)gcc
+AR?=$(CROSS)ar
```

```
.PHONY: all
all: $(DIR)/libocteon-remote.a
```

```
OBJ = $(DIR)/octeon-remote.o \
diff --git a/OCTEON-SDK/host/remote-utils/Makefile b/OCTEON-SDK/host/remote-utils/Makefile
index 11e4742..b41da23 100644
--- a/OCTEON-SDK/host/remote-utils/Makefile
+++ b/OCTEON-SDK/host/remote-utils/Makefile
@@ -28,14 +28,15 @@ CFLAGS+= -static
LIBS_FLAGS += -lc -lnss_files -lnss_dns -lresolv
LIBS_CXX += -Wl,-lstdc++,-lc,-lnss_files,-lnss_dns,-lresolv
endif
endif
```

```
-CROSS=
-CC=$(CROSS)gcc
-CXX=$(CROSS)g++
-STRIP=$(CROSS)strip
+#CISCO_MODIFIED - changed from = to ?=
+CROSS?=
+CC?=$(CROSS)gcc
+CXX?=$(CROSS)g++
+STRIP?=$(CROSS)strip
```

```
BINS = \
$(DIR)/oct-remote-boot \
$(DIR)/oct-remote-bootcmd \
$(DIR)/oct-remote-console \

```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
OCTEON-SDK-host-CROSS-CC-AR-STRIP-variables.patch
```

No license file was found, but licenses were detected in source scan.

```
Copyright (c) 2016 by Cisco Systems, Inc.
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```

```
OCTEON-SDK/bootloader/u-boot/tools/Makefile | 2 +-
1 files changed, 1 insertions(+), 1 deletions(-)
```

```
diff --git a/OCTEON-SDK/bootloader/u-boot/tools/Makefile b/OCTEON-SDK/bootloader/u-boot/tools/Makefile
index e0ab6ca..e5dc669 100644
```

```
--- a/OCTEON-SDK/bootloader/u-boot/tools/Makefile
+++ b/OCTEON-SDK/bootloader/u-boot/tools/Makefile
@@ -249,11 +249,11 @@ $(obj)kwboot$(SFX): $(obj)kwboot.o
```

```
$(obj)update_octeon_header$(SFX): update_octeon_header.c $(obj)crc32.o
$(HOSTCC) $(HOSTCFLAGS_NOPED) -D_GNU_SOURCE $(HOSTLDFLAGS) -o $@ $^
```

```
$(obj)dtc$(SFX):
- CC=$(HOSTCC) $(MAKE) -C dtcsrc
+ CC=$(HOSTCC) LDFLAGS=$(HOSTLDFLAGS) $(MAKE) -C dtcsrc
mv dtcsrc/dtc$(SFX) $@
```

```
Some of the tool objects need to be accessed from outside the tools directory
```

```
$(obj)%.: $(SRCTREE)/common/%.c
$(HOSTCC) -g $(HOSTCFLAGS_NOPED) -c -o $@ $<
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
OCTEON-SDK-bootloader-u-boot-tools-Makefile.patch
```

No license file was found, but licenses were detected in source scan.

```
Copyright (c) 2016 by Cavium, Inc.
```

```
All rights reserved.
```

```
diff -urN OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c OCTEON-SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c
```

```
--- OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-04-11
```

```
17:26:49.408984670 -0700
```

```
+++ OCTEON-SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-04-11 17:25:32.188857052 -0700
```

```
@@ -153,6 +153,7 @@
```

```
#include "configs/octeon_sff7800_shared.h"
```

```
#include "configs/octeon_niagara83x_shared.h"
```

```
#include "configs/octeon_rainier_shared.h"
```

```
+#include "configs/octeon_kilburn_shared.h"
```

```
#ifdef __U_BOOT__
```

```
include <asm/arch/octeon_eeprom_types.h>
```

```
#else
```

```
@@ -11790,6 +11791,11 @@
```

```
 { OCTEON_CLARK_SPD_VALUES };
```

```
#endif
```

```
+#if !defined(CVMX_BUILD_FOR_UBOOT) || defined(CONFIG_OCTEON_KILBURN)
```

```
+static uint8_t __attribute__((unused)) octeon_kilburn_cfg0_spd_values[256] =
```

```
+ {
```

```
 OCTEON_KILBURN_CFG0_SPD_VALUES };
```

```
+#endif
```

```
+
```

```
#if !defined(CVMX_BUILD_FOR_UBOOT) || \
```

```
 defined(CONFIG_OCTEON_NAS7000_REF_EMMC_STAGE2) || \
```

```
 (defined(CONFIG_OCTEON_NAS7000_REF) && defined(unix))
```

```
@@ -12215,6 +12221,18 @@
```

```
}
```

```
},
```

```
endif
```

```
+# if !defined(CVMX_BUILD_FOR_UBOOT) || \
```

```
+ defined(CONFIG_OCTEON_KILBURN)
```

```
+ {
```

```
+ .board_type = CVMX_BOARD_TYPE_CUST_KILBURN,
```

```
+ .eeprom_addr = OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR,
```

```
+ .chip_dds_config = {
```

```
+ {
```

```
+ .dds_config = {
```

```
OCTEON_KILBURN_DDR_CONFIGURATION(OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION)
```

```
}
```

```
+ }
```

```
+ }
```

```

+ },
+# endif
if defined(CONFIG_OCTEON_RAINIER) || !defined(CVMX_BUILD_FOR_UBOOT)
{
.board_type = CVMX_BOARD_TYPE_RAINIER,
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/config.mk
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/config.mk 2016-04-11
17:24:08.439857563 -0700
@@ -0,0 +1,35 @@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+# (C) Copyright 2011
+# Cavium Inc., Inc. support@cavium.com
+#
+# See file CREDITS for list of people who contributed to this
+# project.
+#
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+# the License, or (at your option) any later version.
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+# MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
+# GNU General Public License for more details.
+#
+# You should have received a copy of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307
+ USA
+#
+
+# Oocteon CN63XX
+#
+
+CONFIG_SYS_TEXT_BASE = 0xC0000000
+PLATFORM_CPPFLAGS += -mtune=octeon2 -finline -ffunction-sections \
+ -fdata-sections -ffree-pre -fno-asynchronous-unwind-tables \
+ -DCVMX_ENABLE_PARAMETER_CHECKING=0 \
+ -DCVMX_ENABLE_DEBUG_PRINTS=1
+#PLATFORM_CPPFLAGS += -DCVMX_ENABLE_CSR_ADDRESS_CHECKING=1
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c OCTEON-

```

```

SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 1969-12-31 16:00:00.000000000 -
0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 2016-04-11 17:24:08.439857563 -
0700
@@ -0,0 +1,576 @@
+/*
+ * (C) Copyright 2015 Cavium Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+#include <common.h>
+#include <asm/mipsregs.h>
+#include <asm/arch/octeon_boot.h>
+#include <asm/arch/cvmx-sim-magic.h>
+#include <asm/arch/cvmx-qlm.h>
+#include <asm/arch/octeon_fdt.h>
+#include
<fdt_support.h>
+#include <asm/arch/octeon_board_common.h>
+#include <asm/arch/lib_octeon.h>
+#include <asm/arch/lib_octeon_shared.h>
+#include <asm/arch/octeon_qlm.h>
+#include <asm/arch/cvmx-helper-util.h>
+#include <asm/arch/cvmx-helper-cfg.h>
+#include <asm/arch/cvmx-smi-defs.h>
+#include <asm/gpio.h>
+
+#include <command.h>
+#include <spi_flash.h>
+#include <spi.h>
+#define MAX_MIX_ENV_VARS 4
+
+extern void print_isp_volt(const char **labels, uint16_t isp_dev_addr, uint8_t adc_chan);
+
+extern int read_ispPAC_mvols(uint16_t isp_dev_addr, uint8_t adc_chan);
+
+extern int read_ispPAC_mvols_avg(int loops, uint16_t isp_dev_addr, uint8_t adc_chan);
+
+DECLARE_GLOBAL_DATA_PTR;
+
+/*
+ * parse_env_var: Parse the environment variable ("bgx_for_mix%d") to
+ * extract the lmac it is set to.
+ *
+ * index: Index of environment variable to parse.
+ * environment variable.

```

```

+ * env_bgx: Updated with the bgx of the lmac in the environment
+ * variable.
+ * env_lmac: Updated
with the index of lmac in the environment
+ * variable.
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static int parse_env_var(int index, int *env_bgx, int *env_lmac)
+{
+ char env_var[20];
+ ulong xipd_port;
+
+ sprintf(env_var, "bgx_for_mix%d", index);
+ if ((xipd_port = getenv_ulong(env_var, 0, 0xffff)) != 0xffff) {
+ int xiface;
+ struct cvmx_xiface xi;
+ struct cvmx_xport xp;
+
+ /*
+ * The environemt variable is set to the xipd port. Convert the
+ * xipd port to numa node, bgx, and lmac.
+ */
+ xiface = cvmx_helper_get_interface_num(xipd_port);
+ xi = cvmx_helper_xiface_to_node_interface(xiface);
+ xp = cvmx_helper_ipd_port_to_xport(xipd_port);
+ *env_bgx = xi.interface;
+ *env_lmac = cvmx_helper_get_interface_index_num(xp.port);
+ return 0;
+ }
+
+ return -1;
+}
+
+/*
+ * get_lmac_fdt_node: Search the device tree for the node corresponding to
+ * a given bgx lmac.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Numa node of
the lmac to search for.
+ * search_bgx: Bgx of the lmac to search for.
+ * search_lmac: Lmac index to search for.
+ * compat: Compatible string to search for.
+
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */

```



```

+static int get_lmac_fdt_node(const void *fdt, int search_node, int search_bgx,
+ int search_lmac, const char *compat)
+{
+ int node;
+ const fdt32_t *reg;
+ u64 addr;
+ int fdt_node = -1;
+ int fdt_bgx = -1;
+ int fdt_lmac = -1;
+ int len;
+ int parent;
+
+ /* Iterate through all bgx ports */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ compat)) >= 0) {
+ /* Get the node and bgx from the physical address */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ !(reg = fdt_getprop(fdt, parent, "reg", &len)))
+ continue;
+
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ fdt_node = (addr >> 36) & 0x7;
+ fdt_bgx = (addr >> 24) & 0xf;
+
+ /* Get the lmac
+ index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len)))
+ fdt_lmac = *reg;
+
+ /* Check for a match */
+ if (search_node == fdt_node && search_bgx == fdt_bgx &&
+ search_lmac == fdt_lmac)
+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * get_mix_fdt_node: Search the device tree for the node corresponding to
+ * a given mix.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Mix numa node to search for.
+ * search_index: Mix index to search for.
+ *
+ * returns: The device tree node of the lmac if found,

```

```

+ * or -1 otherwise.
+ */
+static int get_mix_fdt_node(const void *fdt, int search_node, int search_index)
+{
+ int node;
+
+ /* Iterate through all the mix fdt nodes */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ "cavium,octeon-7890-mix")) >= 0) {
+ int parent;
+ int len;
+ const char *name;
+ int mix_numa_node;
+ const fdt32_t *reg;
+ int mix_index = -1;
+ u64 addr;
+
+ /* Get the
+ numa node of the mix from the parent node name */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ ((name = fdt_get_name(fdt, parent, &len)) == NULL) ||
+ ((name = strchr(name, '@')) == NULL))
+ continue;
+
+ name++;
+ mix_numa_node = simple_strtol(name, NULL, 0) ? 1 : 0;
+
+ /* Get the mix index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len))) {
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ mix_index = (addr >> 11) & 1;
+ }
+
+ /* Check for a match */
+ if (mix_numa_node == search_node && mix_index == search_index)
+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * fdt_fix_mix: Fix the mix nodes in the device tree. Only the mix nodes
+ * configured by the user will be preserved. All other mix
+ * nodes will be trimmed.
+ *
+ * fdt: Pointer to flat device tree

```

```

+ *
+ * returns: Zero on success, error otherwise.
+ */
+static int fdt_fix_mix(const void *fdt)
+{
+ int node;
+ int next_node;
+ int len;
+ int i;
+
+ /*
+ Parse all the mix port environment variables */
+ for (i = 0; i < MAX_MIX_ENV_VARS; i++) {
+ int env_node = 0;
+ int env_bgx = -1;
+ int env_lmac = -1;
+ int lmac_fdt_node = -1;
+ int mix_fdt_node = -1;
+ int lmac_phandle;
+ char *compat;
+
+ /* Get the lmac for this environment variable */
+ if (parse_env_var(i, &env_bgx, &env_lmac))
+ continue;
+
+ /* Get the fdt node for this lmac and add a phandle to it */
+ compat = "cavium,octeon-7890-bgx-port";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node, env_bgx,
+ env_lmac, compat)) < 0) {
+ /* Must check for the xcv compatible string too */
+ compat = "cavium,octeon-7360-xcv";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node,
+ env_bgx,
+ env_lmac,
+ compat)) < 0) {
+ printf("WARNING: Failed to get lmac fdt node "
+ "for %d%d%d\n", env_node, env_bgx,
+ env_lmac);
+ continue;
+ }
+ }
+
+ lmac_phandle = fdt_alloc_phandle((void
+ *)fdt);
+ fdt_set_phandle((void *)fdt, lmac_fdt_node, lmac_phandle);
+
+ /* Get the fdt mix node corresponding to this lmac */
+ if ((mix_fdt_node = get_mix_fdt_node(fdt, env_node,

```

```

+ env_lmac)) < 0)
+ continue;
+
+ /* Point the mix to the lmac */
+ fdt_getprop(fdt, mix_fdt_node, "cavium,mac-handle", &len);
+ fdt_setprop_inplace((void *)fdt, mix_fdt_node,
+ "cavium,mac-handle", &lmac_phandle, len);
+ }
+
+ /* Trim unused mix'es from the device tree */
+ for (node = fdt_next_node(fdt, -1, NULL); node >= 0;
+ node = next_node) {
+ const char *compat;
+ const fdt32_t *reg;
+
+ next_node = fdt_next_node(fdt, node, NULL);
+
+ if ((compat = fdt_getprop(fdt, node, "compatible", &len))) {
+ if (strcmp(compat, "cavium,octeon-7890-mix"))
+ continue;
+
+ if ((reg = fdt_getprop(fdt, node,
+ "cavium,mac-handle", &len))) {
+ if (*reg == 0xffff)
+ fdt_nop_node((void *)fdt, node);
+ }
+ }
+ }
+
+ return 0;
+
+int no_phy[8] = {0, 0,
+ 0, 0, 0, 0, 0, 0};
+
+static void kill_fdt_phy(void *fdt, int offset, void *arg)
+{
+ int len, phy_offset;
+ const fdt32_t *php;
+ uint32_t phandle;
+
+ php = fdt_getprop(fdt, offset, "phy-handle", &len);
+ if (php && len == sizeof(*php)) {
+ phandle = fdt32_to_cpu(*php);
+ fdt_nop_property(fdt, offset, "phy-handle");
+ phy_offset = fdt_node_offset_by_phandle(fdt, phandle);
+ if (phy_offset > 0)
+ fdt_nop_node(fdt, phy_offset);
+ }
+ }

```

```

+ }
+ }
+
+void __fixup_xcv(void)
+{
+ unsigned long bgx = getenv_ulong("bgx_for_rgmii", 10, (unsigned long)-1);
+ char fdt_key[16];
+ int i;
+
+ debug("%s: BGX %d\n", __func__, (int)bgx);
+
+ for (i = 0; i < 3; i++) {
+ snprintf(fdt_key, sizeof(fdt_key),
+ bgx == i ? "%d,xcv" : "%d,not-xcv", i);
+ debug("%s: trimming bgx %lu with key %s\n",
+ __func__, bgx, fdt_key);
+
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key,
+ "cavium,xcv-trim", true, NULL, NULL);
+ }
+ }
+
+/* QLM0 - QLM6 */
+void __fixup_fdt(void)
+{
+ int qlm;
+ int speed
+ = 0;
+
+ for (qlm = 0; qlm < 7; qlm++) {
+ enum cvmx_qlm_mode mode;
+ char fdt_key[16];
+ const char *type_str = "none";
+ mode = cvmx_qlm_get_mode(qlm);
+ switch (mode) {
+ case CVMX_QLM_MODE_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII_1X1:
+ type_str = "sgmii";
+ break;
+ case CVMX_QLM_MODE_XAUI:
+ case CVMX_QLM_MODE_RGMII_XAUI:
+ speed = (cvmx_qlm_get_gbaud_mhz(qlm) * 8 / 10) * 4;
+ if (speed == 10000)
+ type_str = "xaui";
+ else
+ type_str = "dxaui";
+ break;
+ }
+ }

```

```

+ case CVMX_QLM_MODE_RXAUI:
+ case CVMX_QLM_MODE_RGMII_RXAUI:
+ type_str = "rxauai";
+ break;
+ case CVMX_QLM_MODE_XLAUI:
+ case CVMX_QLM_MODE_RGMII_XLAUI:
+ type_str = "xlaui";
+ break;
+ case CVMX_QLM_MODE_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI_1X1:
+ type_str = "xfi";
+ break;
+ case CVMX_QLM_MODE_10G_KR:
+ case CVMX_QLM_MODE_RGMII_10G_KR:
+ type_str = "10G_KR";
+ break;
+ case CVMX_QLM_MODE_40G_KR4:
+ case CVMX_QLM_MODE_RGMII_40G_KR4:
+ type_str
= "40G_KR4";
+ break;
+ case CVMX_QLM_MODE_SATA_2X1:
+ type_str = "sata";
+ break;
+ case CVMX_QLM_MODE_SGMII_2X1:
+ case CVMX_QLM_MODE_XFI_1X2:
+ case CVMX_QLM_MODE_10G_KR_1X2:
+ case CVMX_QLM_MODE_RXAUI_1X2:
+ case CVMX_QLM_MODE_MIXED: // special for DLM5 & DLM6
+ {
+ cvmx_bgxx_cmr_config_t cmr_config;
+ cvmx_bgxx_spux_br_pmd_control_t pmd_control;
+ int mux = cvmx_qlm_mux_interface(2);
+ if (mux == 2) { // only dlm6
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CM_RX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2, 2));
+ } else {
+ if (qlm == 5) {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CM_RX_CONFIG(0, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(0, 2));
+ } else {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CM_RX_CONFIG(2, 2));

```

```

+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2,
+ 2));
+ }
+ }
+ switch (cmr_config.s.lmac_type) {
+ case 0:
+ type_str = "sgmii";
+ break;
+ case 1:
+ type_str = "xaui";
+ break;
+ case 2:
+ type_str = "rxau";
+ break;
+ case 3:
+ if (pmd_control.s.train_en)
+ type_str = "10G_KR";
+ else
+ type_str = "xfi";
+ break;
+ case 4:
+ if (pmd_control.s.train_en)
+ type_str = "40G_KR4";
+ else
+ type_str = "xlau";
+ break;
+ default:
+ type_str = "none";
+ break;
+ }
+ break;
+ }
+ default:
+ type_str = "none";
+ break;
+ }
+ sprintf(fdt_key, "%d,%s", qlm, type_str);
+ debug("Patching qlm %d for %s for mode %d%s\n",
+ qlm, fdt_key, mode, no_phy[qlm] ? ", removing PHY" : "");
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key, NULL,
+ true, no_phy[qlm] ? kill_fdt_phy : NULL, NULL);
+ }
+ }
+
+int board_fixup_fdt(void)
+{
+ __fixup_fdt();

```

```

+ __fixup_xcv();
+
+ /* Fix the mix ports */
+ fdt_fix_mix(gd->fdt_blob);
+
+ return 0;
+}
+
+/*
+ Raise an integer to a power */
+static uint64_t ipow(uint64_t base, uint64_t exp)
+{
+ uint64_t result = 1;
+ while (exp) {
+ if (exp & 1)
+ result *= base;
+ exp >>= 1;
+ base *= base;
+ }
+ return result;
+}
+
+/*
+ * This function now supports only single (node0) configuration.
+ * Some of the called funcs (read_ispPAC_mvols_avg(), octeon_led_str_write())
+ * are hard-coded for node0. Also we set gd->arch.mcu_rev_maj, ...
+ * It can be extended to support multinode if needed/important
+ */
+static int checkboardinfo(void)
+{
+ int core_mVolts, dram_mVolts0, dram_mVolts1;
+ char mcu_ip_msg[64] = { 0 };
+ char tmp[10];
+ int characters, idx = 0, value = 0;
+
+ debug("In %s\n", __func__);
+ if (octeon_show_info()) {
+
+ int mcu_rev_maj = 0;
+ int mcu_rev_min = 0;
+
+ if (twsii_mcu_read(0x00) == 0xa5
+ && twsii_mcu_read(0x01) == 0x5a) {
+ gd->arch.mcu_rev_maj = mcu_rev_maj = twsii_mcu_read(2);
+ gd->arch.mcu_rev_min = mcu_rev_min = twsii_mcu_read(3);
+ }
+ else {
+ return -1; /* Abort if we can't access the MCU */

```



```

+ }
+
+ core_mVolts = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 8);
+ dram_mVolts0 = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 7); /* DDR0 */
+ dram_mVolts1 = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 6); /* DDR1 */
+
+ if (twsii_mcu_read(0x14) == 1)
+ sprintf(mcu_ip_msg, "MCU IPaddr: %d.%d.%d.%d, ",
+ twsii_mcu_read(0x10),
+ twsii_mcu_read(0x11),
+ twsii_mcu_read(0x12),
+ twsii_mcu_read(0x13));
+ printf("MCU rev: %d.%02d, %sCPU voltage: %d.%03d DDR{0,1} voltages: %d.%03d,%d.%03d\n",
+ gd->arch.mcu_rev_maj, gd->arch.mcu_rev_min, mcu_ip_msg,
+ core_mVolts / 1000, core_mVolts % 1000,
+ dram_mVolts0 / 1000, dram_mVolts0 % 1000,
+ dram_mVolts1 / 1000, dram_mVolts1 % 1000);
+
+ #define LED_CHARACTERS 8
+ value = core_mVolts;
+ idx = sprintf(tmp, "%lu ", gd->cpu_clk/(1000*1000));
+ characters = LED_CHARACTERS - idx;
+
+ if
+ (value / 1000) {
+ idx += sprintf(tmp + idx, "%d", value / 1000);
+ characters = LED_CHARACTERS - idx;
+ }
+
+ characters -= 1; /* Account for decimal point */
+
+ value %= 1000;
+ value = DIV_ROUND_UP(value, ipow(10, max(3 - characters, 0)));
+
+ idx += sprintf(tmp + idx, ".%0*d", min(3, characters), value);
+
+ /* Display CPU speed and voltage on display */
+ octeon_led_str_write(tmp);
+ } else {
+ octeon_led_str_write("Boot ");
+ }
+
+ return 0;
+ }
+
+ /**
+ * Here is the description of the parameters that are passed to QLM configuration
+ * param0 : The QLM to configure

```

```

+ * param1 : Speed to configure the QLM at
+ * param2 : Mode the QLM to configure
+ * param3 : 1 = RC, 0 = EP
+ * param4 : 0 = GEN1, 1 = GEN2, 2 = GEN3
+ * param5 : ref clock select, 0 = 100Mhz, 1 = 125MHz, 2 = 156MHz
+ * param6 : ref clock input to use:
+ * 0 - external reference (QLMx_REF_CLK)
+ * 1 = common clock 0 (QLMC_REF_CLK0)
+ * 2 = common_clock 1 (QLMC_REF_CLK1)
+ */
+void board_configure_qlms(void)
+{
+ int
+ qlm;
+ char env_var[16];
+ int node = 0;
+ int pcie_gen;
+ const char *mode_str;
+ int rc;
+ uint8_t qlm45_gpio;
+ bool qlm4_present, qlm5_present;
+
+ octeon_init_qlm(0);
+
+ mdelay(100);
+
+ cvmx_coremask_for_each_node(node, gd->arch.node_mask) {
+ debug("Configuring node %d, QLM 0 and 1 \n", node);
+ snprintf(env_var, sizeof(env_var), "pcie0:%d_gen", node);
+ pcie_gen = getenv_ulong(env_var, 10, 3) - 1;
+ octeon_configure_qlm(0, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+ octeon_configure_qlm(1, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+
+ debug("Configuring node %d, QLMs 2 for XLAUI\n", node);
+ octeon_configure_qlm(2, 103125, CVMX_QLM_MODE_XLAUI, 0, 0, 2, 2); /* 40G_KR4 */
+ octeon_qlm_tune_v3(0, 2, 103125, 0xd, 0xd0);
+
+ /* For other skews of kilburn, uncomment them */
+
+ printf("Configuring node %d, QLMs 3 for XAUI\n", node);
+ octeon_configure_qlm(3,
+ 103125, CVMX_QLM_MODE_XFI, 0, 0, 2, 2); /* 10G_KR */
+ octeon_qlm_tune_v3(0, 3, 103125, 0xd, 0xd0);
+
+ }
+ }
+
+

```

```

+int checkboard(void)
+{
+ checkboardinfo();
+
+ return 0;
+}
+
+int early_board_init(void)
+{
+ octeon_board_get_clock_info(KILBURN_DEF_DRAM_FREQ);
+ octeon_board_get_descriptor(CVMX_BOARD_TYPE_CUST_KILBURN, 1, 0);
+
+ /* Even though the CPU ref freq is stored in the clock descriptor, we
+ * don't read it here. The MCU reads it and configures the clock, and
+ * we read how the clock is actually configured.
+ * The bootloader does not need to read the clock descriptor tuple for
+ * normal operation on rev 2 and later boards.
+ */
+ octeon_board_get_mac_addr();
+
+ return 0;
+}
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/kilburn.dts
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 2016-04-11
17:24:08.439857563 -0700
@@ -0,0 +1,181 @@
+/dts-v1/;
+/*
+ * Cavium Inc. Kilburn 76XX
+ */
+ {
+ model = "cavium,kilburn";
+ compatible = "cavium,kilburn";
+ #address-cells = <2>;
+ #size-cells = <2>;
+
+ soc@0 {
+ interrupt-parent = <&ciu3>;
+ compatible = "simple-bus";
+ #address-cells = <2>;
+ #size-cells = <2>;
+ ranges; /* Direct mapping */
+ cavium,node-trim = "0,soc";
+
+ ciu3: interrupt-controller@101000000000 {
+ compatible = "cavium,octeon-7890-ciu3";

```

```

+ interrupt-controller;
+ /* Interrupts are specified by two parts:
+ * 1) Source number (20 significant bits)
+ * 2) Trigger type: (4 == level, 1 == edge)
+ */
+ #address-cells = <0>;
+ #interrupt-cells = <2>;
+ reg = <0x10100 0x00000000 0x0 0xb0000000>;
+ };
+
+ bootbus: bootbus@118000000000 {
+ compatible = "cavium,octeon-3860-bootbus";
+ reg = <0x11800 0x00000000 0x0 0x200>;
+ /* The chip select number and offset */
+ #address-cells = <2>;
+ /* The size of the
+ chip select region */
+ #size-cells = <1>;
+ ranges = <0 0 0 0x1ec00000 0x1400000>,
+ <1 0 0x10000 0x10000000 0>,
+ <2 0 0x10000 0x20000000 0>,
+ <3 0 0x10000 0x30000000 0>,
+ <4 0 0x10000 0x40000000 0>,
+ <5 0 0x10000 0x50000000 0>,
+ <6 0 0x10000 0x60000000 0>,
+ <7 0 0x10000 0x90000000 0>;
+
+ cavium,cs-config@0 {
+ compatible = "cavium,octeon-3860-bootbus-config";
+ cavium,cs-index = <0>;
+ cavium,t-adr = <10>;
+ cavium,t-ce = <100>;
+ cavium,t-oe = <30>;
+ cavium,t-we = <25>;
+ cavium,t-rd-hld = <0>;
+ cavium,t-wr-hld = <20>;
+ cavium,t-pause = <10>;
+ cavium,t-wait = <0>;
+ cavium,t-page = <20>;
+ cavium,t-rd-dly = <0>;
+
+ cavium,page-mode = <1>;
+ cavium,pages = <8>;
+ cavium,bus-width = <16>;
+ cavium,ale-mode = <1>;
+ };
+
+ flash0: nor@0,0 {

```

```

+ compatible = "cfi-flash";
+ reg = <0 0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <1>;
+
+ partition@0
+ {
+ label = "bootloader";
+ reg = <0 0x340000>;
+ read-only;
+ };
+ partition@300000 {
+ label = "storage";
+ reg = <0x340000 0xcbe000>;
+ };
+ partition@fe0000 {
+ label = "environment";
+ reg = <0xfe0000 0x20000>;
+ read-only;
+ };
+ };
+ };
+
+ serial@1180000000800 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg = <0x11800 0x00000800 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08000 4>;
+ };
+
+ serial@1180000000c00 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg = <0x11800 0x00000c00 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08040 4>;
+ };
+
+ gpio0: gpio-controller@1070000000800 {
+ #gpio-cells = <2>;
+ compatible = "cavium,octeon-7890-gpio";
+ reg = <0x10700 0x00000800 0x0 0x100>;
+ gpio-controller;
+ /* Interrupts are specified by two

```

```

parts:
+ * 1) GPIO pin number (0..15)
+ * 2) Triggering (1 - edge rising
+ * 2 - edge falling
+ * 4 - level active high
+ * 8 - level active low)
+ */
+ interrupt-controller;
+ #interrupt-cells = <2>;
+ /* The GPIO pins connect to 16 consecutive CUI bits */
+ interrupts = <0x03000 4>, <0x03001 4>, <0x03002 4>, <0x03003 4>,
+ <0x03004 4>, <0x03005 4>, <0x03006 4>, <0x03007 4>,
+ <0x03008 4>, <0x03009 4>, <0x0300a 4>, <0x0300b 4>,
+ <0x0300c 4>, <0x0300d 4>, <0x0300e 4>, <0x0300f 4>;
+ };
+
+
+
+ /* BGX definitions here must match kilburn_board.c settings. */
+ /* BGX 0*/
+ ethernet-mac-nexus@11800e0000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe0000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+
+ /* SerDes 0, may differ from PCS Lane/LMAC */
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00 00 00 00
+];
+ cavium,qlm-trim = "2,xlai";
+ };
+ };
+
+ /* BGX 1*/
+ ethernet-mac-nexus@11800e1000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe1000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };

```

```

+ ethernet-mac@1 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <1>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@2 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <2>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@3 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <3>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+
+ };
+ };
+};
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile
OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/Makefile
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/Makefile 2016-04-11 17:24:08.439857563 -0700
@@ -0,0 +1,68 @@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+#
+# See file CREDITS for list of people who contributed to this
+# project.
+#
+# This program is free software; you can redistribute it and/or
+# modify it under the terms of the GNU General Public License as
+# published by the Free Software Foundation; either version 2 of
+# the License, or (at your option) any later version.
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+# GNU General Public License for more details.
+#
+# You should have received a copy
+# of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,

```

```

+# MA 02111-1307 USA
+#
+#
+include $(TOPDIR)/config.mk
+
+CFLAGS += -I$(TOPDIR)/arch/mips/include/asm/arch-octeon
+
+LIB = $(obj)lib$(BOARD).o
+
+COBJS = $(BOARD)_board.o
+SOBJS =
+COBJS-y =
+#OBS = $(BOARD).o flash.o
+
+SRCS := $(SOBJS:.o=.S) $(COBJS:.o=.c) $(COBJS-y:.o=.c)
+OBS := $(addprefix $(obj),$(COBJS))
+SOBJS := $(addprefix $(obj),$(SOBJS))
+
+all: $(obj).depend $(LIB)
+
+DTB = $(BOARD).dtb.o
+DTBOBJS := $(addprefix $(obj),$(DTB))
+
+$(BOARD).dtb.o: $(BOARD).dtb.S
+
+$(BOARD).dtb.S: $(BOARD).dtb
+ echo '.section .rodata, "a"' > $@
+ echo '.balign 8' >> $@
+ echo '.global __dtb_begin' >> $@
+ echo '__dtb_begin:' >> $@
+ echo '.incbin "$<"' >> $@
+ echo '__dtb_end:' >> $@
+ echo '.global __dtb_end' >> $@
+ echo '.balign 8' >> $@
+
+$(BOARD).dtb: $(BOARD).dts
+ $(TOPDIR)/tools/dtc -O dtb -p 1024 -o $@ $<
+
+$(LIB): $(obj).depend
+ $(OBS) $(SOBJS) $(DTBOBJS)
+ $(call cmd_link_o_target, $(OBS) $(SOBJS) $(DTBOBJS))
+
+#####
+include $(SRCTREE)/rules.mk
+
+sinclude $(obj).depend
+
+clean:

```



```

+ -rm -f $(BOARD).dtb $(BOARD).dtb.S
diff -urN OCTEON-SDK-old/bootloader/u-boot/boards.cfg OCTEON-SDK/bootloader/u-boot/boards.cfg
--- OCTEON-SDK-old/bootloader/u-boot/boards.cfg 2016-04-11 17:45:55.361670048 -0700
+++ OCTEON-SDK/bootloader/u-boot/boards.cfg 2016-04-11 17:23:03.316970283 -0700
@@ -112,3 +112,4 @@
Target ARCH CPU Board name Vendor SoC Options
#####
#####
octeon_rainier mips octeon rainier octeon -
octeon_rainier:OCTEON_RAINIER,OCTEON_PCI_HOST=1
+octeon_kilburn mips octeon kilburn octeon -
octeon_kilburn:OCTEON_KILBURN,OCTEON_PCI_HOST=0
diff
-urN OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h OCTEON-SDK/bootloader/u-
boot/include/configs/octeon_kilburn.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn.h 2016-04-11 17:24:54.087755446 -0700
@@ -0,0 +1,279 @@
+/*
+ * (C) Copyright 2003
+ * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+ *
+ * Copyright (C) 2015 Cavium, Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+/*
+ * This file contains the configuration parameters for
+ * Octeon EBB7304 board.
+ */
+
+#ifndef __CONFIG_H__
+#define __CONFIG_H__
+
+/**
+ * Define CONFIG_OCTEON_PCI_HOST = 1 to map the pci devices on the
+ * bus. Define CONFIG_OCTEON_PCI_HOST = 0 for target mode when the
+ * host system performs the pci bus mapping instead. Note that pci
+ * commands are enabled to allow access to configuration
+ * space for
+ * both modes.
+ */
+#ifndef CONFIG_OCTEON_PCI_HOST
+# define CONFIG_OCTEON_PCI_HOST 1
+#endif
+
+/** Enable USB support on OCTEON III */

```

```

+#define CONFIG_OCTEON_USB_OCTEON3
+
+#define CONFIG_OCTEON_BIG_STACK_SIZE 0x4000 /** 16K stack */
+
+#include "octeon_common.h"
+
+/* CONFIG_OCTEON_KILBURN set by Makefile in include/config.h */
+
+/** Default DDR clock if tuple doesn't exist in EEPROM */
+#define KILBURN_DEF_DRAM_FREQ 1050
+
+//#define CONFIG_OCTEON_ENABLE_LED_DISPLAY /** 8-char LED support */
+
+#define CONFIG_LBA48 /** 48-bit mode */
+#define CONFIG_SYS_64BIT_LBA /** 64-bit LBA support */
+#define CONFIG_SYS_ATA_BASE_ADDR 0 /** Make compile happy */
+/* Base address of Common memory for Compact flash */
+
+/* eMMC support */
+#define CONFIG_OCTEON_MMC /** Enable MMC support */
+#define CONFIG_MMC_MBLOCK /** Multi-block support */
+#define CONFIG_CMD_MMC /** Enable mmc command */
+#define CONFIG_SYS_MMC_SET_DEV /** Enable multiple MMC devices */
+#define
CONFIG_MMC /** Enable MMC support */
+#define CONFIG_OCTEON_MIN_BUS_SPEED_HZ 100000 /** Minimum MMC bus speed */
+
+/** Maximum number of MMC devices needed for API */
+#define CONFIG_SYS_MMC_MAX_DEVICE 2
+
+#if 0
+/** Enable support for native U-Boot application API */
+#define CONFIG_API
+#endif
+
+
+/* TWSI address for the ispPAC-POWR1220AT8 Voltage Controller chip */
+#define BOARD_ISP_TWSI_ADDR 0x08
+#define CONFIG_ISPPAC_POWER
+
+/** Address of MCU on TWSI bus */
+#define BOARD_MCU_TWSI_ADDR 0x60
+
+/** Address of board EEPROM on TWSI bus */
+#define CONFIG_SYS_I2C_EEPROM_ADDR OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR
+/** Default EEPROM address */
+#define CONFIG_SYS_DEF_EEPROM_ADDR CONFIG_SYS_I2C_EEPROM_ADDR
+/* These speed up writes to the serial EEPROM by enabling page writes.

```

```

+ * Please see the datasheet for details.
+ */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_BITS 5 /* 32 bytes */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_DELAY_MS 5
+
+/* Set bootdelay to 0 for immediate
boot */
+#define CONFIG_BOOTDELAY 0 /*** autoboot after X seconds */
+
+#undef CONFIG_BOOTARGS
+
+/*** Enable octbootbus command */
+#define CONFIG_CMD_OCTEON_BOOTBUS
+
+/***
+ * The 'mtdids' environment variable is used in conjunction with the 'mtdparts'
+ * variable to define the MTD partitions for u-boot.
+ */
+#define MTDPARTS_DEFAULT \
+ "mtdparts=octeon_nor0:3m(bootloader)ro," \
+ "2m(kernel)," \
+ "3064k(cramfs)," \
+ "8k(environment)ro;" \
+ "octeon_nand0:6m(bootloader)ro," \
+ "2m(environment)," \
+ "8m(kernel)," \
+ "512m(rootfs)\0"
+
+#define MTDIDS_DEFAULT "nor0=octeon_nor0,nand0=octeon_nand0\0"
+
+/***
+ * End of the NAND boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary. It should be large enough to be able to
handle
+ * at least one bad block.
+ */
+#define CONFIG_OCTEON_NAND_BOOT_END 0x600000
+
+
+/* Define this to enable built-in octeon ethernet support */
+#define CONFIG_OCTEON_SGMII_ENET /*** SGMII ethernet support */
+#define CONFIG_OCTEON_XAUI_ENET /*** XAUI ethernet support */
+
+
+/* Enable Octeon built-in networking if RGMII support is enabled */
+#if defined(CONFIG_OCTEON_RGMII_ENET) || defined(CONFIG_OCTEON_SGMII_ENET) || \
+ defined(CONFIG_OCTEON_XAUI_ENET) || defined(CONFIG_OCTEON_QSGMII_ENET)
+# define CONFIG_OCTEON_INTERNAL_ENET
+#endif

```

```

+#define CONFIG_OCTEON_MGMT_ENET
+
+/* PCI console is supported since oct-remote-boot is supported.
+ * Enable MUX and oct-remote-bootcmd support as well
+ */
+#define CONFIG_SYS_PCI_CONSOLE
+#define CONFIG_CONSOLE_MUX
+#define CONFIG_OCTEON_BOOTCMD
+
+#include "octeon_cmd_conf.h"
+
+#ifdef CONFIG_CMD_NET
+/**
+ * Define available PHYs
+ */
+# define CONFIG_PHY_GIGE /** Gigabit Ethernet PHY support */
+# define CONFIG_PHY_MARVELL /**
Marvell PHY support */
+# define CONFIG_PHY_VITESSE /** Vitesse PHY support */
+# define CONFIG_PHY_BROADCOM /** Broadcom PHY support */
+# define CONFIG_PHY_CORTINA /** Cortina PHY support */
+# include "octeon_network.h"
+#endif
+
+
+
+#define CONFIG_CMD_OCTEON_TLVEEPROM /** "tlv_eeeprom" command */
+/* "qlm" command */
+#define CONFIG_CMD_QLM /** QLM command */
+
+
+/* Enable MTD partitioning support */
+#define CONFIG_CMD_MTDPARTS
+
+
+/* Enable JFFS2 support */
+#define CONFIG_CMD_JFFS2
+
+
+/** Enable "date" command to set and get date from RTC */
+#define CONFIG_CMD_DATE
+#define CONFIG_CMD_FLASH /** flinfo, erase, protect */
+#define CONFIG_CMD_EXT2 /** EXT2/3 filesystem support */
+#define CONFIG_CMD_EXT4 /** EXT4 filesystem support */
+#define CONFIG_CMD_FAT /** FAT support */
+#define CONFIG_FAT_WRITE /** FAT write support */
+
+
+/* SPI NOR flash support */
+#define CONFIG_SF_DEFAULT_BUS 0 /** SPI flash default bus */
+#define CONFIG_SF_DEFAULT_CS 0 /** SPI flash default

```

```

chip select */
+#define CONFIG_SF_DEFAULT_SPEED 50000000 /** Default speed in MHz */
+
+#define CONFIG_OCTEON_SPI /** Enable OCTEON SPI driver */
+#define CONFIG_SPI_FLASH /** Enable SPI flash driver */
+#define CONFIG_SPI_FLASH_STMICRO /** Enable ST Micro SPI flash */
+#define CONFIG_CMD_SPI /** Enable SPI command */
+#define CONFIG_CMD_SF /** Enable SPI flash command */
+
+#define CONFIG_OCTEON_SPI_NO_FAILSAFE /** SPI failsafe not yet supported */
+/**
+ * Start of where the SPI bootloader images are placed. Must be on an erase
+ * block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_START 0x10000
+
+/**
+ * End of the SPI boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_END 0x400000
+
+/** Maximum number of NAND devices */
+#define CONFIG_SYS_MAX_NAND_DEVICE 1
+#define CONFIG_SYS_NAND_FORCE_WIDTH 8 /** Force to 8-bit width */
+/** Not used for Octeon, but must be set */
+#define
CONFIG_SYS_NAND_BASE 0
+/** Maximum number of NAND devices */
+#define CONFIG_SYS_NAND_MAX_CHIPS CONFIG_SYS_MAX_NAND_DEVICE
+
+/** Enable multi-bit ECC support */
+#define CONFIG_OCTEON_HW_BCH
+#include "config_octeon_nand.h"
+
+
+/**
+ * Miscellaneous configurable options
+ */
+/** Environment variables that will be set by default */
+#define CONFIG_EXTRA_ENV_SETTINGS \
+ "burn_app=erase $(flash_unused_addr) +$(filesize);cp.b $(fileaddr) $(flash_unused_addr) $(filesize)\0" \
+ "bf=bootoct $(flash_unused_addr) forceboot numcores=$(numcores)\0" \
+ "linux_mmc=fatload mmc 1 $(loadaddr) vmlinux.64;bootoctlinux $(loadaddr)\0" \
+ "autoload=n\0" \
+ "ls=fatls mmc 0\0" \
+ ""
+

```

```

+/*-----
+ * FLASH and environment organization
+ */
+#define CONFIG_SYS_FLASH_SIZE (16*1024*1024) /** Flash size (bytes) */
+#define CONFIG_SYS_MAX_FLASH_BANKS 1 /* max
number of memory banks */
+#define CONFIG_SYS_MAX_FLASH_SECT (256) /* max number of sectors on one chip */
+
+/* Width of CFI bus to start scanning */
+#define CONFIG_SYS_FLASH_CFI_WIDTH FLASH_CFI_16BIT
+/* Enable extra elements in CFI driver for storing flash geometry */
+#define CONFIG_SYS_FLASH_CFI 1
+/* Enable CFI flash driver */
+#define CONFIG_FLASH_CFI_DRIVER 1
+#define CONFIG_SYS_FLASH_USE_BUFFER_WRITE
+
+/* We're not RAM_RESIDENT so CONFIG_ENV_IS_IN_FLASH will be set. */
+#if CONFIG_RAM_RESIDENT
+# define CONFIG_ENV_IS_NOWHERE 1
+#else
+# define CONFIG_ENV_IS_IN_FLASH 1
+#endif
+
+#define CONFIG_LIBATA /** Needed for SATA */
+#define CONFIG_CMD_SATA /** Enable the sata command */
+#define CONFIG_SYS_SATA_MAX_DEVICE 2 /** Support up to 2 devices */
+#define CONFIG_SATA_AHCI /** Enable AHCI driver */
+#define CONFIG_SATA_AHCI_PLAT /** Enable AHCI platform support */
+#define CONFIG_SATA_AHCI_OCTEON /** Enable Octeon AHCI driver */
+#define CONFIG_AHCI_SETFEATURES_XFER /**
Enable setfeature xfer feature */
+
+/* Address and size of Primary Environment Sector */
+#define CONFIG_ENV_SIZE (128*1024) /** Size of environment */
+#define CONFIG_ENV_ADDR (CONFIG_SYS_FLASH_BASE + CONFIG_SYS_FLASH_SIZE -
CONFIG_ENV_SIZE)
+
+/*-----
+ * Cache Configuration (not used)
+ */
+#define CONFIG_SYS_DCACHE_SIZE (32 * 1024) /** L1 Data cache size */
+#define CONFIG_SYS_ICACHE_SIZE (78 * 1024) /** L1 icache size */
+
+#if CONFIG_OCTEON_PCI_HOST
+/** Right now only the Intel E1000 driver has been ported */
+# define CONFIG_E1000
+#endif
+

```

```

+/** The CN73XX has two TWSI buses and the EBB7304 board uses both of them */
+#define CONFIG_SYS_MAX_I2C_BUS 1
+
+#define CONFIG_RTC_DS1337 /** RTC clock chip */
+#define CONFIG_SYS_I2C_RTC_ADDR 0x68 /** RTC I2C address */
+#define CONFIG_SYS_RTC_BUS_NUM 0 /** RTC I2C bus */
+
+/** Enable watchdog support */
+#define CONFIG_HW_WATCHDOG
+
+/** Configure QLM support
 */
+#define CONFIG_OCTEON_QLM
+
+/** Include shared board configuration, consisting mainly of DRAM details. */
+#include "octeon_kilburn_shared.h"
+
+/** Disable DDR2 support */
+#define CONFIG_OCTEON_DISABLE_DDR2
+
+#endif /** __CONFIG_H__ */
diff -urN OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h OCTEON-
SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 1969-12-31 16:00:00.000000000
-0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 2016-04-11 17:25:01.645670598 -
0700
@@ -0,0 +1,598 @@
+#ifndef __OCTEON_KILBURN_SHARED_H__
+#define __OCTEON_KILBURN_SHARED_H__
+
+
+
+/**
+ * Define this to use SPD data defined in header files instead of
+ * using TWSI to access the SPDs in the DIMMs. This is a workaround
+ * for boards that lack working TWSI interfaces.
+ */
+
+/** 1-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC0(spd_pointer) \
+ {{0x0,
+ 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \

```

```

+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0,
0x0}, {spd_pointer, NULL}}
+
+/* 1-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC2(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC3(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC2(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC3(spd_pointer)
\
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}

```





```

+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+ */
+#define OCTEON_KILBURN_CFG0_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+/*-----*/
+/* Configuration 1: gpio 1 = ON */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG1_SPD_VALUES MT18ASF1G72AZ_2G1AYESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+ */
+#define OCTEON_KILBURN_CFG1_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+
+#define
MERGE_AGAIN(X, Y) X ## Y
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION0 {{0x50, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION1 {{0x54, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION2 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION3 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+
+#define OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR (0x50)
+
+
+
+/*
+ * Local copy of these parameters to allow for customization for this
+ * board design. The generic version resides in lib_octeon_shared.h.
+ */
+
+/* LMC0_MODEREG_PARAMS1 */
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = 0,

```

```

\
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+
.rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_HiZ, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0,
\
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_HiZ, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \

```

```

+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0 \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \
+ \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 =
+ ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = ddr4_rttnom_120ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = ddr4_rttnom_120ohm, \

```

```

+ .pasr_10 = 0, \
+ .asr_10 = 0,
+
+ \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_240ohm, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = ddr4_rttnom_120ohm, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = ddr4_rttwr_240ohm, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = ddr4_rttnom_120ohm, \
+ }
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = rttwr_60ohm, \
+ .dic_00 = dic_34ohm,
+
+ \
+ .rtt_nom_00 = rttnom_20ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = rttwr_60ohm, \
+ .dic_01 = dic_34ohm, \
+ .rtt_nom_01 = rttnom_none, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = rttwr_60ohm, \
+ .dic_10 = dic_34ohm, \
+ .rtt_nom_10 = rttnom_20ohm, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = rttwr_60ohm,
+
+ \
+ .dic_11 = dic_34ohm, \
+ .rtt_nom_11 = rttnom_none, \
+ }
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT \

```

```

+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00 = 0x22, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = 0, \
+ .vref_value_01 = 0, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+/* FIX */
+#define OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_34ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = 0, \
+ .vref_value_01 = 0, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_34ohm, \
+ .vref_value_10 = 0x1f, \
+ .vref_range_10
= 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_120ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_120ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \

```

```

+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_60ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_60ohm, \
+ .vref_value_10 = 0x19, \
+ .vref_range_10 = 0, \
+ .rtt_park_11
+ = ddr4_rttpark_60ohm, \
+ .vref_value_11 = 0x19, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_80ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_80ohm, \
+ .vref_value_01 = 0x1f, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
+ LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====

```

```

===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm,
 0x00000000ULL, OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT,
 OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT, ddr4_rodt_ctl_48_ohm, 0x00000000ULL,
 0 }, \
+ /* 2 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
 OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT,
 OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT, ddr4_rodt_ctl_48_ohm, 0x00000000ULL,
 0 }
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
 LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
 OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT,
 OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT, ddr4_rodt_ctl_80_ohm, 0x00000000ULL,
 0 }, \
+ /* 2 */ { ddr4_dqx_driver_34_ohm,
 0x0c0c0303ULL, OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT,
 OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT, ddr4_rodt_ctl_48_ohm, 0x04080102ULL,
 0 }
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
 LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x01030203ULL,
 OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT,
 OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT, ddr4_rodt_ctl_48_ohm, 0x01010202ULL,
 0 }
+
+/* Construct a static initializer for the ddr_configuration_t variable that holds
+** (almost) all of the information required for DDR initialization.
+*/
+
+
+/*
+
+ The parameters below make up the custom_lmc_config data structure.
+ This structure
+ is used to customize the way that the LMC DRAM
+ Controller is configured for a particular board design.
+
+ Refer to the file lib_octeon_board_table_entry.h for a description
+ of the custom board settings. It is usually kept in the following
+ location... arch/mips/include/asm/arch-octeon/
+

```



```

+*/
+
+#define OCTEON_KILBURN_DDR_CONFIGURATION(SPD_CONFIGURATION) \
+ /* Interface 0 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##0, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk \
+ = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \

```

```

+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }, \
+ /* Interface 1 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##1, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \

```

```

+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ },
+
+ \
+ /* Interface 2 */
+ {
+ .custom_lmc_config = {
+ .min_rtt_nom_idx = 1, \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2,
+
+ \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0},
+ .dimm_config_table = {
+ SPD_CONFIGURATION##2,
+
+ \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = {
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0},
+ .registered = {
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0},
+ .odt_1rank_config = {

```

```

+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+ \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ },
+ /* Interface 3 */
+ {
+ .custom_lmc_config = {
+ .min_rtt_nom_idx = 1,
+ .max_rtt_nom_idx = 7,
+ .min_rodt_ctl = 1,
+ .max_rodt_ctl = 7,
+ .ck_ctl = ddr4_driver_34_ohm,
+ .cmd_ctl = ddr4_driver_34_ohm,
+ \
+ .ctl_ctl = ddr4_driver_34_ohm,
+ .min_cas_latency = 0,
+ .offset_en = 1,
+ .offset_udimm = 2,
+ .offset_rdimm = 2,
+ .ddr_rtt_nom_auto = 0,
+ .ddr_rodt_ctl_auto = 0,
+ .rlevel_comp_offset_udimm = 0,
+ .rlevel_comp_offset_rdimm = 0,
+ .rlevel_compute = 0,
+ .ddr2t_udimm = 1,
+ .ddr2t_rdimm = 1,
+ .maximum_adjacent_rlevel_delay_increment = 2,
+ .fprch2 = 2,
+ \
+ .dll_write_offset = NULL,
+ .dll_read_offset = NULL,
+ .parity = 0},
+ .dimm_config_table = {
+ SPD_CONFIGURATION##3,
+ DIMM_CONFIG_TERMINATOR},
+ .unbuffered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk = 0,
+ .lmc_delay_cmd = 0,
+ .lmc_delay_dq = 0},
+ .registered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk
= 0,
+ .lmc_delay_cmd = 0,

```

```

+ .lmc_delay_dq = 0},
+ .odt_1rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+ .odt_2rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION},
+ .odt_4rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION}
+ }
+
+
+##endif /* __OCTEON_KILBURN_SHARED_H__ */
diff -urN OCTEON-SDK-old/executive/cvmx-app-init.h OCTEON-SDK/executive/cvmx-app-init.h
--- OCTEON-SDK-old/executive/cvmx-app-init.h 2016-04-11 17:31:52.343671090 -0700
+++ OCTEON-SDK/executive/cvmx-app-init.h 2016-04-11 17:32:06.134670808 -0700
@@ -362,6 +362,7 @@
 ** use any numbers in this
 range. */
 CVMX_BOARD_TYPE_CUST_PRIVATE_MIN = 20001,
 CVMX_BOARD_TYPE_UBNT_E100 = 20002,
+ CVMX_BOARD_TYPE_CUST_KILBURN = 20003,
 CVMX_BOARD_TYPE_CUST_PRIVATE_MAX = 30000,

 /* Range for IO modules */
@@ -503,6 +504,7 @@
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MIN)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_UBNT_E100)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_CLARK)
+ ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_KILBURN)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MAX)
 /* Module range */
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_MODULE_MIN)

```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/kilburn_sdk_p4.patch
```

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```

```
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```

```
diff -aur a/OCTEON-SDK/bootloader/u-boot/tools/setlocalversion b/OCTEON-SDK/bootloader/u-
boot/tools/setlocalversion
```

```
--- a/OCTEON-SDK/bootloader/u-boot/tools/setlocalversion 2018-03-23 14:00:20.547230326 -0400
```

```
+++ b/OCTEON-SDK/bootloader/u-boot/tools/setlocalversion 2018-04-04 08:20:27.261648898 -0400
```

```
@@ -14,6 +14,7 @@
```

```
scm_only=false
```

```
srcree=.
```

```
+sdk="3.1.2-p12"
```

```
if test "$1" = "--save-scmversion"; then
```

```
 scm_only=true
```

```

shift
@@ -154,7 +155,7 @@
if rev=`svnversion 2>/dev/null` ; then
rev=`echo "${rev}" | awk '{print $NF}'`
 sdk_rev=`svnversion $OCTEON_ROOT/executive | awk '{print $NF}'`
- printf -- ' (Development build, svnversion: u-boot:%s, exec:%s)' $rev $sdk
+ printf -- ' (Development build, exec:%s)' $sdk
fi

localversion* files in the build and source directory

Found in path(s):
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/uboot-SDK-version-
banner.patch
No license file was found, but licenses were detected in source scan.

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CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile | 4 +++
1 file changed, 2 insertions(+), 2 deletions(-)

diff --git a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile b/CNNIC-SDK-
SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile
index 09be185..d220800 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile
+++ b/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile
@@ -24,4 +24,4 @@

-CC = gcc -include /usr/include/errno.h
-AR = ar
+CC ?= gcc -include /usr/include/errno.h
+AR ?= ar
NAME = cnutils

Found in path(s):
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
CNNIC-SDK-SRC-modules-driver-src-host-linux-user-drv-cnutils-Makefile.patch
No license file was found, but licenses were detected in source scan.

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diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/host-tap-defs.h
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@

```

```

*
* CNNIC SDK
*
- * Copyright (c) 2015 Cavium Networks. All rights reserved.
+ * Copyright (c) 2016 Cavium Networks. All rights reserved.
*
* This file, which is part of the CNNIC SDK which also includes the
* CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -28,8 +28,6 @@
/* defines */
#define cavium_read_mem(buf, idx, len, dev) user_drv_pci_read_mem(buf, idx, len, dev)
#define cavium_write_mem(idx, buf, len, dev) user_drv_pci_write_mem(idx, buf, len, dev)
-#define READ_IDX_PTR
 read_idx_addr
-#define WRITE_IDX_PTR
 write_idx_addr

#define CNXX_GPIO_TX_SET (0x0001070000000888ull)
#define CNXX_GPIO_TX_CLR (0x0001070000000890ull)
@@ -43,10 +41,11 @@
#define RX_BUF_SIZE 2048

/* globals */
-uint64_t write_idx_addr, read_idx_addr;
FILE *cutils_stdout = NULL;
void *oct_pci_dev = NULL;
int tun_fd = 0;
+fd_set readfds;
+struct timeval tv;

/* Functions */
void * user_drv_pci_open(int device);
@@ -73,10 +72,9 @@ uint64_t user_drv_pci_get_scratchreg(voi
* The function waits indefinitely for OCTEON to respond otherwise.
*/

-static void host_oct_handshake(void *user_dev)
+static void host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
 uint64_t scratch_reg_addr, scratch_val = 0;
-
 scratch_reg_addr = user_drv_pci_get_scratchreg(user_dev);

 /* Wait for OCTEON to to send indication of
handshake start */
@@ -96,12 +94,11 @@ static void host_oct_handshake(void *us
 while(scratch_val == HOSTFW_HS_ACK)
 scratch_val = user_drv_pci_read_csr(scratch_reg_addr, user_dev);

```

```

- write_idx_addr = scratch_val;
-
- cavium_print_dbg("Received phys addr of NB as %lx\n",write_idx_addr);
-
- /* Set Read address */
- read_idx_addr = write_idx_addr + RX_BUF_SIZE;
+ /*Host read follows host write*/
+ write_buf->base = scratch_val;
+ read_buf->base = write_buf->base + sizeof(nw_buf_t);
+
+ cavium_print_dbg("write: %lx read: %lx\n",write_buf->base,read_buf->base);
scratch_val = HOSTFW_HS_NB_DONE;
user_drv_pci_write_csr(scratch_reg_addr, scratch_val, user_dev);

```

```

@@ -109,6 +106,27 @@ static void host_oct_handshake(void *us
return;
}

```

```

+static int host_wait_on_fds()
+{
+ int nfds = 0;
+ int ret = -1;
+
+ nfds = tun_fd;
+ tv.tv_sec = 0;
+ tv.tv_usec = 0;
+ FD_ZERO(&readfds);
+ FD_SET(tun_fd, &readfds);
+
+ ret = select(nfds + 1, &readfds, NULL, NULL, &tv);
+
+ if (ret == -1)
+ {
+ cavium_print_msg("Select faied with error %d\n", errno);
+ return 0;
+ }
+ return 1;
+
+}
+
+/*
+ * Interrupt Octeon after data has been written to BAR1 memory.
+ * Octeon upon receiving this interrupt would wake up the process

```

```

@@ -137,7 +155,7 @@ static inline int host_verify_data_valid
cavium_read_mem((uint8_t*)read_buf, addr, 64, oct_pci_dev);

```

```

if (memcmp((void *)read_buf, (void *)buffer, 64))
- return FAILED;

```



```

+ return FAILED;

 return SUCCESS;
}
@@ -161,34 +179,31 @@ static inline int host_oct_init()
}

/*
- * Forever data loop for host to read/write data from/to BAR1/TAP.
+ * Forever data loop for host.
+ * Read from Host TAP and write to BAR1.
+ * Read from BAR1 and write to Host TAP.
+ *
+ * @param read and write buffers.
+ */
static void host_oct_data_loop(struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
- int nread
= 0;
 while (1)
 {
- /* Tx: Read data from tap and write to OCTEON memory */
-
- nread = read(tun_fd, write_buf->data, NW_BUF_SIZE);
-
- /* Proceed only when data is available from tap interface*/
- if(nread > 0)
+
+ if(host_wait_on_fds())
 {
- host_oct_network_to_pci(write_buf, nread);
-
- /* Inform Octeon */
- host_interrupt_octeon(oct_pci_dev);
+ if (FD_ISSET(tun_fd, &readfds))
+ {
+ if(host_oct_network_to_pci(write_buf, tun_fd)){
+ /* Inform Octeon */
+ host_interrupt_octeon(oct_pci_dev);
+ }
+ }
 }

- /* Rx: Read data from OCTEON memory and write to tap */
-
- host_oct_pci_to_network(read_buf);
-
- /* Yield the CPU */

```

```

- nsleep(5);
 }

 user_drv_pci_close(oct_pci_dev);
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/gpio_tap.c pci-tap/CNNIC-SDK-
SRC/modules/pci-tap/kernel/drv/gpio_tap.c

pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/gpio_tap.c 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/gpio_tap.c 2016-06-14 19:57:33.000000000 +0530
@@ -1,3 +1,26 @@
+/*
+ *
+ * CNNIC SDK
+ *
+ * Copyright (c) 2016 Cavium Networks. All rights reserved.
+ *
+ * This file, which is part of the CNNIC SDK which also includes the
+ * CNNIC SDK Package from Cavium Networks, contains proprietary and
+ * confidential information of Cavium Networks and in some cases its
+ * suppliers.
+ *
+ * Any licensed reproduction, distribution, modification, or other use of
+ * this file or the confidential information or patented inventions
+ * embodied in this file is subject to your license agreement with Cavium
+ * Networks. Unless you and Cavium Networks have agreed otherwise in
+ * writing, the applicable license terms "OCTEON SDK License Type 5" can be
+ * found under the directory: $CNNIC_ROOT/licenses/
+ *
+ *
+ * All other use and disclosure is prohibited.
+ *
+ * Contact Cavium Networks at info@caviumnetworks.com for more information.
+ *
+ */
#include <linux/module.h>
#include <linux/wait.h>
#include <linux/rculist.h>
@@ -68,11 +91,19 @@ static unsigned int gpio_dev_poll(struct
static irqreturn_t octeon_gpio_handler(int irq, void * data)
{
 unsigned long flags;
- cvmx_gpio_tx_clr_t tx_clr;
-
+
+ #ifdef CONFIGURE_GPIO_OUTPUT
+ cvmx_gpio_tx_clr_t tx_clr;
+ tx_clr.u64 = cvmx_read_csr(CVMX_GPIO_TX_CLR);
+ tx_clr.s.clr |= GPIO_TX_CLEAR_MASK;

```

```

 cvmx_write_csr(CVMX_GPIO_TX_CLR, tx_clr.u64);
+##else
+ cvmx_gpio_intrx_t intrx;
+ /*Clear any previous or Stale interrupt*/
+ intrx.u64 = cvmx_read_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ));
+ intrx.cn73xx.intr = 0x1; /*Clear*/
+ cvmx_write_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ), intrx.u64);
+##endif

 spin_lock_irqsave(&gpio_lock, flags);
 wakeup_fd = 1;
@@ -91,9 +122,34 @@ int octeon_gpio_ciu3_init(void)
 irq_handler_t isr;
 unsigned long irqflags;
 int error;
- cvmx_gpio_bit_cfgx_t gpio_cfg;
+##ifdef CONFIGURE_GPIO_OUTPUT
+ cvmx_gpio_bit_cfgx_t cfg;
+##endif
 const char *desc = "gpio_tap";

+##ifdef DISABLE_INITIAL_SPURIOUS_INTR /* Not tested*/
+
+ /*Code to remove initial Spurious interrupt*/
+ cvmx_gpio_bit_cfgx_t gpio_cfg;
+ cvmx_gpio_intrx_t intrx;
+
+ /*Errata CN73XX GPIO_BIT_CFGX[int_en]=1->0 does not make any affect*/
+ if(OCTEON_IS_MODEL(OCTEON_CN73XX_PASS1_X)){
+ cvmx_write_csr(CVMX_CIU3_ISCX_W1C(0x3000 + GPIO_CIU3_BASE_IRQ), 1);
+ cvmx_write_csr(CVMX_CIU3_ISCX_W1C(0x3008), 1);
+ }
+
+ /*Disable interrupt. Let user process enables it*/
+ gpio_cfg.u64 = cvmx_read_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
+ gpio_cfg.s.int_en = 0;
+ cvmx_write_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ), gpio_cfg.u64);
+
+ /*Clear any previous or Stale interrupt*/
+ intrx.u64=cvmx_read_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ));
+
+ intrx.cn73xx.intr = 0x1; /*Clear*/
+ cvmx_write_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ), intrx.u64);
+##endif
+
 error = register_chrdev(GPIO_TAP_DEV_MAJOR, DRIVER_NAME, &octeon_gpio_fops);
 if(error < 0) {
 panic("OCTEON: Device Registration failed, error:%d\n", error);

```

```

@@ -124,11 +180,12 @@ int octeon_gpio_ciu3_init(void)
 irq, error);
 goto fail;
}
-
- gpio_cfg.u64 = cvmx_read_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
- gpio_cfg.s.tx_oe = 1;
- gpio_cfg.s.int_type = 1;
- cvmx_write_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ), gpio_cfg.u64);
+#ifdef CONFIGURE_GPIO_OUTPUT
+ cfg.u64 = cvmx_read_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
+ cfg.s.tx_oe = 1;
+ cfg.s.int_type = 1;
+ cvmx_write_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ), cfg.u64);
+#endif
 init_waitqueue_head(&gpio_dev_waitqueue);

 return 0;
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/Makefile pci-tap/CNNIC-SDK-
SRC/modules/pci-tap/kernel/drv/Makefile

pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/Makefile 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/Makefile 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
#
CNNIC SDK
#
-# Copyright (c) 2015 Cavium Networks. All rights reserved.
+# Copyright (c) 2016 Cavium Networks. All rights reserved.
#
This file, which is part of the CNNIC SDK which also includes the
CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -32,7 +32,7 @@
#

#CFLAGS_LOCAL = -O2 -W -Wall -Wno-unused-parameter -fno-strict-aliasing
-CFLAGS_LOCAL = -O2 -DGPIO_CIU3_BASE_IRQ=1
+CFLAGS_LOCAL = -O2 -DGPIO_CIU3_BASE_IRQ=1 -DCONFIGURE_GPIO_OUTPUT

EXTRA_CFLAGS = $(CFLAGS_LOCAL)

diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/Makefile pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/Makefile
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/Makefile 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/Makefile 2016-06-14
19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
#

```

```

CNNIC SDK
#
-# Copyright (c) 2015 Cavium Networks. All rights reserved.
+# Copyright (c) 2016 Cavium Networks. All rights reserved.
#
This file, which is part of the CNNIC SDK which also includes the
CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -35,7 +35,7 @@ CFLAGS_LOCAL = -O2 -W -Wall -Werror -Wno

EXTRA_CFLAGS = $(CFLAGS_LOCAL) -DGPIO_CIU3_BASE_IRQ=1

-
+export NUMBER_OF_BAR_DESC=1
OCT_PCI_TAP=pci-tap
HOST_TAP_BIN=host-pci-tap

@@ -43,7 +43,7 @@ all: hostapp octapp

hostapp: CC=gcc
hostapp: AR=ar
-hostapp: EXTRA_CFLAGS += -Wno-error=declaration-after-statement
+hostapp: EXTRA_CFLAGS += -Wno-error=declaration-after-statement -
DN_DESC=$(NUMBER_OF_BAR_DESC)
hostapp: EXTRA_LDFLAGS = -L $(CNNIC_ROOT)/modules/pci-tap/ -lpthread -lnutils
hostapp: $(OCT_PCI_TAP)

@@ -64,9 +64,9 @@ octapp : check_octeon_sdk build_oct
OCTEON_TARGET = linux_64
TARGET
 = oct-pci-tap

-build_oct: EXTRA_CFLAGS += -DOCTEON_LE -DENABLE_OCTEON=0 -DENABLE_OCTEON_PLUS=0
+build_oct: EXTRA_CFLAGS += -DOCTEON_LE -DENABLE_OCTEON=0 -DENABLE_OCTEON_PLUS=0 -
DN_DESC=$(NUMBER_OF_BAR_DESC)
build_oct: BINDIR := $(CNNIC_ROOT)/modules/driver/bin
-build_oct: COMPILE += -Wa,-a=$@.list -DOCT_TAP $(EXTRA_CFLAGS)
+build_oct: COMPILE += -Wa,-a=$@.list -DOCT_TAP -DCONFIGURE_GPIO_OUTPUT $(EXTRA_CFLAGS)
build_oct: $(TARGET)
build_fs: build_oct
 cp $(TARGET) $(TARGET).strip;
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/oct-tap-defs.h
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
*
* CNNIC SDK
*

```

```

- * Copyright (c) 2015 Cavium Networks. All rights reserved.
+ * Copyright (c) 2016 Cavium Networks. All rights reserved.
*
* This file, which is
part of the CNNIC SDK which also includes the
* CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -34,10 +34,8 @@
/* defines */
#define cast64(val) ((unsigned long long)val)
#define CVMX_DRV_GET_PHYS(ptr) (cast64(cvmx_ptr_to_phys(ptr)))
-#define cavium_read_mem(dest, src, len,dev) memcpy(dest, src, len)
+#define cavium_read_mem(dest, src, len,dev) memcpy(dest,src, len)
#define cavium_write_mem(dest, src, len, dev) memcpy(dest, src, len)
-#define READ_IDX_PTR read_idx_ptr
-#define WRITE_IDX_PTR write_idx_ptr

#define HOSTFW_HS_IND 0x1501
#define HOSTFW_HS_ACK 0x1502
@@ -61,7 +59,7 @@
#define max(a, b) ((a) > (b) ? (a) : (b))

/* globals */
-uint8_t *write_idx_ptr = NULL, *read_idx_ptr= NULL;
+
int gpio_fd = -1;
int tun_fd = -1;
fd_set readfds;
@@ -81,33 +79,25 @@ void *oct_pci_dev
= NULL;
* return Setup Read and write addresses for Host and return.
* The function waits indefinitely for HOST to respond otherwise.
*/
-static void host_oct_handshake(void *user_dev)
+static void
+host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
 uint64_t read_idx_addr, scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1, scratch_val;
 const cvmx_bootmem_named_block_desc_t *desc;
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
+ scratch_val |= 0x1; /* Enable Output bit */
+
+ cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ),
scratch_val);
+
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET);
+ if (scratch_val & 0x1) {
+ cavium_print_msg("TX_SET configured for GPIO 0.\n");

```

```

+ } else {
+ scratch_val |= 0x1;
+ cvmx_write_csr_node(cvmx_get_node_num(),
CVMX_GPIO_TX_SET, scratch_val);
+ }

-
- /* Setup interrupt for GPIO 0: Assumes kernel module is loaded. */
- scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
-
- scratch_val |= 0x1; /* Enable Output bit */
-
- cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ),
scratch_val);
-
- scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET);
-
- if (scratch_val & 0x1)
- {
- cavium_print_msg("TX_SET configured for GPIO 0.\n");
- }
- else
- {
- scratch_val |= 0x1;
- cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET, scratch_val);
- }
-
-
- /* Handshake */
+ /* Handshake */
 scratch_val = HOSTFW_HS_IND;
 cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

@@ -117,28 +107,31 @@ static void host_oct_handshake(void *use

 /* Fetch the named block allocated for pci-tap buffers*/
 desc = cvmx_bootmem_find_named_block(BLOCK_NAME);
-
+
 if(!desc)
 {
 /* Allocate named block for write index */
- read_idx_ptr = cvmx_bootmem_alloc_named_range(BLOCK_SIZE, NB_MIN_ADDR, NB_MAX_ADDR,
NB_4KB_ALIGNED, BLOCK_NAME);
+ read_buf->base = cvmx_bootmem_alloc_named_range(N_DESC*BLOCK_SIZE,
+ NB_MIN_ADDR, NB_MAX_ADDR, NB_4KB_ALIGNED, BLOCK_NAME);
 }

```

```

else
- read_idx_ptr = cvmx_phys_to_ptr(desc->base_addr);
-
- if(read_idx_ptr == (void *)-1)
+ read_buf->base = cvmx_phys_to_ptr(desc->base_addr);
+ if(read_buf->base == (void *)-1)
 {
 cavium_print_msg("Failed to allocate named block\n");
 exit(1);
 }

- cavium_print_dbg("read_idx_ptr: %p\n",read_idx_ptr);
+ cavium_print_dbg("read_idx_ptr: %p\n",read_buf->base);
+
+ /*memset complete block including w/r all desc*/
+ memset(read_buf->base, 0, N_DESC*BLOCK_SIZE);

- memset(read_idx_ptr, 0, BLOCK_SIZE);
- write_idx_ptr = (uint8_t *)((uint8_t *)read_idx_ptr + RX_BUF_SIZE);
+ /*OCTEON write
follows OCTEON read*/
+ write_buf->base = read_buf->base + sizeof(nw_buf_t);

- read_idx_addr = CVM_DRV_GET_PHYS(read_idx_ptr);
- cavium_print_dbg("write_idx_ptr:%p\n", write_idx_ptr);
+ read_idx_addr = CVM_DRV_GET_PHYS(read_buf->base);
+ cavium_print_dbg("write_idx_ptr:%p\n", write_buf->base);

 /* Write the phys address of named block to scratch reg */
 scratch_val = read_idx_addr;
@@ -152,7 +145,6 @@ static void host_oct_handshake(void *use
 {
 scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr);
 }
-
 cavium_print_msg("Host-Octeon Handshake Completed\n");
 cavium_print_msg("OCT-PCI-TAP running as daemon\n");
}
@@ -209,30 +201,27 @@ static inline int host_oct_init()
}

/*
- * Forever data loop for Octeon to read/write data from/to BAR1/TAP.
+ * Forever data loop for OCTEON.
+ * Read from OCTEON TAP and write to OCTEON DRAM.
+ * Read from OCTEON DRAM and write to OCTEON TAP.
+ *
+ * @param read and write buffers.

```



```

*/
static void host_oct_data_loop(nw_buf_t *read_buf, nw_buf_t *write_buf)
{
- int nread = 0;
 while (1)
 {
 oct_wait_on_fds();

 /* Data available from Host */
 if (FD_ISSET(gpio_fd, &readfds))
- {
+ {
 host_oct_pci_to_network(read_buf);
 }
-
+
 if (FD_ISSET(tun_fd, &readfds))
 {
- nread = read(tun_fd, write_buf->data, NW_BUF_SIZE);
- if(nread > 0)
- {
- host_oct_network_to_pci(write_buf, nread);
- }
+ host_oct_network_to_pci(write_buf, tun_fd);
 }
 }
}
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/pci-tap.c
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
*
* CNNIC SDK
*
- * Copyright (c) 2015
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+ * Copyright (c) 2016 Cavium Networks. All rights reserved.
*
* This file, which is part of the CNNIC SDK which also includes the
* CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -33,7 +33,6 @@
#include <stdint.h>
#include <time.h>
#include <unistd.h>
-
#include "pci-tap.h"

```

```

/*
@@ -81,41 +80,81 @@ int host_oct_tun_alloc(char *dev, int fl
 return fd;
}

+static inline int buf_isempty(nw_buf_t *pbuf,void *dev)
+{
+ int ret =0;
+ cavium_read_mem(pbuf ,pbuf->base, 2*sizeof(uint64_t), oct_pci_dev);
+
+ if(pbuf->ridx == pbuf->widx){
+ ret =1;
+ } else {
+ ret=0;
+ if(N_DESC == pbuf->ridx){
+ pbuf->ridx =0;
+ pbuf->flag =1;
+ }
+ }
+ return(ret);
+}
+
+static inline int buf_isfull(nw_buf_t *pbuf,void *dev)
+{
+ int ret=0;
+ cavium_read_mem(pbuf ,pbuf->base, 2*sizeof(uint64_t), oct_pci_dev);
+ if(pbuf->ridx == PREDEC_IDX(pbuf->widx)){
+
+ ret =1;
+ } else {
+ ret =0;
+ if(N_DESC == pbuf->widx){
+ pbuf->widx=0;
+ pbuf->flag=1;
+ }
+ }
+ return(ret);
+}
/*
* Read data from BAR1 memory and write it to the tap device.
*
* @param network buffer.
*
*/
-void host_oct_pci_to_network(void *buf)
+int host_oct_pci_to_network(void *pbuf)
{
- nw_buf_t *read_buf = (nw_buf_t *)buf;

```

```

-
- /* Rx: Read data from OCTEON memory and write to tap */
- cavium_read_mem((uint8_t*)read_buf, READ_IDX_PTR, 2*sizeof(uint64_t), oct_pci_dev);
-
- /* Wait until we have read access and a valid buffer to read */
- if(!read_buf->rw_idx) && (read_buf->len > 0)
- {
- cavium_print_dbg("Read %ld bytes from named block\n",read_buf->len);
- cavium_read_mem(read_buf, READ_IDX_PTR, NW_BUF_SIZE, oct_pci_dev);
- read_buf->rw_idx = 1;
-
- /* Update the index to allow peer to perform next write */
- cavium_write_mem(READ_IDX_PTR,(uint8_t*)read_buf,
sizeof(uint64_t), oct_pci_dev);
-
- /* Send the buffer to the linux stack */
- write(tun_fd, read_buf->data, read_buf->len);
- cavium_print_dbg("Wrote %ld bytes to /dev/%s\n", read_buf->len, TAP_IF_NAME);
- }
+ nw_buf_t *preadbuf = (nw_buf_t *)pbuf;
+ desc_t local_desc;
+ int actual_read = 0, skipidx;
+
+ if(!buf_isempty(preadbuf, oct_pci_dev)){
+
+ /*Go to the ridx data offset and read the data*/
+ memset(&local_desc, 0 , sizeof(desc_t));
+
+ /*Calculate of amount of descriptor index to be skipped if ndescs > 1*/
+ skipidx = (preadbuf->ridx)?(preadbuf->ridx -1):0;
+
+ /*Read complete descriptor data into local variable*/
+ cavium_read_mem(&local_desc, DESC_BASE_OFF(preadbuf) +
+ skipidx * sizeof(desc_t), sizeof(desc_t), oct_pci_dev);
+
+ actual_read = (local_desc.len > MAX_ETH_FRAM_SZ) ? MAX_ETH_FRAM_SZ : local_desc.len;
+ cavium_print_dbg("Read
%d bytes from named block\n",actual_read);
+ if(actual_read){
+ /* Send the buffer to the linux stack */
+ if(write(tun_fd, local_desc.data, actual_read)<0){
+ cavium_print_msg("Tunfd write error [%d, %s]\n", errno, strerror(errno));
+ } else {
+ cavium_print_dbg("Wrote %ld bytes to /dev/%s\n", actual_read, TAP_IF_NAME);
+ }
+ BUF_INC_IDX(preadbuf, ridx);
+ cavium_write_mem(preadbuf->base + OFFSETOF(nw_buf_t, ridx), &(preadbuf->ridx), sizeof(uint64_t),
oct_pci_dev);

```

```

+ }
#ifdef OCT_TAP
- else
- {
- cavium_print_msg("Invalid interrupt from Host\n");
+ else {
+ cavium_print_msg("Invalid interrupt from host with Packet len: 0\n");
+ }
+#endif
 }
-#endif /* OCT_TAP */
-
- return;
+ return actual_read;
}

/*
@@ -124,35 +163,33 @@ void host_oct_pci_to_network(void *buf)
 * @param network buffer.
 *
 */
-void
host_oct_network_to_pci(void * buf, int nread)
-{
- nw_buf_t *write_buf = (nw_buf_t *)buf;
-
- /* Octeon will read the data after clearing the interrupt, wait for write access. */
- while(!write_buf->rw_idx)
- {
- cavium_read_mem(write_buf, WRITE_IDX_PTR, sizeof(uint64_t), oct_pci_dev);
- }
-
- write_buf->len = nread;
-
- /* Reset the rw_idx to allow peer to perform next write */
- write_buf->rw_idx = 0;
-
- /* Writing data before index ensures data integrity. */
- cavium_write_mem(WRITE_IDX_PTR + 2*sizeof(uint64_t), (uint8_t*)write_buf + 2*sizeof(uint64_t), nread,
oct_pci_dev);
- cavium_write_mem(WRITE_IDX_PTR, (uint8_t*)write_buf, 2*sizeof(uint64_t), oct_pci_dev);
-
-#ifndef HOST_TAP
- /* Read the data back */
- if (host_verify_data_valid(WRITE_IDX_PTR + 2*sizeof(uint64_t),
- (uint8_t*)write_buf + 2*sizeof(uint64_t)) == FAILED)
- {
- cavium_print_msg("Data integrity check failed\n");

```

```

+int host_oct_network_to_pci(void
* buf, int tun_fd)
+{
+ int nread = 0, skipidx;
+ nw_buf_t *pwbuff = (nw_buf_t *)buf;
+
+ if(!buf_isfull(pwbuff, oct_pci_dev)){
+
+ /*Calculate of amount of descriptor index to be skipped if ndescs > 1*/
+ skipidx = (pwbuff->widx)?(pwbuff->widx -1):0;
+ if((nread = read(tun_fd, pwbuff->desc[pwbuff->widx].data, MAX_ETH_FRAM_SZ))<0){
+ cavium_print_msg("TUNFD read error [%d, %s]\n", errno, strerror(errno));
+ return -1;
+ }
+ /*Udate Length field and update the write index*/
+ if(nread){
+ cavium_write_mem((DESC_BASE_OFF(pwbuff) + (skipidx * sizeof(desc_t))
+ + OFFSETOF(desc_t, len)), &nread, sizeof(uint64_t), oct_pci_dev);
+ cavium_write_mem(DESC_BASE_OFF(pwbuff) + (skipidx * sizeof(desc_t)) +
+ OFFSETOF(desc_t, data), &(pwbuff->desc[pwbuff->widx].data),
+ nread, oct_pci_dev);
+
+ BUF_INC_IDX(pwbuff, widx);
+
+ cavium_write_mem(pwbuff->base + OFFSETOF(nw_buf_t, widx),
+ &(pwbuff->widx), sizeof(uint64_t), oct_pci_dev);
+ }
+ }
-#endif /* HOST_TAP */
-
- return;
+ return nread;
+ }

/*
@@ -195,14 +232,19 @@ int main(int argc, char *argv[])
memset(read_buf, 0x0, NW_BUF_SIZE);
memset(write_buf, 0x0, NW_BUF_SIZE);

- host_oct_handshake(oct_pci_dev);
-
- /* Set indices to allow both sides to begin transfer */
- read_buf->rw_idx = write_buf->rw_idx = 1;
-
- cavium_write_mem(WRITE_IDX_PTR,(uint8_t*)write_buf, sizeof(uint64_t), oct_pci_dev);
- cavium_write_mem(READ_IDX_PTR,(uint8_t*)read_buf, sizeof(uint64_t), oct_pci_dev);
+ host_oct_handshake(oct_pci_dev,read_buf,write_buf);

```

```

+ /*Double check: Reset indexes after handshake*/
+ read_buf->ridx = 0;
+ read_buf->widx = 0;
+ write_buf->ridx = 0;
+ write_buf->widx = 0;
+
+ /*Write widx/ridx values to nw_buf_t on PCI BAR*/
+ cavium_write_mem(write_buf->base,
+ &(write_buf->widx),
+ 2*sizeof(uint64_t), oct_pci_dev);
+ cavium_write_mem(read_buf->base, &(read_buf->widx),
+ 2*sizeof(uint64_t), oct_pci_dev);
+ daemon(0,0);

host_oct_data_loop(read_buf, write_buf);
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.h pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/pci-tap.h
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.h 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.h 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
*
* CNNIC SDK
*
- * Copyright (c) 2015 Cavium Networks. All rights reserved.
+ * Copyright (c) 2016 Cavium Networks. All rights reserved.
*
* This file, which is part of the CNNIC SDK which also includes the
* CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -25,7 +25,10 @@
#ifndef _PCI_TAP_H
#define _PCI_TAP_H
#include <syslog.h>
#include <stdio.h>
#include <errno.h>

extern int errno;
#ifndef
TAP_IF_NAME
#define TAP_IF_NAME "tap0"
#endif /* TAP_IF_NAME */
@@ -34,8 +37,7 @@
#define TUN_DEV_FILE "/dev/net/tun"
#endif /* TUN_DEV_FILE */

#define MAX_ETH_FRAM_SZ 1518
-
+#define MAX_ETH_FRAM_SZ 1536
#define cavium_print_msg(...) syslog(LOG_INFO,__VA_ARGS__);

```

```

#ifdef PRINT_DBG
#define cavium_print_dbg(...) syslog(LOG_DEBUG, __VA_ARGS__);
@@ -46,26 +48,52 @@
#define cavium_openlog()
#define SUCCESS 0
#define FAILED 1
+
+// #define OFFSETOF(TYPE, ELE) ((size_t)&(((TYPE*)0)->ELE) - (size_t)&((TYPE *)0))
+#define OFFSETOF(TYPE, ELE) __builtin_offsetof(TYPE, ELE)
+#ifdef HOST_TAP
+#define DESC_BASE_OFF(buf) ((buf->base) + OFFSETOF(nw_buf_t, desc))
+#else
+#define DESC_BASE_OFF(buf) ((uint8_t *)((buf->base) + OFFSETOF(nw_buf_t, desc)))
+#endif
+#define BUF_INC_IDX(buf, idx) ((buf->flag)? (buf->flag)--
:(buf->idx)++)
+#define PREDEC_IDX(idx) ((idx)? idx-1: N_DESC)
+
+
+
+/*
+ * Common definition for network buffer
+ * @param rw_idx, to sync read/write between OCTEON and host
+ * @param len, len of read/write data.
+ * @param data, L2 network Buffer
+ */
-typedef struct nw_buffer
- {
- uint64_t rw_idx;
+typedef struct desc_t
+ {
+ uint64_t len;
+ char data[MAX_ETH_FRAM_SZ];
-} nw_buf_t;
+} desc_t;

+typedef struct nw_buffer
+ {
+ /*First two variables ridx and widx
+ * offsets should not change*/
+ uint64_t widx;
+ uint64_t ridx;
+#ifdef OCT_TAP
+ uint8_t * base;
+#else
+ uint64_t base;
+#endif
+ desc_t desc[N_DESC];

```

```

+ uint64_t flag;
+} nw_buf_t;
#define NW_BUF_SIZE (sizeof(nw_buf_t))

/* Functions */

int host_oct_tun_alloc(char *dev, int flags);
-void host_oct_pci_to_network(void *buf);
-void host_oct_network_to_pci(void * buf, int nread);
+int host_oct_pci_to_network(void *buf);
+int
host_oct_network_to_pci(void * buf, int tun_fd);

/*
 * Wrapper over nanosleep
@@ -81,6 +109,7 @@ static inline void nsleep(long ns)
 nanosleep(&wait, NULL);
}

+
#endif OCT_TAP
#include "oct-tap-defs.h"
#else

```

Found in path(s):

```

* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/OCTEON-PCI-TAP-3.3.5-01_bidirectional.patch

```

No license file was found, but licenses were detected in source scan.

```

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```

```

OCTEON-SDK/env-setup | 4 +++-
1 file changed, 2 insertions(+), 2 deletions(-)

```

```

diff --git a/OCTEON-SDK/env-setup b/OCTEON-SDK/env-setup
index 9c3582b..61ab8f0 100644
--- a/OCTEON-SDK/env-setup
+++ b/OCTEON-SDK/env-setup
@@ -4,16 +4,16 @@
constraints on what can be done directly.

```

```

Check to see if the shell we are running uses 'setenv' or 'export' to
set variables. If the shell uses setenv, then the variable is set with
the value 2, otherwise stderr is redirected to /dev/null
-setenv __OCTEON_SHELL_TEST__ 2>/dev/null
+setenv __OCTEON_SHELL_TEST__ 2>/dev/null || true

```



```
Invoke the env-setup.pl script, which processes the arguments and constructs the correct
environment changes for the shell that it was called from.
eval `./env-setup.pl "$1" "$2" "$3" "$4" "$5" "$6" "$7`
```

```
Clean up test variable so that it is not left around to
confuse subshells
-unsetenv __OCTEON_SHELL_TEST__ 2> /dev/null
+unsetenv __OCTEON_SHELL_TEST__ 2> /dev/null || true
```

```
Return success (otherwise we return an error in bash)
echo -n ""
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-env-setup.patch
```

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```
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```

```
CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c | 3 +++
1 file changed, 3 insertions(+)
```

```
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-drv.c
```

```
index 2878be4..9fcdf51 100644
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
@@ -143,10 +143,13 @@ int cvmcs_pki_reconfig() {
```

```
 printf("Entered pki reconfig\n");
 /*1. Always use cluster group-0. */
 cluster_group =0;
```

```
+ /* FIXME - hack to get rid of a 'set but not used' error */
```

```
+ cluster_mask = cluster_group;
```

```
+
```

```
 /* 2. Assign the clusters to the cluster group -0 */
```

```
 if(OCTEON_IS_MODEL(OCTEON_CN78XX))
 cluster_mask = 0xf;
```

```
 if(OCTEON_IS_MODEL(OCTEON_CN73XX))
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-CNNIC-SDK-SRC-modules-driver-src-firmware-core-cvm-drv.c.patch
```

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```

```
OCTEON-SDK/executive/cvmx.mk | 6 +++++-
1 file changed, 4 insertions(+), 2 deletions(-)
```

```
diff --git a/OCTEON-SDK/executive/cvmx.mk b/OCTEON-SDK/executive/cvmx.mk
index 035b2b5..aae36bb 100644
```

```
--- a/OCTEON-SDK/executive/cvmx.mk
+++ b/OCTEON-SDK/executive/cvmx.mk
@@ -56,17 +56,18 @@ MADE_WITH_OCTEON_MODEL = "runtime"-
$(CVMX_USE_1_TO_1_TLB_MAPPINGS)
else
MADE_WITH_OCTEON_MODEL = $(OCTEON_MODEL)-$(CVMX_USE_1_TO_1_TLB_MAPPINGS)
endif
```

```
set special filename for target and change any spaces in it to commas
-LIB_MADE_WITH = $(shell echo "$(EXEC_OBJ_DIR)/lib-made_with-
OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)=-ISA=${OCTEON_ISA}=-
OCTEON_CPPFLAGS_GLOBAL_ADD=$(OCTEON_CPPFLAGS_GLOBAL_ADD)=." | sed 's\/\/g')
+MADE_WITH_CPPFLAGS_GLOBAL_ADD = $(shell echo "$(OCTEON_CPPFLAGS_GLOBAL_ADD)" | sed
's/-I.*\(\.*\)\/-I...\/g')
+LIB_MADE_WITH = $(shell echo "$(EXEC_OBJ_DIR)/lib-made_with-
OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)=-ISA=${OCTEON_ISA}=-
OCTEON_CPPFLAGS_GLOBAL_ADD=$(MADE_WITH_CPPFLAGS_GLOBAL_ADD)=."
| sed 's\/\/g')
```

```
LIB_MADE_WITH_ALL = $(EXEC_OBJ_DIR)/lib-made_with-*
```

```
$(LIB_MADE_WITH):
mkdir -p $(EXEC_OBJ_DIR)
-rm -f $(LIB_MADE_WITH_ALL)
+ -rm -f $(LIB_MADE_WITH_ALL)
touch "$(LIB_MADE_WITH)"
```

```
component specification
```

```
LIBRARY_CVMX := $(EXEC_OBJ_DIR)/libcvmx.a
```

```
@@ -193,10 +194,11 @@ EXEC_OBJS_$(d) := \
$(EXEC_OBJ_DIR)/cvmx-zip.o \
$(EXEC_OBJ_DIR)/cvmx-zone.o \
$(EXEC_OBJ_DIR)/cvmx-mbox.o \
$(EXEC_OBJ_DIR)/octeon-feature.o \
$(EXEC_OBJ_DIR)/octeon-model.o \
+ $(EXEC_OBJ_DIR)/cvmx-config-init.o \
$(EXEC_OBJ_DIR)/octeon-pci-console.o
```

```
ifeq (linux,$(findstring linux,$(OCTEON_TARGET)))
```

```
EXEC_OBJS_$(d) += \
$(EXEC_OBJ_DIR)/cvmx-app-init-linux.o
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-executive-cvmx-mk.patch
```

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```
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```

```
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```

```
OCTEON-SDK/bootloader/u-boot/Makefile | 1 -
1 files changed, 0 insertions(+), 1 deletions(-)
```

```
diff --git a/OCTEON-SDK/bootloader/u-boot/Makefile b/OCTEON-SDK/bootloader/u-boot/Makefile
index e2d16b5..d8cce23 100644
```

```
--- a/OCTEON-SDK/bootloader/u-boot/Makefile
+++ b/OCTEON-SDK/bootloader/u-boot/Makefile
@@ -907,11 +907,10 @@ clobber: tidy
 @rm -f $(obj)MLO MLO.byteswap
 @rm -f $(obj)SPL
 @rm -f $(obj)tools/xway-swap-bytes
 @rm -f $(obj)arch/powerpc/cpu/mpc824x/bedbug_603e.c
 @rm -f $(obj)arch/powerpc/cpu/mpc83xx/ddr-gen?.c
- @rm -fr $(obj)include/asm/proc $(obj)include/asm/arch $(obj)include/asm
 @rm -fr $(obj)include/asm/proc
ifneq ($(CPU),octeon)
 @rm -fr $(obj)include/asm/arch $(obj)include/asm
endif
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
OCTEON-SDK-bootloader-u-boot-Makefile.patch
```

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```

```
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```

```
OCTEON-SDK/bootloader/emmc-boot/tools/Makefile | 11 ++++++-----
OCTEON-SDK/host/coredump-util/Makefile | 9 ++++++---
OCTEON-SDK/host/debug-agent/Makefile | 9 ++++++---
OCTEON-SDK/host/remote-utils/Makefile | 6 +++++-
4 files changed, 23 insertions(+), 12 deletions(-)
```

```
diff --git a/OCTEON-SDK/bootloader/emmc-boot/tools/Makefile b/OCTEON-SDK/bootloader/emmc-
boot/tools/Makefile
```

```
index da570c4..0fcc1b7 100644
```

```
--- a/OCTEON-SDK/bootloader/emmc-boot/tools/Makefile
```

```
+++ b/OCTEON-SDK/bootloader/emmc-boot/tools/Makefile
@@ -2,20 +2,23 @@ CFLAGS = -g -O0 -W -Wall -Wno-unused-parameter -I$(OCTEON_ROOT)/executive
LDFLAGS = -O2 -lz
```

```
.PHONY: all
```

```
all: write_stage1 translate_env
```

```
-- sudo cp -pf $^ $(OCTEON_ROOT)/host/bin
+#CISCO_MODIFICATION - commented out
+# - sudo cp -pf $^ $(OCTEON_ROOT)/host/bin
```

```
write_stage1: write_stage1.o
```

```
$(CC) $(LDFLAGS) -o $@ $?
```

```
-- sudo chown -f root $@
-- sudo chmod -f +s,g+r,o+r $@
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $@
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $@
```

```
translate_env: translate_env.o
```

```
$(CC) $(LDFLAGS) -o $@ $?
```

```
%.o: %.c
```

```
$(CC) $(CFLAGS) -c $< -o $@
```

```
clean:
```

```
- rm -f write_stage1 *.o
```

```
\ No newline at end of file
```

```
+ rm -f write_stage1 *.o
```

```
diff --git a/OCTEON-SDK/host/coredump-util/Makefile b/OCTEON-SDK/host/coredump-util/Makefile
index ebe04f5..f386adc 100644
```

```
--- a/OCTEON-SDK/host/coredump-util/Makefile
```

```
+++ b/OCTEON-SDK/host/coredump-util/Makefile
```

```
@@ -31,14 +31,17 @@ STRIP=$(CROSS)strip
```

```
BINS=$(DIR)/oct-remote-coredump
```

```
all: $(BINS) $(LIBS)
```

```
- $(STRIP) $(BINS)
```

```
-- sudo chown -f root $(BINS)
```

```
-- sudo chmod -f +s,g+r,o+r $(BINS)
```

```
+#CISCO_MODIFICATION - commented out
```

```
+# - sudo chown -f root $(BINS)
```

```
+#CISCO_MODIFICATION - commented out
```

```
+# - sudo chmod -f +s,g+r,o+r $(BINS)
```

```
$(DIR)/oct-remote-coredump:
```

```

oct-remote-coredump.c oct-remote-coredump-elf.c $(LIBS)
$(CC) $(CFLAGS) -o $@ $^ $(LIBS) $(LIBS_FLAGS)

clean:
- - sudo rm -f $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo rm -f $(BINS)
- rm -f $(BINS)
diff --git a/OCTEON-SDK/host/debug-agent/Makefile b/OCTEON-SDK/host/debug-agent/Makefile
index 40726c5..63dad9a 100644
--- a/OCTEON-SDK/host/debug-agent/Makefile
+++ b/OCTEON-SDK/host/debug-agent/Makefile
@@ -28,14 +28,17 @@ STRIP=$(CROSS)strip

BINS=$(DIR)/oct-debug-agent

all: $(BINS) $(LIBS)
- $(STRIP) $(BINS)
- - sudo chown -f root $(BINS)
- - sudo chmod -f +s,g+r,o+r $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $(BINS)

$(DIR)/oct-debug-agent: agent.c proxy.c remote.c $(LIBS)
$(CC) $(CFLAGS) -o $@ $^ $(LIBS) $(LIBS_FLAGS)

clean:
- - sudo rm -f $(DIR)/oct-debug-agent
+#CISCO_MODIFICATION - commented out
+# - sudo rm -f $(DIR)/oct-debug-agent
- rm -f $(DIR)/oct-debug-agent
diff
--git a/OCTEON-SDK/host/remote-utils/Makefile b/OCTEON-SDK/host/remote-utils/Makefile
index 70a73e0..4935119 100644
--- a/OCTEON-SDK/host/remote-utils/Makefile
+++ b/OCTEON-SDK/host/remote-utils/Makefile
@@ -56,13 +56,15 @@ BINS = \
$(DIR)/oct-remote-mmc

.PHONY: all
all: $(BINS)
- $(STRIP) $(BINS)
- - sudo chown -f root $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $(BINS)
ifndef OCTEON_LIQUIDIO

```

```
-- sudo chmod -f +s,g+r,o+r $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $(BINS)
endif
```

```
CVMX_COMMON_FILES = $(OCTEON_ROOT)/executive/cvmx-clock.c \
$(OCTEON_ROOT)/executive/cvmx-l2c.c \
$(OCTEON_ROOT)/executive/cvmx-twsi.c \
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/0001-Removed-unnecessary-use-of-sudo-from-makefiles.patch
```

No license file was found, but licenses were detected in source scan.

```
Copyright (c) 2016 by Cisco Systems, Inc.
```

```
All rights reserved.
```

```
OCTEON-SDK/host/remote-utils/Makefile | 3 +++
```

```
1 files changed, 3 insertions(+), 0 deletions(-)
```

```
diff --git a/OCTEON-SDK/host/remote-utils/Makefile b/OCTEON-SDK/host/remote-utils/Makefile
index eb47e32..08ebb01 100644
```

```
--- a/OCTEON-SDK/host/remote-utils/Makefile
```

```
+++ b/OCTEON-SDK/host/remote-utils/Makefile
```

```
@@ -78,10 +78,13 @@ CVMX_FILES = $(OCTEON_ROOT)/executive/cvmx-csr-db.c \
$(OCTEON_ROOT)/executive/cvmx-qlm.c \
$(OCTEON_ROOT)/executive/cvmx-qlm-tables.c \
$(OCTEON_ROOT)/executive/cvmx-srio.c \
$(CVMX_COMMON_FILES)
```

```
+$$(LIBS):
```

```
+ $(MAKE) -C ../remote-lib all
```

```
+
```

```
$(DIR)/oct-remote-boot: $(LIBS) oct-remote-boot.c crc32.c $(BOOTLOADER_FILES)/lib_octeon_shared.c
```

```
$(CVMX_COMMON_FILES) oct-remote-common.c
```

```
$(CC) $(CFLAGS) -o $$@ $$^ $(LIBS) $(LIBS_FLAGS)
```

```
$(DIR)/oct-remote-bootcmd: $(LIBS) oct-remote-bootcmd.c oct-remote-common.c
```

```
$(CC)
```

```
$(CFLAGS) -o $$@ $$^ $(LIBS) $(LIBS_FLAGS)
```

```
$(DIR)/oct-remote-console: $(LIBS) oct-remote-console.c
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-OCTEON-SDK-host-remote-utils-Makefile-remote-lib-target.patch
```

No license file was found, but licenses were detected in source scan.

```
Copyright (c) 2016 by Cisco Systems, Inc.
```

```
All rights reserved.
```

OCTEON-SDK/executive/cvmx-platform.h | 2 +-  
1 file changed, 1 insertion(+), 1 deletion(-)

diff --git a/OCTEON-SDK/executive/cvmx-platform.h b/OCTEON-SDK/executive/cvmx-platform.h  
index 173f400..10f76bc 100644

--- a/OCTEON-SDK/executive/cvmx-platform.h

+++ b/OCTEON-SDK/executive/cvmx-platform.h

@@ -111,11 +111,11 @@

\* section that the loader will map as shared for all cores

\* This is for data structures use by software ONLY,

\* as it is not 1-1 VA-PA mapped.

\*/

#if !defined(CVMX\_BUILD\_FOR\_LINUX\_HOST) && !defined(CVMX\_BUILD\_FOR\_UBOOT)

+#define CVMX\_SHARED \_\_attribute\_\_((cvmx\_shared))

+#define CVMX\_SHARED

#else

#define CVMX\_SHARED

#endif

/\* Thread local storage , to have a global variable be per core

Found in path(s):

\* /opt/cola/permits/1588488807\_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-

OCTEON-SDK-executive-cvmx-platform.h.patch

No license file was found, but licenses were detected in source scan.

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diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c

--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2019-03-18

17:56:09.872534277 -0400

+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2019-03-19

13:45:15.897107069 -0400

@@ -516,11 +516,10 @@

if(OCTEON\_IS\_MODEL(OCTEON\_CN56XX))

scratch\_reg\_addr = CVMX\_PEXP\_NPEI\_SCRATCH\_1;

CFG\_GET\_APP\_MODE(&oct\_cfg) = app\_mode;

/\* 1: Send App Mode to Host and wait for acknowledgement. \*/

- printf("Send App Mode to Host\n");

scratch\_val = app\_mode << 16 | HOSTFW\_HS\_APP\_INDICATION;

scratch\_val = scratch\_val | ((uint64\_t)(hs\_support ? HOSTFW\_HS\_SUPPORT\_INDICATION : 0) << 48);

cvmx\_write\_csr\_node(node, scratch\_reg\_addr, scratch\_val);

while( (scratch\_val & 0xffff) != HOSTFW\_HS\_ACK)

```

@@ -533,11 +532,10 @@
while(counter--)
{
 block_ptr = cvmx_bootmem_alloc_named(BLOCK_SIZE, NB_4MB_ALLIGNED, BLOCK_NAME);
 if (block_ptr && !(CVM_DRV_GET_PHYS(block_ptr) & (NB_4MB_ALLIGNED - 1)))
 {
- printf(" NB Allocated with Physical Address : %016llx\n", CVM_DRV_GET_PHYS(block_ptr));
 break;
 }

 if (block_ptr)
 cvmx_bootmem_free_named(BLOCK_NAME);
@@ -554,18 +552,12 @@
/* 3: write the physical address of buffer to scratch register.*/
num_if = cvmx_helper_get_number_of_interfaces();
phys_addr = CVM_DRV_GET_PHYS(block_ptr);
scratch_val = (phys_addr << 16) | HOSTFW_HS_NB_BLOCK_INDICATION;

- printf(" num_cores: %u, num intf: %lu, NB Buff Phys Addr: %016lx \n",
- num_cores, num_if, phys_addr);
- printf(" Sending NB Phys Addr to host:%016lx \n", scratch_val);
-
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

- printf(" Waiting for cfg details
from the core.\n");
-
/* 4: Wait for host to send cfg details. */
while(cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr) != HOSTFW_HS_ACK);

/* 5: Rad the cfg details */
@@ -587,22 +579,16 @@

support_extended_stats = CFG_GET_EXTENDED_STATS(&oct_cfg);
fw_dmac_filter = CFG_GET_FW_DMACHFLT_STATUS(&oct_cfg);
enable_mtu_vlan = CFG_GET_ENABLE_MTU_VLAN(&oct_cfg);

- printf("Extended stats support %s.\n", support_extended_stats?"Enabled":"Disabled");
- printf("Firmware DMAC filter %s.\n", fw_dmac_filter?"Enabled":"Disabled");
- printf("Allocate Buffers for Control Requests: %s\n", setup_ctl_bufs?"Enabled":"Disabled");
- printf("VLAN MTU:%d\n", enable_mtu_vlan);
-
/* 6: Send HS completion indication to host */
scratch_val = (num_if & 0xff) << 24 | (num_cores & 0xff) << 16 | HOSTFW_HS_READ_DONE_INDICATION
;
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

```



```

/* wait for ACK from HOST */
while(cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr)!=
HOSTFW_HS_ACK);
- printf(" Host Firmware Handshake Completed : \n");

/* Free the Allocated NB */
cvmx_bootmem_free_named(BLOCK_NAME);

/** Allocate the memory for required host pci function info. */
@@ -661,17 +647,15 @@
 pkind = cvmx_helper_get_pknd(interface, index);

 CFG_GET_DPI_PKIND(&oct_cfg) = pkind;
 /*Higher 32 bits contains pkind value and lower 32 bits contains Indication*/
 scratch_val = (pkind << 16) | HOSTFW_HS_PKIND_INDICATION;
- printf("DBG: Pkind for dpi port is: 0x%016lx\n", pkind);

 cvmx_write_csr_node(node, scratch_reg_addr, scratch_val);

/* wait for ACK from Host */
while(cvmx_read_csr_node(node, scratch_reg_addr) != HOSTFW_HS_ACK);
- printf("Received ACK from Host for Pkind value.\n");
}

/* Send core clk, co-proc clk and Active indication to Host */
if(OCTEON_IS_MODEL(OCTEON_CN78XX) || OCTEON_IS_MODEL(OCTEON_CN73XX) ||
 OCTEON_IS_MODEL(OCTEON_CN70XX)
|| OCTEON_IS_MODEL(OCTEON_CN71XX))
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c 2019-03-18 17:56:09.872534277 -
0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c 2019-03-18 18:49:58.437223960
-0400
@@ -742,11 +742,10 @@
 /* wait for ACK from Host */
 while(scratch_val != HOSTFW_HS_ACK)
 {
 scratch_val = cvmx_read_csr_node(node, scratch_reg_addr);
 }
- cvmcs_print("# cvmcs-ioq: Received ACK from Host for Pkind value.\n");

 return;
}

@@ -865,11 +864,10 @@
 while(scratch_val != HOSTFW_HS_PKO_READY_ACK)

```

```

 {
 cvmx_wait(500);
 scratch_val = cvmx_read_csr_node(node, scratch_reg_addr);
 }
- cvmcs_print("# cvmcs-ioq: PKO ready.\n");

 return;
}

/*
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c 2019-03-18 17:56:09.867534215 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c 2019-03-19 11:32:01.175748226 -0400
@@ -139,11 +139,10 @@
 cvmx_pki_clx_pkindx_cfg_t pkind_cfg_reg;
 cvmx_pki_stylex_buf_t style_buf_reg;

 node = cvmx_get_node_num();

- printf("Entered pki reconfig\n");
 /*1. Always use cluster group-0. */
 cluster_group =0;

 /* FIXME - hack to get rid of a 'set but not used' error */
 cluster_mask = cluster_group;
@@ -200,33 +199,28 @@
 pkind_cfg_style.u64 = cvmx_read_csr_node(node, CVMX_PKI_CLX_PKINDX_STYLE(pkind, cluster_idx));
 style = pkind_cfg_style.s.style;

 /* Set SW bit in the CVMX_PKI_STYLE[style]_WQ2 Register */
 word2 = cvmx_read_csr_node(node, CVMX_PKI_STYLEX_WQ2(style));
- printf("PKI_STYLE[%d]_WQ2[sw] - Before : 0x%016lx\n", style, word2);
-#if 0
 0
 word2 = word2 | 0x8000000000000000; /* set the SW bit for the packets coming from host */
+#if 0
 /* write Word-2 of WQE to the STYLEn register with s/w bit set to 1 */
 cvmx_write_csr_node(node, CVMX_PKI_STYLEX_WQ2(style), word2);
#endif
 word2 = cvmx_read_csr_node(node, CVMX_PKI_STYLEX_WQ2(style));
- printf("PKI_STYLE[%d]_WQ2[sw] - After : 0x%016lx \n\n", style, word2);

 /* Enable this if sharing the first buffer for WQE and Packet data */
 /* Configure the first skip */
 style_buf_reg.u64 = cvmx_read_csr_node(node, CVMX_PKI_STYLEX_BUF(style));
- printf("PKI_STYLE[%d]_BUF[first_skip] - Before : 0x%016lx\n", style, style_buf_reg.u64);
-#if 0

```

```

/* Set the FIRST_SKIP = 0x5 in CVMX_PKI_STYLE[style]_BUF - The space for WQE first 5 words */
/* Space of Five 8B words for WQE words */
style_buf_reg.s.first_skip = 0x5;
/* Use separate buffers for WQE and Packet Data */
style_buf_reg.s.dis_wq_dat = 0x1;
#endif
cvmx_write_csr_node(node,
CVMX_PKI_STYLEX_BUF(style), style_buf_reg.u64);
- printf("PKI_STYLE[%d]_BUF[first_skip] - After : 0x%016lx \n\n", style,
- cvmx_read_csr_node(node, CVMX_PKI_STYLEX_BUF(style)));

/* For 73XX DPI Has 2 Pkinds */
if(OCTEON_IS_MODEL(OCTEON_CN73XX) && index == 0) {
 index = 1;
 goto CFG_73XX;
@@ -243,17 +237,15 @@

int
cvm_drv_init()
{

- printf("[DRV] Driver Initialization\n");
if(OCTEON_IS_MODEL(OCTEON_CN56XX_PASS1)) {
 printf("[DRV] CN56XX Pass1 is not supported\n");
 return -1;
}

- printf("[DRV] Allocating memory for octeon device\n");
oct = cvmx_drv_fpa_alloc_sync(CVMX_FPA_PACKET_POOL);
if(oct == NULL) {
 printf("[DRV] octeon device alloc failed\n");
 return -1;
}
@@ -298,11 +290,10 @@
__cn68xx_qlm_gen2_fix();

if(setup_pci_pko_ports())
 return -1;
- printf("[DRV] PKO Done\n");

/* To indicate a RAW mode packet in WQE word2 */
if (OCTEON_IS_MODEL(OCTEON_CN78XX) || OCTEON_IS_MODEL(OCTEON_CN73XX))
{
 cvmcs_pki_reconfig();

@@ -399,17 +390,15 @@

```

```

 }
#endif
#endif
- printf("----- PKI Registers Configuration : End -----\n\n");

}
else
 cvmx_write_csr(CVMX_PIP_RAW_WORD, cvmx_read_csr(CVMX_PIP_RAW_WORD)|0x8000);

- printf("[DRV] Setting up DMA Engines\n");
if(cvm_dma_queue_init(oct)) {
 printf("[DRV] DMA Queue initialization failed\n");
 return -1;
}

diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-dma.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-dma.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-dma.c 2019-03-18 17:56:09.867534215 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-dma.c 2019-03-18 18:36:00.876939905 -0400
@@ -541,17 +541,10 @@
 for(i = 0; i < 6; i++) {
 cvmx_write_csr_node(node, CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
 }

 cvmx_write_csr_node(node, CVMX_DPI_CTL, 1);
-
- printf("DPI_DMA_CONTROL:
0x%016lx\n", cvmx_read_csr_node(node, CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr_node(node,
CVMX_DPI_ENGX_BUF(i)));
- }
-
}

/** OCTEON III models DMA Engines set up.
@@ -616,17 +609,10 @@
for(i = 0; i < 6; i++) {
 cvmx_write_csr_node(node, CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
}

cvmx_write_csr_node(node, CVMX_DPI_CTL, 1);
-
- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr_node(node, CVMX_DPI_DMA_CONTROL));

```

```

-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr_node(node, CVMX_DPI_ENGX_BUF(i)));
- }
-
}

```

static void

cvm\_enable\_cn70xx\_dma(cvm\_oct\_dev\_t \*oct)

@@ -670,18 +656,10 @@

cvmx\_write\_csr(CVMX\_DPI\_DMA\_CONTROL, dma\_ctl.u64);

```

for(i = 0; i < 6; i++) {

```

```

 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);

```

```

}

```

-

-

```

- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));

```

-

```

- for(i

```

```

= 0; i < 6; i++) {

```

```

- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));

```

```

- }

```

-

```

}

```

/\* Enable this flag if only the DMA engines in CN63XX are used and the

DPI packet Input/Output will not be used.

@@ -728,17 +706,10 @@

```

for(i = 0; i < 6; i++) {

```

```

 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);

```

```

}

```

```

cvmx_write_csr(CVMX_DPI_CTL, 1);

```

-

```

- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));

```

-

```

- for(i = 0; i < 6; i++) {

```

```

- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));

```

```

- }

```

-

```

}

```

static void

cvm\_enable\_cn66xx\_dma(cvm\_oct\_dev\_t \*oct)

```

@@ -775,18 +746,10 @@
cvmx_write_csr(CVMX_DPI_DMA_CONTROL, dma_ctl.u64);

for(i = 0; i < 6; i++) {
 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
}
-
-
- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));
- }
-
- }

```

```

@@ -832,17 +795,10 @@
for(i = 0; i < 6; i++) {
 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
}

cvmx_write_csr(CVMX_DPI_CTL, 1);
-
- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));
- }
-
- }

```

```

@@ -1069,12 +1025,10 @@
if(oct->chunk[i] == NULL) {
 printf("[DRV] Chunk alloc failed for DMA Queue[%d]\n", i);
 cvm_free_dma_queue(oct);
 return -ENOMEM;
}
- printf("[DRV] DMA Q[%d]: Chunk @ %p size: %d max_words: %d\n", i,
- oct->chunk[i], oct->chunk[i]->chunk_size, oct->chunk[i]->max_words);

if(cvm_dma_chip_specific_setup(oct, i))
 return -EINVAL;

```

```
}
```

```
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c 2019-03-18
```

```
17:56:09.868534227 -0400
```

```
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c 2019-03-18 18:29:05.857842039 -0400
```

```
@@ -239,12 +239,10 @@
```

```
void
```

```
cvm_setup_pci_load_store()
```

```
{
```

```
- printf("%s: Setting up registers for PCI load/store ops\n", __FUNCTION__);
```

```
-
```

```
if(OCTEON_IS_MODEL(OCTEON_CN73XX))
```

```
 cn73xx_setup_pci_load_store();
```

```
if(OCTEON_IS_MODEL(OCTEON_CN78XX))
```

```
 cn78xx_setup_pci_load_store();
```

```
@@ -261,12 +259,10 @@
```

```
if(OCTEON_IS_MODEL(OCTEON_CN56XX))
```

```
 cn56xx_setup_pci_load_store();
```

```
if(OCTEON_IS_MODEL(OCTEON_CN58XX) || OCTEON_IS_MODEL(OCTEON_CN38XX))
```

```
 cn3xxx_setup_pci_load_store();
```

```
-
```

```
- printf("[DRV] subdid base is 0x%lx\n", pci_mem_subdid_base);
```

```
}
```

```
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c 2019-03-18 17:56:09.868534227 -0400
```

```
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c 2019-03-19
```

```
13:42:32.055133736 -0400
```

```
@@ -77,11 +77,10 @@
```

```
if(OCTEON_IS_MODEL(OCTEON_CN73XX))
```

```
{
```

```
 if(max_droq){
```

```
 activeqcnt = max_droq;
```

```
 size = sizeof(struct __cvm_pci_pko_qmap) * max_droq;
```

```
- printf("[DRV] Active CN73xx PCI Queues: %d \n", activeqcnt);
```

```
}
```

```
else {
```

```

 printf("[DRV] Active CN73xx PCI Queue Count is Zero\n");
 return -1;
}
@@ -93,12 +92,10 @@
if(max_droq)
 activeqcnt = max_droq;
else
 activeqcnt = __get_active_pci_oq_count();

- printf("[DRV] Active CN78xx PCI Queues: %d (derived from checking queue registers)\n", activeqcnt);
-
}
else if(OCTEON_IS_MODEL(OCTEON_CN68XX))
{
 cvmx_sli_tx_pipe_t slitxpipe;
 slitxpipe.u64 = cvmx_read_csr(CVMX_PEXP_SLI_TX_PIPE);
@@ -108,11 +105,10 @@
 else
 activeqcnt = __get_active_pci_oq_count();

 slitxpipe.s.nump = activeqcnt;
 cvmx_write_csr(CVMX_PEXP_SLI_TX_PIPE, slitxpipe.u64);
- printf("[DRV] Active CN68xx PCI Queues: %d (derived
from checking queue registers)\n", activeqcnt);
}
else
{

if(max_droq)

```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/remove-excessive-boottime-console-output.patch
```

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OCTEON-SDK/executive/cvmx-app-init-linux.c | 4 +++-

1 file changed, 3 insertions(+), 1 deletion(-)

```
diff --git a/OCTEON-SDK/executive/cvmx-app-init-linux.c b/OCTEON-SDK/executive/cvmx-app-init-linux.c
```

```
index 30da977..dbbfd1 100644
```

```
--- a/OCTEON-SDK/executive/cvmx-app-init-linux.c
```

```
+++ b/OCTEON-SDK/executive/cvmx-app-init-linux.c
```

```
@@ -54,11 +54,11 @@
```

```
* protections are circumvented. If you do something bad, Linux won't
```

```
* save you.
```



```

* -# Most hardware can only be initialized once. Unless you're very careful,
* this also means you Linux application can only run once.
*
- * <hr>$Revision: 107404 $<hr>
+ * <hr>$Revision: #1 $<hr>
*
*/
#define _GNU_SOURCE
#include <stdint.h>
#include <stdio.h>
@@ -158,10 +158,11 @@ void simprintf(const char *format, ...)
vprintf(format, ap);
va_end(ap);
cvmx_spinlock_unlock(&simprintf_lock);
}

+#if 0
/**
 * Setup the CVMX_SHARED data section to be shared across
 * all processors running this application. A memory mapped
 * region is allocated using shm_open and mmap. The current
 * contents of the CVMX_SHARED section are copied into the
@@ -408,5 +409,6 @@ int main(int argc, const char *argv[])
}
}

return result;
}
+#endif

```

Found in path(s):

```

* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-executive-cvmx-app-init-linux.c.patch

```

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```

OCTEON-SDK/application.mk | 7 +++++-
1 file changed, 4 insertions(+), 3 deletions(-)

```

```

diff --git a/OCTEON-SDK/application.mk b/OCTEON-SDK/application.mk

```

```

index 4e91c40..7b69d89 100644

```

```

--- a/OCTEON-SDK/application.mk

```

```

+++ b/OCTEON-SDK/application.mk

```

```

@@ -76,11 +76,12 @@ ifeq (${MATCH}, DUSE_RUNTIME_MODEL_CHECKS=1)

```

```

MADE_WITH_OCTEON_MODEL = "runtime"-${CVMX_USE_1_TO_1_TLB_MAPPINGS}

```

```

else

```

```

MADE_WITH_OCTEON_MODEL = $(OCTEON_MODEL)-$(CVMX_USE_1_TO_1_TLB_MAPPINGS)
endif
set special filename for target and change any spaces in it to commas
-MADE_WITH = $(shell echo "$(OBJ_DIR)/made_with-
OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)=-ISA=${OCTEON_ISA}=-
OCTEON_CPPFLAGS_GLOBAL_ADD=$(OCTEON_CPPFLAGS_GLOBAL_ADD)=." | sed 's\/\ /,g')
+MADE_WITH_CPPFLAGS_GLOBAL_ADD = $(shell echo "$(OCTEON_CPPFLAGS_GLOBAL_ADD)" | sed
's/-I.*\(\.*\)\/-I...1/g')
+MADE_WITH = $(shell
echo "$(OBJ_DIR)/made_with-OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)=-
ISA=${OCTEON_ISA}=-
OCTEON_CPPFLAGS_GLOBAL_ADD=$(MADE_WITH_CPPFLAGS_GLOBAL_ADD)=." | sed 's\/\ /,g')

MADE_WITH_ALL = $(OBJ_DIR)/made_with-*

MADE_WITH_DIR = $(subst /,-,.cstamp-$(OBJ_DIR))
MADE_WITH_DIR_ALL = .cstamp-*
@@ -101,15 +102,15 @@ endif

$(MADE_WITH):
mkdir -p $(OBJ_DIR)
-rm -f $(MADE_WITH_ALL)
+ -rm -f $(MADE_WITH_ALL)
touch "$(MADE_WITH)"

$(MADE_WITH_DIR):
-rm -f $(MADE_WITH_DIR_ALL)
+ -rm -f $(MADE_WITH_DIR_ALL)
touch $(MADE_WITH_DIR)

$(OBJ_DIR)/cvmx-config-init.o: $(OCTEON_ROOT)/executive/cvmx-config-init.c
$(CC) $(CFLAGS_CONFIG_INIT) $(CFLAGS_GLOBAL) $(CFLAGS_LOCAL) -MMD -c -o $@ $<

Found in path(s):
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-application-mk.patch
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diff -aur a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c

--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2017-03-28
12:52:41.619920142 -0400

```

+++ a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2017-03-28

12:52:04.561296273 -0400

@@ -567,9 +567,11 @@

```
/* 5: Rad the cfg details */
memcpy(&oct_cfg,block_ptr,sizeof(oct_cfg));
+ /* Remove output to limit exposure */
+#ifdef CAVIUM_DEBUG
printf(" Received Firmware Configuration From Host(oct_cfg size: %lu) \n", sizeof(oct_cfg));
-
 cvmcs_dump_hostfw_config();
+#endif
```

```
ctrlq_num = CFG_GET_CTRL_Q_NO(&oct_cfg);
link_query_interval = CFG_GET_OCT_LINK_QUERY_INTERVAL(&oct_cfg);
```

Found in path(s):

\* /opt/cola/permits/1588488807\_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/suppress-hostfw-config-dump.patch

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CNNIC-SDK-SRC/modules/pci-tap/Makefile | 4 +++-

1 file changed, 2 insertions(+), 2 deletions(-)

diff --git a/CNNIC-SDK-SRC/modules/pci-tap/Makefile b/CNNIC-SDK-SRC/modules/pci-tap/Makefile

index f92ff25..ef34d48 100755

--- a/CNNIC-SDK-SRC/modules/pci-tap/Makefile

+++ b/CNNIC-SDK-SRC/modules/pci-tap/Makefile

@@ -39,12 +39,12 @@ EXTRA\_CFLAGS = \$(CFLAGS\_LOCAL) -DGPIO\_CIU3\_BASE\_IRQ=1

export NUMBER\_OF\_BAR\_DESC=1

OCT\_PCI\_TAP=pci-tap

HOST\_TAP\_BIN=host-pci-tap

all: hostapp octapp

-hostapp: CC=gcc

-hostapp: AR=ar

+hostapp: CC=\$(TARGET\_PREFIX)gcc

+hostapp: AR?=\$(TARGET\_PREFIX)ar

hostapp: EXTRA\_CFLAGS += -Wno-error=declaration-after-statement -

DN\_DESC=\$(NUMBER\_OF\_BAR\_DESC)

hostapp: EXTRA\_LDFLAGS = -L \$(CNNIC\_ROOT)/modules/pci-tap/ -lpthread -lcutils

hostapp: \$(OCT\_PCI\_TAP)

cnutils-link:

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-CNNIC-SDK-SRC-modules-pci-tap-Makefile.patch
```

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```
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```

```
All rights reserved.
```

```
diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h.orig octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h
```

```
--- octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h.orig 2016-08-10 21:58:14.872582801 -0400
```

```
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h 2016-08-10 21:59:52.281988152 -0400
```

```
@@ -72,15 +72,26 @@ uint64_t user_drv_pci_get_scratchreg(voi
```

```
* The function waits indefinitely for OCTEON to respond otherwise.
```

```
*/
```

```
-static void host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
```

```
+static int host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
```

```
{
```

```
 uint64_t scratch_reg_addr, scratch_val = 0;
```

```
+ time_t t0;
```

```
+
```

```
 scratch_reg_addr = user_drv_pci_get_scratchreg(user_dev);
```

```
 /* Wait for OCTEON to to send indication
```

```
of handshake start */
```

```
+ t0 = time(NULL);
```

```
 do
```

```
 {
```

```
 sleep(1);
```

```
+ if (time(NULL) > t0 + HS_TIMEOUT)
```

```
+ {
```

```
+ syslog(LOG_INFO, "Timed out waiting for handshake to start.");
```

```
+ return -1;
```

```
+ }
```

```
+ else if (killed)
```

```
+ return -1;
```

```
+
```

```
 scratch_val = user_drv_pci_read_csr(scratch_reg_addr, user_dev);
```

```
 } while(scratch_val != HOSTFW_HS_IND);
```

```
@@ -91,8 +102,20 @@ static void host_oct_handshake(void *us
```

```
 user_drv_pci_write_csr(scratch_reg_addr, scratch_val, user_dev);
```

```
 /* Loop until OCTEON writes the address */
```

```
+ t0 = time(NULL);
```

```
 while(scratch_val == HOSTFW_HS_ACK)
```

```

+ {
+ usleep(1000);
+ if (time(NULL) > t0 + HS_TIMEOUT)
+ {
+ syslog(LOG_INFO, "Timed out waiting for handshake to complete.\n");
+ return -1;
+ }
+ else if (killed)
+ return -1;
+
+ scratch_val = user_drv_pci_read_csr(scratch_reg_addr, user_dev);
+ }

/*Host read follows host write*/
write_buf->base = scratch_val;
@@ -103,7 +126,7 @@ static void host_oct_handshake(void *us
 user_drv_pci_write_csr(scratch_reg_addr, scratch_val, user_dev);

 cavium_print_msg("Host-Octeon Handshake completed\n");
- return;
+ return 0;
}

static int host_wait_on_fds()
@@ -123,8 +146,8 @@ static int host_wait_on_fds()
 cavium_print_msg("Select faied with error %d\n", errno);
 return 0;
}
- return 1;

+ return ret;
}

/*
@@ -187,26 +210,38 @@ static inline int host_oct_init()
*/
static void host_oct_data_loop(struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
- while (1)
+ int ret = 0, ret2 = 0;
+ int idle_count = 0;
+
+ while (!killed)
 {
-
- if(host_wait_on_fds())
+ if((ret = host_wait_on_fds()))

```

```

 {
- if (FD_ISSET(tun_fd, &readfds))
- {
- if(host_oct_network_to_pci(write_buf, tun_fd)){
- /* Inform Octeon
*/
- host_interrupt_octeon(oct_pci_dev);
+ if (FD_ISSET(tun_fd, &readfds))
+ {
+ if((ret = host_oct_network_to_pci(write_buf, tun_fd)) > 0){
+ #ifdef USE_GPIO
+ /* Inform Octeon */
+ host_interrupt_octeon(oct_pci_dev);
+ #endif
+ }
+ else if (ret < 0)
+ break;
+ }
- }
 }

 /* Rx: Read data from OCTEON memory and write to tap */
- host_oct_pci_to_network(read_buf);
+ ret2 = host_oct_pci_to_network(read_buf);

+ if (ret == 0 && ret2 == 0)
+ {
+ idle_count ++;
+ if (idle_count ++ > 1000)
+ usleep(IDLE_SLEEP);
+ }
+ else
+ idle_count = 0;
 }
-
- user_drv_pci_close(oct_pci_dev);
}

#endif /* _HOST_TAP_DEFS_H */
diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/Makefile.orig octeon-sdk/CNNIC-SDK-SRC/modules/pci-
tap/Makefile

octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/Makefile.orig 2016-08-10 21:58:14.873582795 -0400
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/Makefile 2016-08-10 21:59:55.418969002 -0400
@@ -35,7 +35,7 @@ CFLAGS_LOCAL = -O2 -W -Wall -Werror -Wno

EXTRA_CFLAGS = $(CFLAGS_LOCAL) -DGPIO_CIU3_BASE_IRQ=1

```

```
-export NUMBER_OF_BAR_DESC=1
+export NUMBER_OF_BAR_DESC=16
OCT_PCI_TAP=pci-tap
HOST_TAP_BIN=host-pci-tap
```

```
diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h.orig octeon-sdk/CNNIC-SDK-
SRC/modules/pci-tap/oct-tap-defs.h
```

```
--- octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h.orig 2016-08-10 21:58:14.874582789 -0400
```

```
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h 2016-08-10 21:59:52.320987914 -0400
```

```
@@ -79,11 +79,13 @@ void *oct_pci_dev = NULL;
```

```
* return Setup Read and write addresses for Host and return.
```

```
* The function waits indefinitely for HOST to respond otherwise.
```

```
*/
```

```
-static void
```

```
-host_oct_handshake(void *user_dev, struct nw_buffer
```

```
*read_buf, struct nw_buffer *write_buf)
```

```
+static int host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
```

```
{
```

```
+ time_t t0;
```

```
uint64_t read_idx_addr, scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1, scratch_val;
```

```
const cvmx_bootmem_named_block_desc_t *desc;
```

```
+
```

```
+#ifdef USE_GPIO
```

```
scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
```

```
CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
```

```
scratch_val |= 0x1; /* Enable Output bit */
```

```
@@ -96,30 +98,60 @@ host_oct_handshake(void *user_dev, struct
```

```
scratch_val |= 0x1;
```

```
cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET, scratch_val);
```

```
}
```

```
+#endif
```

```
/* Handshake */
```

```
scratch_val = HOSTFW_HS_IND;
```

```
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);
```

```
/* Wait for acknowledgement from host */
```

```
+ t0 = time(NULL);
```

```
while(scratch_val != HOSTFW_HS_ACK)
```

```
+ {
```

```
+ usleep(10000);
```

```
+ if (time(NULL) > t0 + HS_TIMEOUT)
```

```
+
```

```
{
```

```
+ syslog(LOG_INFO, "Timed out waiting for handshake ack.");
```

```
+ return -1;
```

```
+ }
```

```

+ else if (killed)
+ return -1;
+
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr);
+ }

/* Fetch the named block allocated for pci-tap buffers*/
desc = cvmx_bootmem_find_named_block(BLOCK_NAME);

- if(!desc)
+ if(!desc || desc->size != N_DESC*BLOCK_SIZE)
 {
+ if (desc && desc->size != N_DESC*BLOCK_SIZE)
+ {
+ syslog(LOG_INFO, "Memory block size mismatch: have %lu, need %d.",
+ desc->size, N_DESC*BLOCK_SIZE);
+
+ cvmx_bootmem_free_named(BLOCK_NAME);
+ }
+
+ /* Allocate named block for write index */
+ read_buf->base = cvmx_bootmem_alloc_named_range(N_DESC*BLOCK_SIZE,
+ NB_MIN_ADDR, NB_MAX_ADDR, NB_4KB_ALIGNED, BLOCK_NAME);
+
+ syslog(LOG_INFO, "Allocated memory block at %p of size %d.",
+ read_buf->base,
+ N_DESC*BLOCK_SIZE);
+ }
+ else
+ {
+ syslog(LOG_INFO, "Found memory block at 0x%lx of size %lu.",
+ desc->base_addr, desc->size);
+
+ read_buf->base = cvmx_phys_to_ptr(desc->base_addr);
+ }
+
+ if(read_buf->base == (void *)-1)
+ {
+ cavium_print_msg("Failed to allocate named block\n");
- exit(1);
+ return -1;
+ }

+ cavium_print_dbg("read_idx_ptr: %p\n",read_buf->base);
@@ -141,18 +173,31 @@ host_oct_handshake(void *user_dev,struct
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr);
+ cavium_print_dbg("scratch_val : %lx\n",scratch_val);

```



```

+ t0 = time(NULL);
+ while(scratch_val != HOSTFW_HS_NB_DONE)
+ {
+ usleep(1000);
+ if (time(NULL) > t0 + HS_TIMEOUT)
+ {
+ syslog(LOG_INFO, "Timed out waiting for handshake to complete.");
+ return -1;
+ }
+ else if (killed)
+ return -1;
+
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
+ scratch_reg_addr);
+ }
+ cavium_print_msg("Host-Octeon Handshake Completed\n");
+ cavium_print_msg("OCT-PCI-TAP running as daemon\n");
+
+ return 0;
+ }

/*
 * Wait on GPIO and TAP Fds. A set gpio fd indicates the availability
 * of data on BAR1 memory.
 */
+#ifdef USE_GPIO
static int oct_wait_on_fds()
{
 int nfds = 0;
@@ -175,6 +220,7 @@ static int oct_wait_on_fds()
 return 0;
}
+#endif

/*
 * Perform device and network initialization.
@@ -184,18 +230,31 @@ static int oct_wait_on_fds()
 */
static inline int host_oct_init()
{
+ const cvmx_bootmem_named_block_desc_t *desc;
+
+ /* Run the application only on the first core */
+ if(cvmx_get_core_num() != 0)
+ exit (0);
+
+ /* Fetch the named block allocated for reserving boot memory */

```

```

+ desc = cvmx_bootmem_find_named_block("miscbuf");
+ if(desc)
+ {
+ syslog(LOG_INFO, "Found reserved miscbuf memory block in %luMiB bytes. Freeing it ...",
+
+ desc->size / 1024 / 1024);
+ cvmx_bootmem_free_named("miscbuf");
+ }
+
+ /* Connect to the device */
+ if ((tun_fd = host_oct_tun_alloc(TAP_IF_NAME, IFF_TAP)) < 0) /* tun interface */
+ return tun_fd;

+ #ifdef USE_GPIO
+ if((gpio_fd = open(GPIO_DEV_FILE, O_RDWR)) < 0) {
+ cavium_print_msg("Error opening %s\n", GPIO_DEV_FILE);
+ return gpio_fd;
+ }
+ #endif

+ return 0;
+ }
@@ -209,7 +268,8 @@ static inline int host_oct_init()
+ /*
+ static void host_oct_data_loop(nw_buf_t *read_buf, nw_buf_t *write_buf)
+ {
+ - while (1)
+ + #ifdef USE_GPIO
+ + while (!killed)
+ {
+ oct_wait_on_fds();

@@ -221,9 +281,29 @@ static void host_oct_data_loop(nw_buf_t

+ if (FD_ISSET(tun_fd, &readfds))
+ {
+ - host_oct_network_to_pci(write_buf, tun_fd);
+ + if (host_oct_network_to_pci(write_buf, tun_fd) < 0)
+ + break;
+ + }
+ + }
+ #else
+ + int ret, ret2;
+ + int idle_count
+ = 0;
+ +
+ + while (!killed)
+ + {

```

```

+ ret = host_oct_pci_to_network(read_buf);
+ ret2 = host_oct_network_to_pci(write_buf, tun_fd);
+ if (ret2 < 0)
+ break;
+ if (ret == 0 && ret2 == 0)
+ {
+ if (++ idle_count > 1000)
+ usleep(IDLE_SLEEP);
+ }
+ else
+ idle_count = 0;
+ }
+ #endif
+ }

#endif /* _OCT_TAP_DEFS_H */
diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c.orig octeon-sdk/CNNIC-SDK-SRC/modules/pci-
tap/pci-tap.c
--- octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c.orig 2016-08-10 21:58:14.874582789 -0400
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c 2016-08-10 22:08:33.831402860 -0400
@@ -33,8 +33,22 @@
#include <stdint.h>
#include <time.h>
#include <unistd.h>
+#include <signal.h>
+
+#define USE_GPIO
+#define HS_TIMEOUT 15
+#define TX_TIMEOUT 5
+#define IDLE_SLEEP 200
+
+static volatile sig_atomic_t killed = 0;
+
#include "pci-tap.h"

+void exit_handler(int
sig)
+{
+ killed = 1;
+}
+
+/*
+ * Bind to the TAP adapter and make all reads non blocking.
+ *
@@ -68,7 +82,7 @@ int host_oct_tun_alloc(char *dev, int fl
return err;
}

```

```

-#ifdef HOST_TAP
+#if defined(HOST_TAP) || !defined(USE_GPIO)
 /* Make all reads non-blocking */
 if(fcntl(fd,F_SETFL,O_NONBLOCK) < 0) {
 cavium_print_msg("Failed to make non-blocking fd.\n");
@@ -148,7 +162,7 @@ int host_oct_pci_to_network(void *pbuf)
 BUF_INC_IDX(preadbuf, ridx);
 cavium_write_mem(preadbuf->base + OFFSETOF(nw_buf_t, ridx), &(preadbuf->ridx), sizeof(uint64_t),
oct_pci_dev);
 }
-#ifdef OCT_TAP
+#if defined(OCT_TAP) && defined(USE_GPIO)
 else {
 cavium_print_msg("Invalid interrupt from host with Packet len: 0\n");
 }
@@ -167,12 +181,19 @@ int host_oct_network_to_pci(void * buf,
{
 int nread = 0, skipidx;
 nw_buf_t *pwbuff = (nw_buf_t *)buf;
+ static time_t t0 = 0;

 if(!buf_isfull(pwbuff,
oct_pci_dev)){

+ t0 = time(NULL);
+
 /*Calculate of amount of descriptor index to be skipped if ndescs > 1*/
 skipidx = (pwbuff->widx)?(pwbuff->widx -1):0;
 if((nread = read(tun_fd, pwbuff->desc[pwbuff->widx].data, MAX_ETH_FRAM_SZ))<0){
+#ifndef USE_GPIO
+ if (errno == EAGAIN)
+ return 0;
+#endif
 cavium_print_msg("TUNFD read error [%d, %s]\n", errno, strerror(errno));
 return -1;
 }
@@ -189,6 +210,11 @@ int host_oct_network_to_pci(void * buf,
 &(pwbuff->widx), sizeof(uint64_t), oct_pci_dev);
 }
}
+ else if (t0 && time(NULL) > t0 + TX_TIMEOUT)
+ {
+ syslog(LOG_ERR, "Time out waiting for write buffer.");
+ return -1;
+ }
 return nread;
}

```

```

@@ -220,6 +246,8 @@ int main(int argc, char *argv[])
 nw_buf_t *read_buf = NULL;
 nw_buf_t *write_buf = NULL;

+ openlog("pci-tap", LOG_PID | LOG_PERROR, LOG_USER);
+
 if (host_oct_init())
 {
 cavium_print_msg("Init Failed\n");
@@ -229,25 +257,43 @@ int main(int argc, char *argv[])
 read_buf = (nw_buf_t *) malloc(1*NW_BUF_SIZE);
 write_buf = (nw_buf_t *) malloc(1*NW_BUF_SIZE);

- memset(read_buf, 0x0, NW_BUF_SIZE);
- memset(write_buf, 0x0, NW_BUF_SIZE);
+ if (!read_buf || !write_buf)
+ {
+ cavium_print_msg("Failed to allocate r/w buffers.\n");
+ return 1;
+ }

- host_oct_handshake(oct_pci_dev,read_buf,write_buf);
+ daemon(0,1);

- /*Double check: Reset indexes after handshake*/
- read_buf->ridx = 0;
- read_buf->widx = 0;
- write_buf->ridx = 0;
- write_buf->widx = 0;
-
- /*Write widx/ridx values to nw_buf_t on PCI BAR*/
- cavium_write_mem(write_buf->base, &(write_buf->widx),
- 2*sizeof(uint64_t), oct_pci_dev);
- cavium_write_mem(read_buf->base, &(read_buf->widx),
- 2*sizeof(uint64_t), oct_pci_dev);
- daemon(0,0);
+ //install signal
 handler
+ signal(SIGINT, exit_handler);
+ signal(SIGTERM, exit_handler);
+ signal(SIGHUP, SIG_IGN);
+ signal(SIGPIPE, SIG_IGN);
+
+ while (!killed)
+ {
+ memset(read_buf, 0x0, NW_BUF_SIZE);
+ memset(write_buf, 0x0, NW_BUF_SIZE);
+

```

```

+ if (host_oct_handshake(oct_pci_dev,read_buf,write_buf))
+ continue;

- host_oct_data_loop(read_buf, write_buf);
+ /*Double check: Reset indexes after handshake*/
+ read_buf->ridx = 0;
+ read_buf->widx = 0;
+ write_buf->ridx = 0;
+ write_buf->widx = 0;

+ host_oct_data_loop(read_buf, write_buf);
+ }
+
+ #ifdef HOST_TAP
+ user_drv_pci_close(oct_pci_dev);
+ #endif
+
+ free(read_buf);
+ free(write_buf);
+
+ return 0;
}

```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/pci-tap-timeout-signal-handshake.patch
```

No license file was found, but licenses were detected in source scan.

```

diff -Nru OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c OCTEON-
SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c
--- OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-08-22
11:01:01.569856904 -0700
+++ OCTEON-SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-08-22 10:52:55.786734360 -
0700
@@ -155,6 +155,7 @@
#include "configs/octeon_sff7800_shared.h"
#include "configs/octeon_niagara83x_shared.h"
#include "configs/octeon_rainier_shared.h"
+#include "configs/octeon_kilburn_shared.h"
#ifdef __U_BOOT__
include <asm/arch/octeon_eeprom_types.h>
#else
@@ -11738,6 +11739,11 @@
 { OCTEON_CLARK_SPD_VALUES };
#endif

+#if !defined(CVMX_BUILD_FOR_UBOOT) || defined(CONFIG_OCTEON_KILBURN)
+static uint8_t __attribute__((unused)) octeon_kilburn_cfg0_spd_values[256] =

```

```

+ { OCTEON_KILBURN_CFG0_SPD_VALUES };
+ #endif
+
+ #if !defined(CVMX_BUILD_FOR_UBOOT)
+ \| \
+ defined(CONFIG_OCTEON_NAS7000_REF_EMMC_STAGE2) \| \
+ (defined(CONFIG_OCTEON_NAS7000_REF) && defined(unix))
+ @ -12163,6 +12169,18 @@
+ }
+ },
+ # endif
+ # if !defined(CVMX_BUILD_FOR_UBOOT) \| \
+ defined(CONFIG_OCTEON_KILBURN)
+ {
+ .board_type = CVMX_BOARD_TYPE_CUST_KILBURN,
+ .eeprom_addr = OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR,
+ .chip_ddr_config = {
+ {
+ .ddr_config = {
+ OCTEON_KILBURN_DDR_CONFIGURATION(OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION)
+ }
+ }
+ }
+ },
+ # endif
+ # if defined(CONFIG_OCTEON_RAINIER) \| !defined(CVMX_BUILD_FOR_UBOOT)
+ {
+ .board_type = CVMX_BOARD_TYPE_RAINIER,
+ diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk OCTEON-SDK/bootloader/u-
+ boot/board/octeon/kilburn/config.mk
+ --- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk 1969-12-31 16:00:00.000000000 -0800
+ +++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/config.mk 2016-08-22 11:09:07.448857046 -0700
+ @@ -0,0 +1,35
+ @@
+ + #
+ + # (C) Copyright 2003
+ + # Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+ + # (C) Copyright 2011
+ + # Cavium Inc., Inc. support@cavium.com
+ + #
+ + # See file CREDITS for list of people who contributed to this
+ + # project.
+ + #
+ + # This program is free software; you can redistribute it and/or
+ + # modify it under the terms of the GNU General Public License as
+ + # published by the Free Software Foundation; either version 2 of
+ + # the License, or (at your option) any later version.
+ + #

```

```

+# This program is distributed in the hope that it will be useful,
+# but WITHOUT ANY WARRANTY; without even the implied warranty of
+# MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
+# GNU General Public License for more details.
+#
+# You should have received a copy of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307 USA
+#
+
+#
+# Octeon CN63XX
+#
+
+CONFIG_SYS_TEXT_BASE = 0xC0000000
+PLATFORM_CPPFLAGS
+= -mtune=octeon2 -finline -ffunction-sections \
+ -fdata-sections -ftree-pre -fno-asynchronous-unwind-tables \
+ -DCVMX_ENABLE_PARAMETER_CHECKING=0 \
+ -DCVMX_ENABLE_DEBUG_PRINTS=1
+#PLATFORM_CPPFLAGS += -DCVMX_ENABLE_CSR_ADDRESS_CHECKING=1
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c OCTEON-
SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 1969-12-31 16:00:00.000000000 -
0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 2016-08-22 11:08:39.233919733 -
0700
@@ -0,0 +1,562 @@
+/*
+ * (C) Copyright 2015 Cavium Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+#include <common.h>
+#include <asm/mipsregs.h>
+#include <asm/arch/octeon_boot.h>
+#include <asm/arch/cvmx-sim-magic.h>
+#include <asm/arch/cvmx-qlm.h>
+#include <asm/arch/octeon_fdt.h>
+#include <fdt_support.h>
+#include <asm/arch/octeon_board_common.h>
+#include
<asm/arch/lib_octeon.h>
+#include <asm/arch/lib_octeon_shared.h>
+#include <asm/arch/octeon_qlm.h>
+#include <asm/arch/cvmx-helper-util.h>

```



```

#include <asm/arch/cvmx-helper-cfg.h>
#include <asm/arch/cvmx-smi-defs.h>
#include <asm/gpio.h>
+
#define MAX_MIX_ENV_VARS 4
+
extern void print_isp_volt(const char **labels, uint16_t isp_dev_addr, uint8_t adc_chan);
+
extern int read_ispPAC_mvols(uint16_t isp_dev_addr, uint8_t adc_chan);
+
extern int read_ispPAC_mvols_avg(int loops, uint16_t isp_dev_addr, uint8_t adc_chan);
+
+DECLARE_GLOBAL_DATA_PTR;
+
+/*
+ * parse_env_var: Parse the environment variable ("bgx_for_mix%d") to
+ * extract the lmac it is set to.
+ *
+ * index: Index of environment variable to parse.
+ * environment variable.
+ * env_bgx: Updated with the bgx of the lmac in the environment
+ * variable.
+ * env_lmac: Updated with the index of lmac in the environment
+ * variable.
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static
int parse_env_var(int index, int *env_bgx, int *env_lmac)
+{
+ char env_var[20];
+ ulong xipd_port;
+
+ sprintf(env_var, "bgx_for_mix%d", index);
+ if ((xipd_port = getenv_ulong(env_var, 0, 0xffff)) != 0xffff) {
+ int xiface;
+ struct cvmx_xiface xi;
+ struct cvmx_xport xp;
+
+ /*
+ * The environemt variable is set to the xipd port. Convert the
+ * xipd port to numa node, bgx, and lmac.
+ */
+ xiface = cvmx_helper_get_interface_num(xipd_port);
+ xi = cvmx_helper_xiface_to_node_interface(xiface);
+ xp = cvmx_helper_ipd_port_to_xport(xipd_port);
+ *env_bgx = xi.interface;
+ *env_lmac = cvmx_helper_get_interface_index_num(xp.port);

```

```

+ return 0;
+ }
+
+ return -1;
+}
+
+/*
+ * get_lmac_fdt_node: Search the device tree for the node corresponding to
+ * a given bgx lmac.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Numa node of the lmac to search for.
+ * search_bgx: Bgx of the lmac to search for.
+ * search_lmac: Lmac index to search for.
+ * compat: Compatible
+ string to search for.
+
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */
+static int get_lmac_fdt_node(const void *fdt, int search_node, int search_bgx,
+ int search_lmac, const char *compat)
+{
+ int node;
+ const fdt32_t *reg;
+ u64 addr;
+ int fdt_node = -1;
+ int fdt_bgx = -1;
+ int fdt_lmac = -1;
+ int len;
+ int parent;
+
+ /* Iterate through all bgx ports */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ compat)) >= 0) {
+ /* Get the node and bgx from the physical address */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ !(reg = fdt_getprop(fdt, parent, "reg", &len)))
+ continue;
+
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ fdt_node = (addr >> 36) & 0x7;
+ fdt_bgx = (addr >> 24) & 0xf;
+
+ /* Get the lmac index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len)))
+ fdt_lmac = *reg;

```

```

+
+ /* Check for a
+ match */
+ if (search_node == fdt_node && search_bgx == fdt_bgx &&
+ search_lmac == fdt_lmac)
+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * get_mix_fdt_node: Search the device tree for the node corresponding to
+ * a given mix.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Mix numa node to search for.
+ * search_index: Mix index to search for.
+ *
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */
+static int get_mix_fdt_node(const void *fdt, int search_node, int search_index)
+{
+ int node;
+
+ /* Iterate through all the mix fdt nodes */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ "cavium,octeon-7890-mix")) >= 0) {
+ int parent;
+ int len;
+ const char *name;
+ int mix_numa_node;
+ const fdt32_t *reg;
+ int mix_index = -1;
+ u64 addr;
+
+ /* Get the numa node of the mix from the parent node name */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ ((name =
+ fdt_get_name(fdt, parent, &len)) == NULL) ||
+ ((name = strchr(name, '@')) == NULL))
+ continue;
+
+ name++;
+ mix_numa_node = simple_strtol(name, NULL, 0) ? 1 : 0;
+
+

```

```

+ /* Get the mix index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len)) {
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ mix_index = (addr >> 11) & 1;
+ }
+
+ /* Check for a match */
+ if (mix_numa_node == search_node && mix_index == search_index)
+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * fdt_fix_mix: Fix the mix nodes in the device tree. Only the mix nodes
+ * configured by the user will be preserved. All other mix
+ * nodes will be trimmed.
+ *
+ * fdt: Pointer to flat device tree
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static int fdt_fix_mix(const void *fdt)
+{
+ int node;
+ int next_node;
+ int len;
+ int i;
+
+ /* Parse all the mix port environment variables */
+ for (i = 0; i < MAX_MIX_ENV_VARS; i++) {
+ int env_node = 0;
+ int env_bgx
+ = -1;
+ int env_lmac = -1;
+ int lmac_fdt_node = -1;
+ int mix_fdt_node = -1;
+ int lmac_phandle;
+ char *compat;
+
+ /* Get the lmac for this environment variable */
+ if (parse_env_var(i, &env_bgx, &env_lmac))
+ continue;
+
+ /* Get the fdt node for this lmac and add a phandle to it */
+ compat = "cavium,octeon-7890-bgx-port";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node, env_bgx,

```

```

+ env_lmac, compat)) < 0) {
+ /* Must check for the xcv compatible string too */
+ compat = "cavium,octeon-7360-xcv";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node,
+ env_bgx,
+ env_lmac,
+ compat)) < 0) {
+ printf("WARNING: Failed to get lmac fdt node "
+ "for %d%d%d\n", env_node, env_bgx,
+ env_lmac);
+ continue;
+ }
+ }
+
+ lmac_phandle = fdt_alloc_phandle((void *)fdt);
+ fdt_set_phandle((void *)fdt, lmac_fdt_node, lmac_phandle);
+
+ /* Get the fdt mix node corresponding to this
+ lmac */
+ if ((mix_fdt_node = get_mix_fdt_node(fdt, env_node,
+ env_lmac)) < 0)
+ continue;
+
+ /* Point the mix to the lmac */
+ fdt_getprop(fdt, mix_fdt_node, "cavium,mac-handle", &len);
+ fdt_setprop_inplace((void *)fdt, mix_fdt_node,
+ "cavium,mac-handle", &lmac_phandle, len);
+ }
+
+ /* Trim unused mix'es from the device tree */
+ for (node = fdt_next_node(fdt, -1, NULL); node >= 0;
+ node = next_node) {
+ const char *compat;
+ const fdt32_t *reg;
+
+ next_node = fdt_next_node(fdt, node, NULL);
+
+ if ((compat = fdt_getprop(fdt, node, "compatible", &len))) {
+ if (strcmp(compat, "cavium,octeon-7890-mix"))
+ continue;
+
+ if ((reg = fdt_getprop(fdt, node,
+ "cavium,mac-handle", &len))) {
+ if (*reg == 0xffff)
+ fdt_nop_node((void *)fdt, node);
+ }
+ }
+ }
+ }

```

```

+
+ return 0;
+}
+
+int no_phy[8] = {0, 0, 0, 0, 0, 0, 0, 0};
+
+static void kill_fdt_phy(void *fdt, int offset, void *arg)
+{
+ int len, phy_offset;
+ const fdt32_t
+ *php;
+ uint32_t phandle;
+
+ php = fdt_getprop(fdt, offset, "phy-handle", &len);
+ if (php && len == sizeof(*php)) {
+ phandle = fdt32_to_cpu(*php);
+ fdt_nop_property(fdt, offset, "phy-handle");
+ phy_offset = fdt_node_offset_by_phandle(fdt, phandle);
+ if (phy_offset > 0)
+ fdt_nop_node(fdt, phy_offset);
+ }
+}
+
+void __fixup_xcv(void)
+{
+ unsigned long bgx = getenv_ulong("bgx_for_rgmii", 10, (unsigned long)-1);
+ char fdt_key[16];
+ int i;
+
+ debug("%s: BGX %d\n", __func__, (int)bgx);
+
+ for (i = 0; i < 3; i++) {
+ snprintf(fdt_key, sizeof(fdt_key),
+ bgx == i ? "%d,xcv" : "%d,not-xcv", i);
+ debug("%s: trimming bgx %lu with key %s\n",
+ __func__, bgx, fdt_key);
+
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key,
+ "cavium,xcv-trim", true, NULL, NULL);
+ }
+}
+
+/* QLM0 - QLM6 */
+void __fixup_fdt(void)
+{
+ int qlm;
+ int speed = 0;
+
+

```

```

+ for (qlm = 0; qlm < 7; qlm++) {
+ enum cvmx_qlm_mode mode;
+ char fdt_key[16];
+ const char *type_str = "none";
+ mode
+ = cvmx_qlm_get_mode(qlm);
+ switch (mode) {
+ case CVMX_QLM_MODE_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII_1X1:
+ type_str = "sgmii";
+ break;
+ case CVMX_QLM_MODE_XAUI:
+ case CVMX_QLM_MODE_RGMII_XAUI:
+ speed = (cvmx_qlm_get_gbaud_mhz(qlm) * 8 / 10) * 4;
+ if (speed == 10000)
+ type_str = "xaui";
+ else
+ type_str = "dxaui";
+ break;
+ case CVMX_QLM_MODE_RXAUI:
+ case CVMX_QLM_MODE_RGMII_RXAUI:
+ type_str = "rxaui";
+ break;
+ case CVMX_QLM_MODE_XLAUI:
+ case CVMX_QLM_MODE_RGMII_XLAUI:
+ type_str = "xlaui";
+ break;
+ case CVMX_QLM_MODE_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI_1X1:
+ type_str = "xfi";
+ break;
+ case CVMX_QLM_MODE_10G_KR:
+ case CVMX_QLM_MODE_RGMII_10G_KR:
+ type_str = "10G_KR";
+ break;
+ case CVMX_QLM_MODE_40G_KR4:
+ case CVMX_QLM_MODE_RGMII_40G_KR4:
+ type_str = "40G_KR4";
+ break;
+ case CVMX_QLM_MODE_SATA_2X1:
+ type_str = "sata";
+ break;
+ case
+ CVMX_QLM_MODE_SGMII_2X1:
+ case CVMX_QLM_MODE_XFI_1X2:
+ case CVMX_QLM_MODE_10G_KR_1X2:

```

```

+ case CVMX_QLM_MODE_RXAUI_1X2:
+ case CVMX_QLM_MODE_MIXED: // special for DLM5 & DLM6
+ {
+ cvmx_bgxx_cmr_config_t cmr_config;
+ cvmx_bgxx_spux_br_pmd_control_t pmd_control;
+ int mux = cvmx_qlm_mux_interface(2);
+ if (mux == 2) { // only dlm6
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CMRX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2, 2));
+ } else {
+ if (qlm == 5) {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CMRX_CONFIG(0, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(0, 2));
+ } else {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CMRX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2, 2));
+ }
+ }
+ switch (cmr_config.s.lmac_type) {
+ case 0:
+ type_str = "sgmii";
+ break;
+ case 1:
+ type_str
+ = "xaui";
+ break;
+ case 2:
+ type_str = "rxaui";
+ break;
+ case 3:
+ if (pmd_control.s.train_en)
+ type_str = "10G_KR";
+ else
+ type_str = "xfi";
+ break;
+ case 4:
+ if (pmd_control.s.train_en)
+ type_str = "40G_KR4";
+ else
+ type_str = "xlaui";
+ break;
+ default:

```



```

+ type_str = "none";
+ break;
+ }
+ break;
+ }
+ default:
+ type_str = "none";
+ break;
+ }
+ sprintf(fdt_key, "%d,%s", qlm, type_str);
+ debug("Patching qlm %d for %s for mode %d%s\n",
+ qlm, fdt_key, mode, no_phy[qlm] ? ", removing PHY" : "");
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key, NULL,
+ true, no_phy[qlm] ? kill_fdt_phy : NULL, NULL);
+ }
+}
+
+int board_fixup_fdt(void)
+{
+ __fixup_fdt();
+ __fixup_xcv();
+
+ /* Fix the mix ports */
+ fdt_fix_mix(gd->fdt_blob);
+
+ return 0;
+}
+
+/* Raise an integer to a power */
+static uint64_t ipow(uint64_t base, uint64_t exp)
+{
+ uint64_t result = 1;
+ while (exp)
+ {
+ if (exp & 1)
+ result *= base;
+ exp >>= 1;
+ base *= base;
+ }
+ return result;
+}
+
+/*
+ * This function now supports only single (node0) configuration.
+ * Some of the called funcs (read_ispPAC_mvols_avg(), octeon_led_str_write())
+ * are hard-coded for node0. Also we set gd->arch.mcu_rev_maj, ...
+ * It can be extended to support multinode if needed/important
+ */

```

```

+static int checkboardinfo(void)
+{
+ int core_mVolts, dram_mVolts0, dram_mVolts1;
+ char mcu_ip_msg[64] = { 0 };
+ char tmp[10];
+ int characters, idx = 0, value = 0;
+
+ + debug("In %s\n", __func__);
+ + if (octeon_show_info()) {
+
+ + int mcu_rev_maj = 0;
+ + int mcu_rev_min = 0;
+
+ + if (twsii_mcu_read(0x00) == 0xa5
+ + && twsii_mcu_read(0x01) == 0x5a) {
+ + gd->arch.mcu_rev_maj = mcu_rev_maj = twsii_mcu_read(2);
+ + gd->arch.mcu_rev_min = mcu_rev_min = twsii_mcu_read(3);
+ + } else {
+
+ + return -1; /* Abort if we can't access the MCU */
+
+ + }
+
+ + core_mVolts = read_ispPAC_mvols_avg(10,
+ BOARD_ISP_TWSI_ADDR, 8);
+ + dram_mVolts0 = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 7); /* DDR0 */
+ + dram_mVolts1 = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 6); /* DDR1 */
+
+ + if (twsii_mcu_read(0x14) == 1)
+ + sprintf(mcu_ip_msg, "MCU IPaddr: %d.%d.%d.%d, ",
+ + twsii_mcu_read(0x10),
+ + twsii_mcu_read(0x11),
+ + twsii_mcu_read(0x12),
+ + twsii_mcu_read(0x13));
+ + printf("MCU rev: %d.%02d, %sCPU voltage: %d.%03d DDR{0,1} voltages: %d.%03d,%d.%03d\n",
+ + gd->arch.mcu_rev_maj, gd->arch.mcu_rev_min, mcu_ip_msg,
+ + core_mVolts / 1000, core_mVolts % 1000,
+ + dram_mVolts0 / 1000, dram_mVolts0 % 1000,
+ + dram_mVolts1 / 1000, dram_mVolts1 % 1000);
+
+ + #define LED_CHARACTERS 8
+ + value = core_mVolts;
+ + idx = sprintf(tmp, "%lu ", gd->cpu_clk/(1000*1000));
+ + characters = LED_CHARACTERS - idx;
+
+ +
+ + if (value / 1000) {
+ + idx += sprintf(tmp + idx, "%d", value / 1000);
+ + characters = LED_CHARACTERS - idx;
+ + }
+
+ +

```

```

+ characters
+ = 1; /* Account for decimal point */
+
+ value %= 1000;
+ value = DIV_ROUND_UP(value, ipow(10, max(3 - characters, 0)));
+
+ idx += sprintf(tmp + idx, "%.0*d", min(3, characters), value);
+
+ /* Display CPU speed and voltage on display */
+ octeon_led_str_write(tmp);
+ } else {
+ octeon_led_str_write("Boot ");
+ }
+
+ return 0;
+}
+
+/**
+ * Here is the description of the parameters that are passed to QLM configuration
+ * param0 : The QLM to configure
+ * param1 : Speed to configure the QLM at
+ * param2 : Mode the QLM to configure
+ * param3 : 1 = RC, 0 = EP
+ * param4 : 0 = GEN1, 1 = GEN2, 2 = GEN3
+ * param5 : ref clock select, 0 = 100Mhz, 1 = 125MHz, 2 = 156MHz
+ * param6 : ref clock input to use:
+ * 0 - external reference (QLMx_REF_CLK)
+ * 1 = common clock 0 (QLMC_REF_CLK0)
+ * 2 = common_clock 1 (QLMC_REF_CLK1)
+ */
+void board_configure_qlms(void)
+{
+ int node=0;
+
+
+ octeon_init_qlm(0);
+ mdelay(100);
+
+
+ cvmx_coremask_for_each_node(node, gd->arch.node_mask)
+ {
+ debug("Configuring node %d, QLM 0 and 1\n", node);
+ octeon_configure_qlm(0, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+ octeon_configure_qlm(1, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+
+
+
+ debug("Configuring node %d, QLMs 2 for XLAUI\n", node);
+ octeon_configure_qlm(2, 103125, CVMX_QLM_MODE_XLAUI, 0, 0, 2, 2); /* 40G_KR4 */
+ octeon_qlm_tune_v3(0, 2, 103125, 0xd, 0xd0, -1, -1);
+
+
+

```

```

+/* For other skews of kilburn, uncomment them */
+
+ printf("Configuring node %d, QLMs 3 for XAUI\n", node);
+ octeon_configure_qlm(3, 103125, CVMX_QLM_MODE_XFI, 0, 0, 2, 2); /* 10G_KR */
+ octeon_qlm_tune_v3(0, 3, 103125, 0xd, 0xd0, -1, -1);
+ }
+}
+
+int checkboard(void)
+{
+ checkboardinfo();
+
+ return 0;
+}
+
+int early_board_init(void)
+{
+ octeon_board_get_clock_info(KILBURN_DEF_DRAM_FREQ);
+ octeon_board_get_descriptor(CVMX_BOARD_TYPE_CUST_KILBURN, 1, 0);
+
+ /* Even though the CPU ref freq
+ is stored in the clock descriptor, we
+ * don't read it here. The MCU reads it and configures the clock, and
+ * we read how the clock is actually configured.
+ * The bootloader does not need to read the clock descriptor tuple for
+ * normal operation on rev 2 and later boards.
+ */
+ octeon_board_get_mac_addr();
+
+ return 0;
+}
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/kilburn.dts
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 2016-08-22 11:08:51.849670066 -0700
@@ -0,0 +1,181 @@
+/dts-v1/;
+/*
+ * Cavium Inc. Kilburn 76XX
+ */
+ / {
+ model = "cavium,kilburn";
+ compatible = "cavium,kilburn";
+ #address-cells = <2>;
+ #size-cells = <2>;
+
+ soc@0 {
+ interrupt-parent = <&ciu3>;

```

```

+ compatible = "simple-bus";
+ #address-cells = <2>;
+ #size-cells = <2>;
+ ranges; /* Direct
mapping */
+ cavium,node-trim = "0,soc";
+
+ ciu3: interrupt-controller@101000000000 {
+ compatible = "cavium,octeon-7890-ciu3";
+ interrupt-controller;
+ /* Interrupts are specified by two parts:
+ * 1) Source number (20 significant bits)
+ * 2) Trigger type: (4 == level, 1 == edge)
+ */
+ #address-cells = <0>;
+ #interrupt-cells = <2>;
+ reg = <0x10100 0x00000000 0x0 0xb0000000>;
+ };
+
+ bootbus: bootbus@118000000000 {
+ compatible = "cavium,octeon-3860-bootbus";
+ reg = <0x11800 0x00000000 0x0 0x200>;
+ /* The chip select number and offset */
+ #address-cells = <2>;
+ /* The size of the chip select region */
+ #size-cells = <1>;
+ ranges = <0 0 0 0x1ec00000 0x1400000>,
+ <1 0 0x10000 0x10000000 0>,
+ <2 0 0x10000 0x20000000 0>,
+ <3 0 0x10000 0x30000000 0>,
+ <4 0 0x10000 0x40000000 0>,
+ <5 0 0x10000 0x50000000 0>,
+ <6 0 0x10000 0x60000000 0>,
+ <7 0 0x10000 0x90000000 0>;
+
+ cavium,cs-config@0
+ {
+ compatible = "cavium,octeon-3860-bootbus-config";
+ cavium,cs-index = <0>;
+ cavium,t-adr = <10>;
+ cavium,t-ce = <100>;
+ cavium,t-oe = <30>;
+ cavium,t-we = <25>;
+ cavium,t-rd-hld = <0>;
+ cavium,t-wr-hld = <20>;
+ cavium,t-pause = <10>;
+ cavium,t-wait = <0>;
+ cavium,t-page = <20>;

```

```

+ cavium,t-rd-dly = <0>;
+
+ cavium,page-mode = <1>;
+ cavium,pages = <8>;
+ cavium,bus-width = <16>;
+ cavium,ale-mode = <1>;
+ };
+
+ flash0: nor@0,0 {
+ compatible = "cfi-flash";
+ reg = <0 0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <1>;
+
+ partition@0 {
+ label = "bootloader";
+ reg = <0 0x340000>;
+ read-only;
+ };
+ partition@300000 {
+ label = "storage";
+ reg = <0x340000 0xcbe000>;
+ };
+ partition@fe0000 {
+ label = "environment";
+ reg = <0xfe0000 0x20000>;
+ read-only;
+ };
+ };
+ };
+
+ serial@1180000000800 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg
+ = <0x11800 0x00000800 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08000 4>;
+ };
+
+ serial@1180000000c00 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg = <0x11800 0x00000c00 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08040 4>;

```

```

+ };
+
+ gpio0: gpio-controller@107000000800 {
+ #gpio-cells = <2>;
+ compatible = "cavium,octeon-7890-gpio";
+ reg = <0x10700 0x00000800 0x0 0x100>;
+ gpio-controller;
+ /* Interrupts are specified by two parts:
+ * 1) GPIO pin number (0..15)
+ * 2) Triggering (1 - edge rising
+ * 2 - edge falling
+ * 4 - level active high
+ * 8 - level active low)
+ */
+ interrupt-controller;
+ #interrupt-cells = <2>;
+ /* The GPIO pins connect to 16 consecutive CUI bits */
+ interrupts = <0x03000 4>, <0x03001 4>, <0x03002 4>, <0x03003 4>,
+
+ <0x03004 4>, <0x03005 4>, <0x03006 4>, <0x03007 4>,
+ <0x03008 4>, <0x03009 4>, <0x0300a 4>, <0x0300b 4>,
+ <0x0300c 4>, <0x0300d 4>, <0x0300e 4>, <0x0300f 4>;
+ };
+
+
+
+ /* BGX definitions here must match kilburn_board.c settings. */
+ /* BGX 0*/
+ ethernet-mac-nexus@11800e0000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe0000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+
+ /* SerDes 0, may differ from PCS Lane/LMAC */
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "2,xloui";
+ };
+ };
+
+ /* BGX 1*/
+ ethernet-mac-nexus@11800e1000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe1000000 0x0 0x1000000>;
+ #address-cells = <1>;

```

```

+ #size-cells = <0>;
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00
00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@1 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <1>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@2 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <2>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@3 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <3>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+
+ };
+ };
+};
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/Makefile
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/Makefile 2016-08-22 11:09:00.567759719 -0700
@@ -0,0 +1,68 @@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering,
+ wd@denx.de.
+#
+# See file CREDITS for list of people who contributed to this
+# project.
+#
+# This program is free software; you can redistribute it and/or
+# modify it under the terms of the GNU General Public License as
+# published by the Free Software Foundation; either version 2 of
+# the License, or (at your option) any later version.
+#

```



```

+# This program is distributed in the hope that it will be useful,
+# but WITHOUT ANY WARRANTY; without even the implied warranty of
+# MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
+# GNU General Public License for more details.
+#
+# You should have received a copy of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307 USA
+#
+#
+include $(TOPDIR)/config.mk
+
+CFLAGS += -I$(TOPDIR)/arch/mips/include/asm/arch-octeon
+
+LIB = $(obj)lib$(BOARD).o
+
+COBJS = $(BOARD)_board.o
+SOBJS =
+COBJS-y =
+#OBJS = $(BOARD).o
flash.o
+
+SRCS := $(SOBJS:.o=.S) $(COBJS:.o=.c) $(COBJS-y:.o=.c)
+OBJS := $(addprefix $(obj),$(COBJS))
+SOBJS := $(addprefix $(obj),$(SOBJS))
+
+all: $(obj).depend $(LIB)
+
+DTB = $(BOARD).dtb.o
+DTBOBJS := $(addprefix $(obj),$(DTB))
+
+$(BOARD).dtb.o: $(BOARD).dtb.S
+
+$(BOARD).dtb.S: $(BOARD).dtb
+ echo '.section .rodata, "a"' > $@
+ echo '.balign 8' >> $@
+ echo '.global __dtb_begin' >> $@
+ echo '__dtb_begin:' >> $@
+ echo '.incbin "$<"' >> $@
+ echo '__dtb_end:' >> $@
+ echo '.global __dtb_end' >> $@
+ echo '.balign 8' >> $@
+
+$(BOARD).dtb: $(BOARD).dts
+ $(TOPDIR)/tools/dtc -O dtb -p 1024 -o $@ $<
+
+$(LIB): $(obj).depend $(OBJS) $(SOBJS) $(DTBOBJS)

```

```

+ $(call cmd_link_o_target, $(OBJS) $(SOBJS) $(DTBOBJS))
+
+#####
+include $(SRCTREE)/rules.mk
+
+sinclude $(obj).depend
+
+clean:
+ -rm -f $(BOARD).dtb $(BOARD).dtb.S
diff -Nru OCTEON-SDK-old/bootloader/u-boot/boards.cfg OCTEON-SDK/bootloader/u-boot/boards.cfg

OCTEON-SDK-old/bootloader/u-boot/boards.cfg 2016-08-22 11:03:25.284816349 -0700
+++ OCTEON-SDK/bootloader/u-boot/boards.cfg 2016-08-22 11:05:15.143742406 -0700
@@ -112,3 +112,4 @@
Target ARCH CPU Board name Vendor SoC Options
#####
#####
octeon_rainier mips octeon rainier octeon -
octeon_rainier:OCTEON_RAINIER,OCTEON_PCI_HOST=1
+octeon_kilburn mips octeon kilburn octeon -
octeon_kilburn:OCTEON_KILBURN,OCTEON_PCI_HOST=0
diff -Nru OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h OCTEON-SDK/bootloader/u-
boot/include/configs/octeon_kilburn.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn.h 2016-08-22
10:53:45.879796696 -0700
@@ -0,0 +1,280 @@
+/*
+ * (C) Copyright 2003
+ * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+ *
+ * Copyright (C) 2015 Cavium, Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+/*
+ * This file contains the configuration parameters for
+ * Octeon EBB7304 board.
+ */
+
+#ifndef __CONFIG_H__
+#define __CONFIG_H__
+
+/**
+ * Define CONFIG_OCTEON_PCI_HOST = 1 to map the pci devices on the
+ * bus. Define CONFIG_OCTEON_PCI_HOST = 0 for target mode when the
+ * host system performs the pci bus mapping instead. Note that pci

```

```

+ * commands are enabled to allow access to configuration space for
+ * both modes.
+ */
+#ifndef CONFIG_OCTEON_PCI_HOST
+# define CONFIG_OCTEON_PCI_HOST 1
+#endif
+
+/** Enable USB support on OCTEON III */
+#define CONFIG_OCTEON_USB_OCTEON3
+
+#define CONFIG_OCTEON_BIG_STACK_SIZE 0x4000 /** 16K stack */
+
+#include "octeon_common.h"
+
+/* CONFIG_OCTEON_KILBURN set by Makefile in include/config.h */
+
+/**
Default DDR clock if tuple doesn't exist in EEPROM */
+#define KILBURN_DEF_DRAM_FREQ 1050
+
+//#define CONFIG_OCTEON_ENABLE_LED_DISPLAY /** 8-char LED support */
+
+#define CONFIG_LBA48 /** 48-bit mode */
+#define CONFIG_SYS_64BIT_LBA /** 64-bit LBA support */
+#define CONFIG_SYS_ATA_BASE_ADDR 0 /** Make compile happy */
+/* Base address of Common memory for Compact flash */
+
+/* eMMC support */
+#define CONFIG_OCTEON_MMC /** Enable MMC support */
+#define CONFIG_MMC_MBLOCK /** Multi-block support */
+#define CONFIG_CMD_MMC /** Enable mmc command */
+#define CONFIG_SYS_MMC_SET_DEV /** Enable multiple MMC devices */
+#define CONFIG_MMC /** Enable MMC support */
+#define CONFIG_OCTEON_MIN_BUS_SPEED_HZ 100000 /** Minimum MMC bus speed */
+
+/** Maximum number of MMC devices needed for API */
+#define CONFIG_SYS_MMC_MAX_DEVICE 2
+
+#if 0
+/** Enable support for native U-Boot application API */
+#define CONFIG_API
+#endif
+
+
+/* TWSI address for the ispPAC-POWR1220AT8 Voltage
Controller chip */
+#define BOARD_ISP_TWSI_ADDR 0x08
+#define CONFIG_ISPPAC_POWER

```

```

+
+/** Address of MCU on TWSI bus */
+#define BOARD_MCU_TWSI_ADDR 0x60
+
+/** Address of board EEPROM on TWSI bus */
+#define CONFIG_SYS_I2C_EEPROM_ADDR OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR
+/** Default EEPROM address */
+#define CONFIG_SYS_DEF_EEPROM_ADDR CONFIG_SYS_I2C_EEPROM_ADDR
+/* These speed up writes to the serial EEPROM by enabling page writes.
+ * Please see the datasheet for details.
+ */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_BITS 5 /* 32 bytes */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_DELAY_MS 5
+
+/* Set bootdelay to 0 for immediate boot */
+#define CONFIG_BOOTDELAY 0 /** autoboot after X seconds */
+
+#undef CONFIG_BOOTARGS
+
+/** Enable octbootbus command */
+#define CONFIG_CMD_OCTEON_BOOTBUS
+
+/**
+ * The 'mtdids' environment variable is used in conjunction with the 'mtdparts'
+ * variable to define the MTD partitions for u-boot.
+ */
+#define MTDPARTS_DEFAULT \
+ "mtdparts=octeon_nor0:3m(bootloader)ro,"
+ \
+ "2m(kernel)," \
+ "3064k(cramfs)," \
+ "8k(environment)ro;" \
+ "octeon_nand0:6m(bootloader)ro," \
+ "2m(environment)," \
+ "8m(kernel)," \
+ "512m(rootfs)\0"
+
+#define MTDIDS_DEFAULT "nor0=octeon_nor0,nand0=octeon_nand0\0"
+
+/**
+ * End of the NAND boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary. It should be large enough to be able to handle
+ * at least one bad block.
+ */
+#define CONFIG_OCTEON_NAND_BOOT_END 0x600000
+
+
+/* Define this to enable built-in octeon ethernet support */

```

```

+#define CONFIG_OCTEON_SGMII_ENET /** SGMII ethernet support */
+#define CONFIG_OCTEON_XAUI_ENET /** XAUI ethernet support */
+
+/* Enable Octeon built-in networking if RGMII support is enabled */
+#if
+ defined(CONFIG_OCTEON_RGMII_ENET) || defined(CONFIG_OCTEON_SGMII_ENET) || \
+ defined(CONFIG_OCTEON_XAUI_ENET) || defined(CONFIG_OCTEON_QSGMII_ENET)
+# define CONFIG_OCTEON_INTERNAL_ENET
+#endif
+#define CONFIG_OCTEON_MGMT_ENET
+
+/* PCI console is supported since oct-remote-boot is supported.
+ * Enable MUX and oct-remote-bootcmd support as well
+ */
+#define CONFIG_SYS_PCI_CONSOLE
+#define CONFIG_CONSOLE_MUX
+#define CONFIG_OCTEON_BOOTCMD
+
+#include "octeon_cmd_conf.h"
+
+#ifdef CONFIG_CMD_NET
+/**
+ * Define available PHYs
+ */
+# define CONFIG_PHY_GIGE /** Gigabit Ethernet PHY support */
+# define CONFIG_PHY_MARVELL /** Marvell PHY support */
+# define CONFIG_PHY_VITESSE /** Vitesse PHY support */
+# define CONFIG_PHY_BROADCOM /** Broadcom PHY support */
+# define CONFIG_PHY_CORTINA /** Cortina PHY support */
+# define CONFIG_PHY_CORTINA_CS4223
+# include "octeon_network.h"
+#endif
+
+
+
+#define CONFIG_CMD_OCTEON_TLVEEPROM /** "tlv_eeeprom" command */
+/* "qlm" command */
+#define
+ CONFIG_CMD_QLM /** QLM command */
+
+
+/* Enable MTD partitioning support */
+#define CONFIG_CMD_MTDPARTS
+
+
+/* Enable JFFS2 support */
+#define CONFIG_CMD_JFFS2
+
+
+/** Enable "date" command to set and get date from RTC */
+#define CONFIG_CMD_DATE

```

```

+#define CONFIG_CMD_FLASH /** flinfo, erase, protect */
+#define CONFIG_CMD_EXT2 /** EXT2/3 filesystem support */
+#define CONFIG_CMD_EXT4 /** EXT4 filesystem support */
+#define CONFIG_CMD_FAT /** FAT support */
+#define CONFIG_FAT_WRITE /** FAT write support */
+
+/* SPI NOR flash support */
+#define CONFIG_SF_DEFAULT_BUS 0 /** SPI flash default bus */
+#define CONFIG_SF_DEFAULT_CS 0 /** SPI flash default chip select */
+#define CONFIG_SF_DEFAULT_SPEED 50000000 /** Default speed in MHz */
+
+#define CONFIG_OCTEON_SPI /** Enable OCTEON SPI driver */
+#define CONFIG_SPI_FLASH /** Enable SPI flash driver */
+#define CONFIG_SPI_FLASH_STMICRO /** Enable ST Micro SPI flash */
+#define CONFIG_CMD_SPI /** Enable SPI command */
+#define
CONFIG_CMD_SF /** Enable SPI flash command */
+
+#define CONFIG_OCTEON_SPI_NO_FAILSAFE /** SPI failsafe not yet supported */
+/**
+ * Start of where the SPI bootloader images are placed. Must be on an erase
+ * block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_START 0x10000
+
+/**
+ * End of the SPI boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_END 0x400000
+
+/* Maximum number of NAND devices */
+#define CONFIG_SYS_MAX_NAND_DEVICE 1
+#define CONFIG_SYS_NAND_FORCE_WIDTH 8 /* Force to 8-bit width */
+/* Not used for Octeon, but must be set */
+#define CONFIG_SYS_NAND_BASE 0
+/* Maximum number of NAND devices */
+#define CONFIG_SYS_NAND_MAX_CHIPS CONFIG_SYS_MAX_NAND_DEVICE
+
+/* Enable multi-bit ECC support */
+#define CONFIG_OCTEON_HW_BCH
+#include "config_octeon_nand.h"
+
+
+/*
+ * Miscellaneous configurable options
+ */
+/** Environment variables that

```

```

will be set by default */
+#define CONFIG_EXTRA_ENV_SETTINGS \
+ "burn_app=erase $(flash_unused_addr) +$(filesize);cp.b $(fileaddr) $(flash_unused_addr) $(filesize)\0" \
+ "bf=bootoct $(flash_unused_addr) forceboot numcores=$(numcores)\0" \
+ "linux_mmc=fatload mmc 1 $(loadaddr) vmlinux.64;bootoctlinux $(loadaddr)\0" \
+ "autoload=n\0" \
+ "ls=fatls mmc 0\0" \
+ ""
+
+/*-----
+ * FLASH and environment organization
+ */
+#define CONFIG_SYS_FLASH_SIZE (16*1024*1024) /** Flash size (bytes) */
+#define CONFIG_SYS_MAX_FLASH_BANKS 1 /** max number of memory banks */
+#define CONFIG_SYS_MAX_FLASH_SECT (256) /** max number of sectors on one chip */
+
+/* Width of CFI bus to start scanning */
+#define CONFIG_SYS_FLASH_CFI_WIDTH FLASH_CFI_16BIT
+/* Enable extra elements in CFI driver for storing flash geometry */
+#define CONFIG_SYS_FLASH_CFI 1
+/* Enable CFI flash
driver */
+#define CONFIG_FLASH_CFI_DRIVER 1
+#define CONFIG_SYS_FLASH_USE_BUFFER_WRITE
+
+/* We're not RAM_RESIDENT so CONFIG_ENV_IS_IN_FLASH will be set. */
+#if CONFIG_RAM_RESIDENT
+# define CONFIG_ENV_IS_NOWHERE 1
+#else
+# define CONFIG_ENV_IS_IN_FLASH 1
+#endif
+
+
+#define CONFIG_LIBATA /** Needed for SATA */
+#define CONFIG_CMD_SATA /** Enable the sata command */
+#define CONFIG_SYS_SATA_MAX_DEVICE 2 /** Support up to 2 devices */
+#define CONFIG_SATA_AHCI /** Enable AHCI driver */
+#define CONFIG_SATA_AHCI_PLAT /** Enable AHCI platform support */
+#define CONFIG_SATA_AHCI_OCTEON /** Enable Octeon AHCI driver */
+#define CONFIG_AHCI_SETFEATURES_XFER /** Enable setfeature xfer feature */
+
+/* Address and size of Primary Environment Sector */
+#define CONFIG_ENV_SIZE (128*1024) /** Size of environment */
+#define CONFIG_ENV_ADDR (CONFIG_SYS_FLASH_BASE + CONFIG_SYS_FLASH_SIZE -
CONFIG_ENV_SIZE)
+
+/*-----
+

```

```

* Cache Configuration (not used)
+ */
+#define CONFIG_SYS_DCACHE_SIZE (32 * 1024) /** L1 Data cache size */
+#define CONFIG_SYS_ICACHE_SIZE (78 * 1024) /** L1 icache size */
+
+#if CONFIG_OCTEON_PCI_HOST
+/** Right now only the Intel E1000 driver has been ported */
+# define CONFIG_E1000
+#endif
+
+/** The CN73XX has two TWSI buses and the EBB7304 board uses both of them */
+#define CONFIG_SYS_MAX_I2C_BUS 1
+
+#define CONFIG_RTC_DS1337 /** RTC clock chip */
+#define CONFIG_SYS_I2C_RTC_ADDR 0x68 /** RTC I2C address */
+#define CONFIG_SYS_RTC_BUS_NUM 0 /** RTC I2C bus */
+
+/** Enable watchdog support */
+#define CONFIG_HW_WATCHDOG
+
+/** Configure QLM support */
+#define CONFIG_OCTEON_QLM
+
+/* Include shared board configuration, consisting mainly of DRAM details. */
+#include "octeon_kilburn_shared.h"
+
+/** Disable DDR2 support */
+#define CONFIG_OCTEON_DISABLE_DDR2
+
+#endif /* __CONFIG_H__ */
diff -Nru OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h
OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 1969-12-31 16:00:00.000000000
-0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 2016-08-22 10:53:46.519732222 -
0700
@@ -0,0 +1,598 @@
+#ifndef __OCTEON_KILBURN_SHARED_H__
+#define __OCTEON_KILBURN_SHARED_H__
+
+
+
+/* Define this to use SPD data defined in header files instead of
+ * using TWSI to access the SPDs in the DIMMs. This is a workaround
+ * for boards that lack working TWSI interfaces.
+ */
+
+
+/** 1-DIMM, 4-LMC */

```



```

+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define
OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+
+/* 1-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC2(spd_pointer)
\
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC3(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC1(spd_pointer) \

```



```

MT18ASF1G72AZ_2G1AYESZG_VALUES ID: 101 DDR4 */
+/* MT18ASF1G72AZ_2G1AXESZG_VALUES ID: 102 DDR4 */
+
+/*-----*/
+/* Configuration 0: gpio 1 = OFF */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG0_SPD_VALUES MT18ASF1G72AZ_2G1AXESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+
+ */
+#define OCTEON_KILBURN_CFG0_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+/*-----*/
+/* Configuration 1: gpio 1 = ON */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG1_SPD_VALUES MT18ASF1G72AZ_2G1AYESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+ */
+#define OCTEON_KILBURN_CFG1_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+
+#define MERGE_AGAIN(X, Y) X ## Y
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION0 {{0x50, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION1 {{0x54, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION2 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION3 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+
+#define OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR (0x50)
+
+
+
+/*
+ * Local copy of these parameters to allow for customization for this
+ * board design. The generic version resides in lib_octeon_shared.h.

```

```

+ */
+
+/* LMC0_MODEREG_PARAMS1 */
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = 0, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01
+ = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_HiZ, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \

```

```

+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_HiZ, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11
= 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0 \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm,
\
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0,
\
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \

```

```

+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = ddr4_rttnom_120ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = ddr4_rttnom_120ohm, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_240ohm, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = ddr4_rttnom_120ohm, \
+ .pasr_11 = 0, \
+ .asr_11
= 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = ddr4_rttwr_240ohm, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = ddr4_rttnom_120ohm, \
+ } \
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = rttwr_60ohm, \
+ .dic_00 = dic_34ohm, \
+ .rtt_nom_00 = rttnom_20ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = rttwr_60ohm, \
+ .dic_01
= dic_34ohm, \
+ .rtt_nom_01 = rttnom_none, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = rttwr_60ohm, \
+ .dic_10 = dic_34ohm, \
+ .rtt_nom_10 = rttnom_20ohm, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = rttwr_60ohm, \

```



```

+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01
 = ddr4_rttpark_120ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_60ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_60ohm, \
+ .vref_value_10 = 0x19, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = ddr4_rttpark_60ohm, \
+ .vref_value_11 = 0x19, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_80ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_80ohm, \
+ .vref_value_01
 = 0x1f, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \

```



```

+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT, ddr4_rodt_ctl_48_ohm, 0x00000000ULL,
0 },\
+ /* 2 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT,
ddr4_rodt_ctl_48_ohm, 0x00000000ULL, 0 }
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT, ddr4_rodt_ctl_80_ohm, 0x00000000ULL,
0 },\
+ /* 2 */ { ddr4_dqx_driver_34_ohm, 0x0c0c0303ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT, ddr4_rodt_ctl_48_ohm, 0x04080102ULL,
0 }
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2
RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x01030203ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT, ddr4_rodt_ctl_48_ohm, 0x01010202ULL,
0 }
+
+/* Construct a static initializer for the ddr_configuration_t variable that holds
+** (almost) all of the information required for DDR initialization.
+*/
+
+/*

```

```

+ The parameters below make up the custom_lmc_config data structure.
+ This structure is used to customize the way that the LMC DRAM
+ Controller is configured for a particular board design.
+
+ Refer to the file lib_octeon_board_table_entry.h for a description
+ of the custom board settings. It is usually kept in the following
+ location... arch/mips/include/asm/arch-octeon/
+
+ */
+
+ #define OCTEON_KILBURN_DDR_CONFIGURATION(SPD_CONFIGURATION)
+
+ \
+ /* Interface 0 */
+ {
+ .custom_lmc_config = {
+ .min_rtt_nom_idx = 1,
+ .max_rtt_nom_idx = 7,
+ .min_rodt_ctl = 1,
+ .max_rodt_ctl = 7,
+ .ck_ctl = ddr4_driver_34_ohm,
+ .cmd_ctl = ddr4_driver_34_ohm,
+ .ctl_ctl = ddr4_driver_34_ohm,
+ .min_cas_latency = 0,
+ .offset_en = 1,
+ .offset_udimm = 2,
+ .offset_rdimm =
+ 2,
+ .ddr_rtt_nom_auto = 0,
+ .ddr_rodt_ctl_auto = 0,
+ .rlevel_comp_offset_udimm = 0,
+ .rlevel_comp_offset_rdimm = 0,
+ .rlevel_compute = 0,
+ .ddr2t_udimm = 1,
+ .ddr2t_rdimm = 1,
+ .maximum_adjacent_rlevel_delay_increment = 2,
+ .fprch2 = 2,
+ .dll_write_offset = NULL,
+ .dll_read_offset = NULL,
+ .parity = 0},
+ .dimm_config_table = {
+ SPD_CONFIGURATION##0,
+
+ \
+ DIMM_CONFIG_TERMINATOR},
+ .unbuffered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk = 0,
+ .lmc_delay_cmd = 0,
+ .lmc_delay_dq = 0},

```

```

+ .registered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk = 0,
+ .lmc_delay_cmd = 0,
+ .lmc_delay_dq = 0},
+ .odt_1rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+ \
+ .odt_2rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ },
+ /* Interface 1 */
+ {
+ .custom_lmc_config = {
+ .min_rtt_nom_idx = 1,
+ .max_rtt_nom_idx = 7,
+ .min_rodt_ctl = 1,
+ .max_rodt_ctl = 7,
+ .ck_ctl = ddr4_driver_34_ohm,
+ .cmd_ctl = ddr4_driver_34_ohm,
+ \
+ .ctl_ctl = ddr4_driver_34_ohm,
+ .min_cas_latency = 0,
+ .offset_en = 1,
+ .offset_udimm = 2,
+ .offset_rdimm = 2,
+ .ddr_rtt_nom_auto = 0,
+ .ddr_rodt_ctl_auto = 0,
+ .rlevel_comp_offset_udimm = 0,
+ .rlevel_comp_offset_rdimm = 0,
+ .rlevel_compute = 0,
+ .ddr2t_udimm = 1,
+ .ddr2t_rdimm = 1,
+ .maximum_adjacent_rlevel_delay_increment = 2,
+ .fprch2 = 2,
+ \
+ .dll_write_offset = NULL,
+ .dll_read_offset = NULL,
+ .parity = 0},
+ .dimmm_config_table = {
+ SPD_CONFIGURATION##1,
+ DIMM_CONFIG_TERMINATOR},
+ .unbuffered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk = 0,
+ .lmc_delay_cmd = 0,

```

```

+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk
= 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }, \
+ /* Interface 2 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##2, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0,

```

```

\
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ },
\
+ /* Interface 3 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = \
2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##3, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \

```

```

+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }
+
+ #endif /* __OCTEON_KILBURN_SHARED_H__ */
diff -Nru OCTEON-SDK-old/executive/cvmx-app-init.h OCTEON-SDK/executive/cvmx-app-init.h
--- OCTEON-SDK-old/executive/cvmx-app-init.h 2016-08-22 11:04:03.746732100 -0700
+++ OCTEON-SDK/executive/cvmx-app-init.h 2016-08-22 10:54:58.536678796 -0700
@@ -363,6 +363,7 @@
 ** use any numbers in this range. */
 CVMX_BOARD_TYPE_CUST_PRIVATE_MIN = 20001,
 CVMX_BOARD_TYPE_UBNT_E100 = 20002,
+ CVMX_BOARD_TYPE_CUST_KILBURN = 20003,
 CVMX_BOARD_TYPE_CUST_PRIVATE_MAX = 30000,

 /* Range for IO modules */
@@ -505,6 +506,7 @@
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MIN)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_UBNT_E100)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_CLARK)
+ ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_KILBURN)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MAX)
 /* Module range */
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_MODULE_MIN)

```

Found in path(s):

\* /opt/cola/permits/1588488807\_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/patch6.patch

No license file was found, but licenses were detected in source scan.

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CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c | 4 +++-

1 file changed, 3 insertions(+), 1 deletion(-)

```
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
```

```
index 7fb8234..7233926 100644
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
@@ -486,11 +486,13 @@ cvmcs_ioq_send_to_pko(cvmx_wqe_t *wqe, int dir, int port, int queue,
 * success, pko will free up the buffer.
 */
```

```
int
```

```
cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
```

```
{
+#if 0 //for LINA: as WQE and data share the same buffer.
cvmx_buf_ptr_t *lptr;
+#endif
cvmx_resp_hdr_t *resp_ptr;
cvmx_buf_ptr_pki_t *pki_lptr;
int port, queue;
```

```
#if 0 //for LINA: as WQE and data share the same buffer.
```

```
@@ -653,11 +655,11 @@ cavium_parse_cvs_string(const char *cvs_name, char *ver_string, int len)
*/
```

```
void cvmcs_ioq_config_interrupts()
```

```
{
if (OCTEON_IS_MODEL(OCTEON_CN78XX)) {
/* Workaround to disable CVMX_DPI_INT_REG[21] */
- cvmx_error_intsn_disable_v3(0xdf015);
+ cvmx_error_intsn_disable_v3(0, 0xdf015);
}
return;
}
```

```
/*
```

Found in path(s):

```
* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-CNNIC-SDK-SRC-modules-driver-src-firmware-apps-ioq-cvmcs-ioq-main.c.patch
```

No license file was found, but licenses were detected in source scan.

```
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```

```
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```

```
OCTEON-SDK/host/remote-utils/oct-remote-boot.c | 2 +-
1 files changed, 1 insertions(+), 1 deletions(-)
```

```
diff --git a/OCTEON-SDK/host/remote-utils/oct-remote-boot.c b/OCTEON-SDK/host/remote-utils/oct-remote-boot.c
```

```
index dfb3006..948ba90 100644
```

```

--- a/OCTEON-SDK/host/remote-utils/oct-remote-boot.c
+++ b/OCTEON-SDK/host/remote-utils/oct-remote-boot.c
@@ -653,11 +653,11 @@ int main(int argc, const char *argv[])

 if (octeon_remote_open(OCTEON_REMOTE_DEFAULT, 0))
 return -1;

 if (OCTEON_IS_OCTEON3() && !OCTEON_IS_MODEL(OCTEON_CN70XX)) {
- load_cache_flag = 1;
+ load_cache_flag = 0;
 printf("CN7XXX: Loading from cache automatically\n");
 }
 if (load_cache_flag)
 {
 if (OCTEON_IS_MODEL(OCTEON_CN3XXX) || OCTEON_IS_MODEL(OCTEON_CN5XXX))

```

Found in path(s):

```

* /opt/cola/permits/1588488807_1691068460.235213/0/octeon-sdk-tar-gz/octeon-sdk/patches/oct-remote-boot-
c.patch

```

## 1.303 yajl 2.1.0

### 1.303.1 Available under license :

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## 1.304 mpclib 1.1.0

### 1.304.1 Available under license :

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Version 2.1, February 1999

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